

LM120/LM320-N Series 3-Terminal Negative Regulators

Check for Samples: LM120, LM320-N

FEATURES

- Preset Output Voltage Error Less than ±3%
- Preset Current Limit
- Internal Thermal Shutdown
- Operates with Input-Output Voltage Differential down to 1V
- Excellent Ripple Rejection
- Low Temperature Drift
- Easily Adjustable to Higher Output Voltage

DESCRIPTION

The LM120 series are three-terminal negative regulators with a fixed output voltage of -5V, -12V, and -15V, and up to 1.5A load current capability. Where other voltages are required, the LM137 and LM137HV series provide an output voltage range of -1.2V to -47V.

The LM120 need only one external component—a compensation capacitor at the output, making them easy to apply. Worst case specifications on output voltage deviation due to any combination of line, load or temperature variation assure satisfactory system operation.

Typical Applications

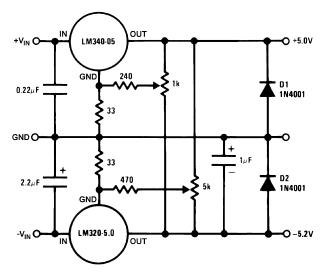


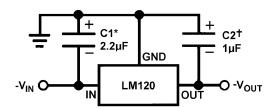
Figure 1. Dual Trimmed Supply

Exceptional effort has been made to make the LM120 Series immune to overload conditions. The regulators have current limiting which is independent of temperature, combined with thermal overload protection. Internal current limiting protects against momentary faults while thermal shutdown prevents junction temperatures from exceeding safe limits during prolonged overloads.

Although primarily intended for fixed output voltage applications, the LM120 Series may be programmed for higher output voltages with a simple resistive divider. The low quiescent drain current of the devices allows this technique to be used with good regulation.

Table 1. LM120 Series Packages and Power Capability

Device	Package	Rated Power Dissipation	Design Load Current
LM120/LM320- N	TO-3 (NDS)	20W	1.5A
	TO (NDT)	2W	0.5A
LM320-N	TO-220 (NDE)	15W	1.5A



*Required if regulator is separated from filter capacitor by more than 3 inches. For value given, capacitor must be solid tantalum. 25 µF aluminum electrolytic may be substituted.

†Required for stability. For value given, capacitor must be solid tantalum. 25 µF aluminum electrolytic may be substituted. Values given may be increased without limit

For output capacitance in excess of 100 μ F, a high current diode from input to output (1N4001, etc.) will protect the regulator from momentary input shorts.

Figure 2. Fixed Regulator

Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

All trademarks are the property of their respective owners.





These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

ABSOLUTE MAXIMUM RATINGS-5 VOLT REGULATORS (1)(2)(3)

Power Dissipation	Internally Limited
Input Voltage	-25V
Input-Output Voltage Differential	25V
Junction Temperatures	(4)
Storage Temperature Range	−65°C to +150°C
Lead Temperature	
(Soldering, 10 sec.)	300°C
Plastic	260°C

- (1) Refer to RETS120-5H drawing for LM120H-5.0 or RETS120-5K drawing for LM120-5K military specifications.
- (2) For -5V 3 amp regulators, see LM145 data sheet.
- (3) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.
- (4) This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.

LM120K-5.0 AND LM320K-5.0 ELECTRICAL CHARACTERISTICS(1)

		Metal Can Package						
	Order Numbers	LI	LM120K-5.0 L (TO-3)			M320K-((TO-3)	Units	
	esign Output Current (I _D) Device Dissipation (P _D)				5A)W			Units
Parameter	Conditions (2)	Min	Тур	Max	Min	Тур	Max	
Output Voltage	$T_J = 25^{\circ}C, V_{IN} = 10V,$	− 5.1	-5	-4.9	-5.2	- 5	-4.8	V
	$I_{LOAD} = 5 \text{ mA}$							
Line Regulation	$T_J = 25$ °C, $I_{LOAD} = 5$ mA,		10	25		10	40	mV
	$V_{MIN} \le V_{IN} \le V_{MAX}$							
Input Voltage		-25		-7	-25		-7	V
Ripple Rejection	f = 120 Hz	54	64		54	64		dB
Load Regulation,	$T_J = 25^{\circ}C, V_{IN} = 10V,$		50	75		60	100	mV
(3)	$5 \text{ mA} \le I_{LOAD} \le I_D$							
Output Voltage,	$-7.5V \le V_{IN} \le V_{MAX}$	-5.20		-4.80	-5.25		-4.75	V
(2)	$5 \text{ mA} \le I_{LOAD} \le I_D, P \le P_D$							
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		1	2		1	2	mA
Quiescent Current	$T_J = 25^{\circ}C$							
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.1	0.4		0.1	0.4	mA
	$5 \text{ mA} \le I_{LOAD} \le I_{D}$		0.1	0.4		0.1	0.4	mA
Output Noise Voltage	$T_A = 25$ °C, $C_L = 1 \mu F$, $I_L = 5 mA$,		150			150		μV
	$V_{IN} = 10V$, 10 Hz $\leq f \leq 100 \text{ kHz}$							
Long Term Stability			5	50		5	50	mV
Thermal Resistance								
Junction to Case				3			3	°C/W
Junction to Ambient				35			35	°C/W

- (1) For -5V 3 amp regulators, see LM145 data sheet.
- (2) This specification applies over $-55^{\circ}\text{C} \le \text{T}_{\text{J}} \le +150^{\circ}\text{C}$ for the LM120 and $0^{\circ}\text{C} \le \text{T}_{\text{J}} \le +125^{\circ}\text{C}$ for the LM320-N.
- (3) Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.

Submit Documentation Feedback

Copyright © 1998–2013, Texas Instruments Incorporated



LM120H-5.0 ELECTRICAL CHARACTERISTICS(1)

			Metal Can Package				
Order Numbers			LM120H-5.0 (TO)				
	Design Output Current (I _D) Device Dissipation (P _D)			Units			
Parameter	Conditions (2)	Min	Тур	Max			
Output Voltage	$T_J = 25^{\circ}C, V_{IN} = 10V,$	− 5.1	-5	-4.9	V		
	I _{LOAD} = 5 mA						
Line Regulation	$T_J = 25^{\circ}C$, $I_{LOAD} = 5$ mA,		10	25	mV		
	$V_{MIN} \le V_{IN} \le V_{MAX}$						
Input Voltage		-25		-7	V		
Ripple Rejection	f = 120 Hz	54	64		dB		
Load Regulation,	$T_J = 25^{\circ}C, V_{IN} = 10V,$		30	50	mV		
(3)	$5 \text{ mA} \leq I_{LOAD} \leq I_{D}$						
Output Voltage,	$-7.5V \le V_{IN} \le V_{MAX}$	-5.20		-4.80	V		
(4)	$5 \text{ mA} \le I_{LOAD} \le I_D, P \le P_D$						
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		1	2	mA		
Quiescent Current	T _J = 25°C						
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.05	0.4	mA		
	$5 \text{ mA} \leq I_{LOAD} \leq I_{D}$		0.04	0.4	mA		
Output Noise Voltage	$T_A = 25^{\circ}C, C_L = 1 \mu F, I_L = 5 \text{ mA},$		150		μV		
	$V_{IN} = 10V$, 10 Hz $\leq f \leq 100 \text{ kHz}$						
Long Term Stability			5		mV		
Thermal Resistance							
Junction to Case				(5)	°C/W		
Junction to Ambient				(5)	°C/W		

- For -5V 3 amp regulators, see LM145 data sheet.
- This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.
- Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.
- This specification applies over −55°C ≤ T₁ ≤ +150°C for the LM120 and 0°C ≤ T₁ ≤ +125°C for the LM320-N.
- Thermal resistance of typically 85°C/W (in 400 linear feet air flow), 224°C/W (in static air) junction to ambient, of typically 21°C/W junction to case.

ABSOLUTE MAXIMUM RATINGS-12 VOLT REGULATORS (1)(2)

Power Dissipation	Internally Limited
Input Voltage	-35V
Input-Output Voltage Differential	30V
Junction Temperatures	(3)
Storage Temperature Range	−65°C to +150°C
Lead Temperature	
(Soldering, 10 sec.)	300°C

- (1) Refer to RETS120H-12 drawing for LM120H-12 or RETS120-12K drawing for LM120K-12 military specifications.
- If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.

Product Folder Links: LM120 LM320-N

This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.



LM120K-12 ELECTRICAL CHARACTERISTICS

		Meta						
	Order Numbers			LM120K-12 (TO-3)				
	Design Output Current (I _D)							
	Device Dissipation (P _D)		20W					
Parameter	Conditions (1)	Min	Тур	Max				
Output Voltage	$T_J = 25^{\circ}C, V_{IN} = 17V,$	-12.3	-12	-11.7	V			
	$I_{LOAD} = 5 \text{ mA}$							
Line Regulation	$T_J = 25$ °C, $I_{LOAD} = 5$ mA,		4	10	mV			
	$V_{MIN} \le V_{IN} \le V_{MAX}$							
Input Voltage		-32		-14	V			
Ripple Rejection	f = 120 Hz	56	80		dB			
Load Regulation,	$T_J = 25^{\circ}C, V_{IN} = 17V,$		30	80	mV			
(2)	$5 \text{ mA} \le I_{LOAD} \le I_{D}$							
Output Voltage,	$14.5V \le V_{IN} \le V_{MAX},$	-12.5		-11.5	V			
(3)	$5 \text{ mA} \le I_{LOAD} \le I_D, P \le P_D$							
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		2	4	mA			
Quiescent Current	$T_J = 25^{\circ}C$							
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.1	0.4	mA			
	$5 \text{ mA} \leq I_{LOAD} \leq I_{D}$		0.1	0.4	mA			
Output Noise Voltage	$T_A = 25^{\circ}C, C_L = 1 \mu F, I_L = 5 mA,$		400		μV			
	$V_{IN} = 17V$, 10 Hz $\leq f \leq 100 \text{ kHz}$							
Long Term Stability			12	120	mV			
Thermal Resistance								
Junction to Case				3	°C/W			
Junction to Ambient				35	°C/W			

⁽¹⁾ This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.

LM120H-12 ELECTRICAL CHARACTERISTICS

		Metal Can Package		ackage		
Order Numbers						
	Design Output Current (I _D)		Units			
Device Dissipation (P _D)		2W		(P _D) 2W		
Parameter	Conditions ⁽¹⁾	Min	Тур	Max		
Output Voltage	T _J = 25°C, V _{IN} = 17V,	-12.3	-12	-11.7	V	
	$I_{LOAD} = 5 \text{ mA}$					
Line Regulation	$T_J = 25$ °C, $I_{LOAD} = 5$ mA,		4	10	mV	
	$V_{MIN} \le V_{IN} \le V_{MAX}$					
Input Voltage		-32		-14	V	
Ripple Rejection	f = 120 Hz	56	80		dB	
Load Regulation,	T _J = 25°C, V _{IN} = 17V,		10	25	mV	

This specification applies over -55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.

⁽²⁾ Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.

⁽³⁾ This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.



LM120H-12 ELECTRICAL CHARACTERISTICS (continued)

		M	Metal Can Package			
Order Numbers Design Output Current (I_D) Device Dissipation (P_D)						
			0.2A		Units	
			2W			
Parameter	Conditions ⁽¹⁾	Min	Max			
(2)	5 mA ≤ I _{LOAD} ≤ I _D					
Output Voltage,	$14.5V \le V_{IN} \le V_{MAX}$	-12.5		-11.5	V	
(1)	$5 \text{ mA} \le I_{LOAD} \le I_D, P \le P_D$					
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		2	4	mA	
Quiescent Current	$T_J = 25$ °C					
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.05	0.4	mA	
	5 mA ≤ I _{LOAD} ≤ I _D		0.03	0.4	mA	
Output Noise Voltage	$T_A = 25^{\circ}C, C_L = 1 \mu F, I_L = 5 mA,$		400		μV	
	$V_{IN} = 17V$, 10 Hz $\leq f \leq 100$ kHz					
Long Term Stability			12	120	mV	
Thermal Resistance						
Junction to Case				(3)	°C/W	
Junction to Ambient				(3)	°C/W	

⁽²⁾ Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.

LM320T-12 ELECTRICAL CHARACTERISTICS

		Power Plastic Package						
Order Numbers								
	Design Output Current (I _D)		Units					
	Device Dissipation (P _D)		15W					
Parameter	Conditions (1) Min Typ		meter Conditions (1) Min Typ Max		Min Typ Max			
Output Voltage	$T_J = 25^{\circ}C, V_{IN} = 17V,$	-12.4	-12	-11.6	V			
	I _{LOAD} = 5 mA							
Line Regulation	$T_J = 25^{\circ}C$, $I_{LOAD} = 5$ mA,		4	20	mV			
	$V_{MIN} \le V_{IN} \le V_{MAX}$							
Input Voltage		-32		-14.5	V			
Ripple Rejection	f = 120 Hz	56	80		dB			
Load Regulation,	$T_J = 25^{\circ}C, V_{IN} = 17V,$		30	80	mV			
(2)	$5 \text{ mA} \leq I_{LOAD} \leq I_{D}$							
Output Voltage,	$14.5V \le V_{IN} \le V_{MAX},$	-12.6		-11.4	V			
(1)	$5 \text{ mA} \le I_{LOAD} \le I_D, P \le P_D$							
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		2	4	mA			
Quiescent Current	T _J = 25°C							
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.1	0.4	mA			
	$5 \text{ mA} \le I_{LOAD} \le I_{D}$		0.1	0.4	mA			

⁽¹⁾ This specification applies over $-55^{\circ}\text{C} \le T_{J} \le +150^{\circ}\text{C}$ for the LM120 and $0^{\circ}\text{C} \le T_{J} \le +125^{\circ}\text{C}$ for the LM320-N.

⁽³⁾ Thermal resistance of typically 85°C/W (in 400 linear feet/min air flow), 224°C/W (in static air) junction to ambient, of typically 21°C/W junction to case.

⁽²⁾ Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.



LM320T-12 ELECTRICAL CHARACTERISTICS (continued)

Order Numbers Design Output Current (I _D)		Powe			
		1A			Units
	Device Dissipation (P _D) 15W		15W		
Parameter	Conditions ⁽¹⁾	Min	Тур	Max	
Output Noise Voltage	$T_A = 25$ °C, $C_L = 1 \mu F$, $I_L = 5 \text{ mA}$,		400		μV
	V _{IN} = 17V, 10 Hz ≤ f ≤ 100 kHz				
Long Term Stability			24		mV
Thermal Resistance					
Junction to Case			4		°C/W
Junction to Ambient			50		°C/W

ABSOLUTE MAXIMUM RATINGS-15 VOLT REGULATORS(1)(2)

Power Dissipation	Internally Limited
Input Voltage	
LM120/LM320-N	-40V
LM320T	-35V
Input-Output Voltage Differential	30V
Junction Temperatures	(3)
Storage Temperature Range	−65°C to +150°C
Lead Temperature	
(Soldering, 10 sec.)	300°C

- (1) Refer to RETS120-15H drawing for LM120H-15 or RETS120-15K drawing for LM120K-15 military specifications.
- (2) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.
- (3) This specification applies over $-55^{\circ}\text{C} \le \text{T}_{\text{J}} \le +150^{\circ}\text{C}$ for the LM120 and $0^{\circ}\text{C} \le \text{T}_{\text{J}} \le +125^{\circ}\text{C}$ for the LM320-N.

LM120K-15 AND LM320K-15 ELECTRICAL CHARACTERISTICS

			Metal Can Package					
Order Numbers Design Output Current (I _D)		L	LM120K-15 (TO-3)		LM320K-15 (TO-3)		-	
					1A			Units
D	evice Dissipation (P _D)	20W						
Parameter	Conditions ⁽¹⁾	Min Typ Max Min Typ M			Max			
Output Voltage	T _J = 25°C, V _{IN} = 20V,	-15.3	-15	-14.7	-15.4	-15	-14.6	V
	I _{LOAD} = 5 mA							
Line Regulation	$T_J = 25$ °C, $I_{LOAD} = 5$ mA,		5	10		5	20	mV
	$V_{MIN} \le V_{IN} \le V_{MAX}$							
Input Voltage		-35		-17	-35		-17	V
Ripple Rejection	f = 120 Hz	56	80		56	80		dB
Load Regulation,	T _J = 25°C, V _{IN} = 20V,		30	80		30	80	mV
(2)	5 mA ≤ I _{LOAD} ≤ I _D							
Output Voltage,	$17.5V \le V_{IN} \le V_{MAX}$	-15.5		-14.5	-15.6		-14.4	V
(1)	$5 \text{ mA} \le I_{LOAD} \le I_D, P \le P_D$							
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		2	4		2	4	mA

⁽¹⁾ This specification applies over $-55^{\circ}\text{C} \le \text{T}_{\text{J}} \le +150^{\circ}\text{C}$ for the LM120 and $0^{\circ}\text{C} \le \text{T}_{\text{J}} \le +125^{\circ}\text{C}$ for the LM320-N.

Submit Documentation Feedback

⁽²⁾ Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.



LM120K-15 AND LM320K-15 ELECTRICAL CHARACTERISTICS (continued)

		Metal Can Package						
	LM120K-15 L (TO-3)				LM320K-15 (TO-3)		-	
Des		1A						
De	evice Dissipation (P _D)		20W					
Parameter	Conditions ⁽¹⁾	Min	Тур Мах		Min	Тур	Max	
Quiescent Current	T _J = 25°C							
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.1	0.4		0.1	0.4	mA
	$5 \text{ mA} \le I_{LOAD} \le I_{D}$		0.1	0.4		0.1	0.4	mA
Output Noise Voltage	$T_A = 25^{\circ}C, C_L = 1 \mu F, I_L = 5 mA,$		400			400		μV
	$V_{IN} = 20V$, 10 Hz $\leq f \leq 100$ kHz							
Long Term Stability			15	150		15	150	mV
Thermal Resistance								
Junction to Case				3			3	°C/W
Junction to Ambient				35			35	°C/W

LM120H-15 ELECTRICAL CHARACTERISTICS

		М	Metal Can Package				
	Order Numbers		Units				
	Design Output Current (I _D)		0.2A				
	Device Dissipation (P _D)						
Parameter	Conditions (1)	Min	Тур	Max			
Output Voltage	$T_J = 25^{\circ}C, V_{IN} = 20V,$	-15.3	-15	-14.7	V		
	$I_{LOAD} = 5 \text{ mA}$						
Line Regulation	$T_J = 25^{\circ}C$, $I_{LOAD} = 5$ mA,		5	10	mV		
	$V_{MIN} \le V_{IN} \le V_{MAX}$						
Input Voltage		-35		-17	V		
Ripple Rejection	f = 120 Hz	56	80		dB		
Load Regulation,	$T_J = 25^{\circ}C, V_{IN} = 20V,$		10	25	mV		
(2)	$5 \text{ mA} \leq I_{LOAD} \leq I_{D}$						
Output Voltage,	$17.5V \le V_{IN} \le V_{MAX}$	-15.5		-14.5	V		
(1)	$5 \text{ mA} \le I_{LOAD} \le I_D, P \le P_D$						
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		2	4	mA		
Quiescent Current	$T_J = 25^{\circ}C$						
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.05	0.4	mA		
	$5 \text{ mA} \le I_{LOAD} \le I_{D}$		0.03	0.4	mA		
Output Noise Voltage	$T_A = 25^{\circ}C$, $C_L = 1 \mu F$, $I_L = 5 mA$,		400		μV		
	$V_{IN} = 20V$, 10 Hz $\leq f \leq 100 \text{ kHz}$						
Long Term Stability			15	150	mV		
Thermal Resistance							
Junction to Case				(3)	°C/W		
Junction to Ambient				(3)	°C/W		

⁽¹⁾ This specification applies over -55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.

⁽²⁾ Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.

specifications apply only up to P_D.

(3) Thermal resistance of typically 85°C/W (in 400 linear feet/min air flow), 224°C/W (in static air) junction to ambient, of typically 21°C/W junction to case.



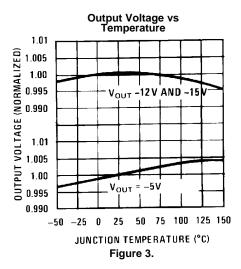
LM320T-15 ELECTRICAL CHARACTERISTICS

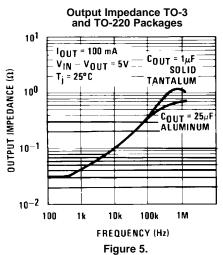
		Powe					
	Order Numbers						
	Design Output Current (I _D)		(TO-220) 1A				
	Device Dissipation (P _D)						
Parameter	Conditions (1)	Min	Тур Мах				
Output Voltage	$T_J = 25^{\circ}C, V_{IN} = 20V,$	-15.5	-15	-14.5	V		
	$I_{LOAD} = 5 \text{ mA}$						
Line Regulation	$T_J = 25$ °C, $I_{LOAD} = 5$ mA,		5	20	mV		
	$V_{MIN} \le V_{IN} \le V_{MAX}$						
Input Voltage		-35		-17.5	V		
Ripple Rejection	f = 120 Hz	56	80		dB		
Load Regulation,	$T_J = 25^{\circ}C, V_{IN} = 20V,$		30	80	mV		
(2)	$5 \text{ mA} \le I_{LOAD} \le I_{D}$						
Output Voltage,	$17.5V \le V_{IN} \le V_{MAX}$	-15.7		-14.3	V		
(1)	$5 \text{ mA} \le I_{LOAD} \le I_D, P \le P_D$						
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		2	4	mA		
Quiescent Current	T _J = 25°C						
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.1	0.4	mA		
	$5 \text{ mA} \le I_{LOAD} \le I_{D}$		0.1	0.4	mA		
Output Noise Voltage	$T_A = 25$ °C, $C_L = 1 \mu F$, $I_L = 5 \text{ mA}$,		400		μV		
	$V_{IN} = 20V$, 10 Hz $\leq f \leq 100 \text{ kHz}$						
Long Term Stability			30		mV		
Thermal Resistance							
Junction to Case			4		°C/W		
Junction to Ambient			50		°C/W		

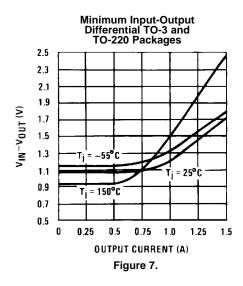
This specification applies over $-55^{\circ}\text{C} \leq \text{T}_{\text{J}} \leq +150^{\circ}\text{C}$ for the LM120 and $0^{\circ}\text{C} \leq \text{T}_{\text{J}} \leq +125^{\circ}\text{C}$ for the LM320-N. Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.

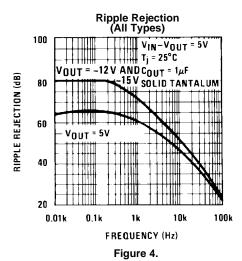


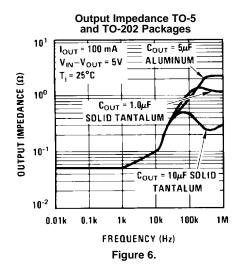
TYPICAL PERFORMANCE CHARACTERISTICS

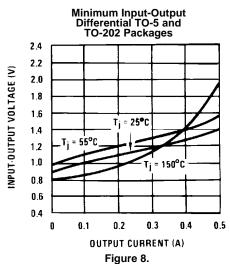






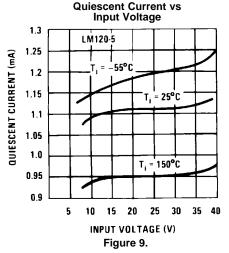


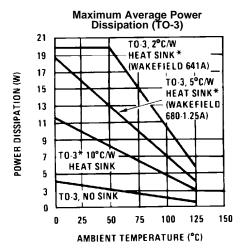




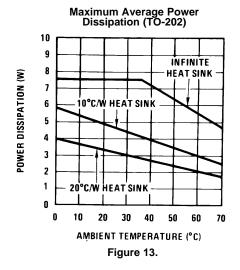


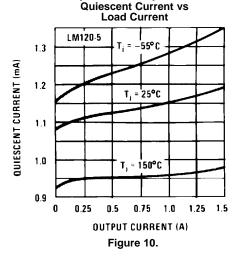
TYPICAL PERFORMANCE CHARACTERISTICS (continued)





*These curves for LM120. Derate 25°C further for LM320-N. Figure 11.





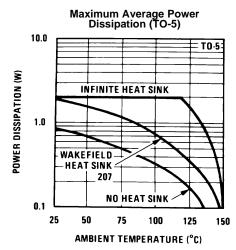
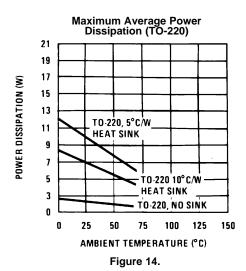
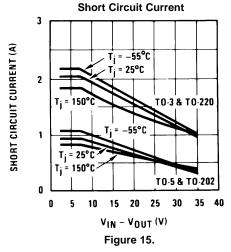


Figure 12.



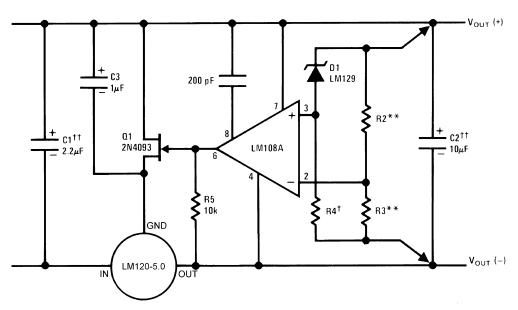


TYPICAL PERFORMANCE CHARACTERISTICS (continued) Short Circuit Current





TYPICAL APPLICATIONS



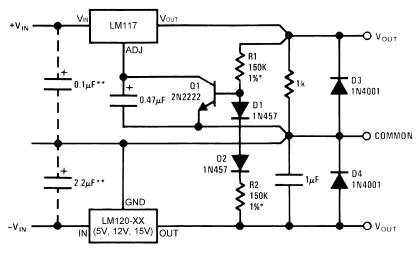
Lead and line regulation — 0.01% temperature stability — 0.2%

†Determines Zener current.

††Solid tantalum.

An LM120-12 or LM120-15 may be used to permit higher input voltages, but the regulated output voltage must be at least -15V when using the LM120-12 and -18V for the LM120-15.

Figure 16. High Stability 1 Amp Regulator



^{*}Resistor tolerance of R1 and R2 determine matching of (+) and (-) inputs.

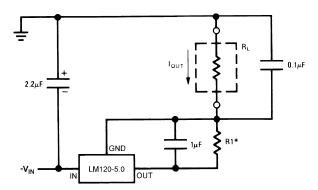
An LM3086N array may substitute for Q1, D1 and D2 for better stability and tracking. In the array diode transistors Q5 and Q4 (in parallel) make up D2; similarly, Q1 and Q2 become D1 and Q3 replaces the 2N2222.

Figure 17. Wide Range Tracking Regulator

^{**}Select resistors to set output voltage. 2 ppm/°C tracking suggested.

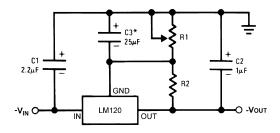
^{**}Necessary only if raw supply capacitors are more than 3" from regulators





* $I_{OUT} = 1 \text{ mA} + \frac{5.0 \text{V}}{\text{R1}}$

Figure 18. Current Source



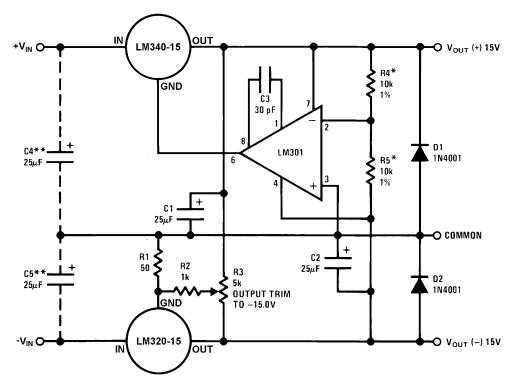
SELECT R2 AS FOLLOWS:

 $\begin{array}{lll} \text{LM120-5} & 300\Omega \\ \text{LM120-12} & 750\Omega \\ \text{LM120-15} & 1k \\ v_{\text{OUT}} = v_{\text{SET}} \frac{\text{R1+R2}}{\text{R2}} \\ ^*\text{C3 option} & & \end{array}$

Figure 19. Variable Output Current Source

^{*}C3 optional. Improves transient response and ripple rejection.





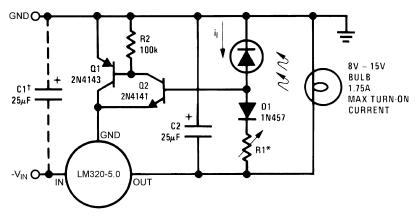
See Performance (Typical)

Figure 20. ±15V, 1 Amp Tracking Regulators

Performance (Typical)

Load Regulation at $\Delta I_L = 1A$	10 mV	1 mV
Output Ripple, C_{IN} = 3000 μF , I_L = 1A	100 μVrms	100 μVrms
Temperature Stability	+50 mV	+50 mV
Output Noise 10 Hz ≤ f ≤ 10 kHz	150 μVrms	150 μVrms

Light Controllers Using Silicon Photo Cells



^{*}Lamp brightness increases until $i_l = 5V/R1$ (i_l can be set as low as 1 μ A).

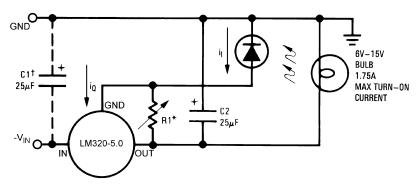
Submit Documentation Feedback

^{*}Resistor tolerance of R4 and R5 determine matching of (+) and (-) outputs.

^{**}Necessary only if raw supply filter capacitors are more than 2 inches from regulators.

[†]Necessary only if raw supply filter capacitor is more than 2 inches from LM320MP.





^{*}Lamp brightness increases until $i_l = i_Q (1 \text{ mA}) + 5 \text{V/R1}$.

†Necessary only if raw supply filter capacitor is more than 2 inches from LM320-N.

Connection Diagram

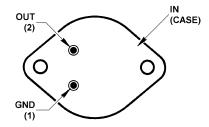


Figure 21. Steel Metal Can Package TO-3 (NDS) (Bottom View)

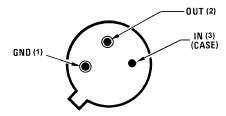


Figure 22. Metal Can Package TO (NDT) (Bottom View)

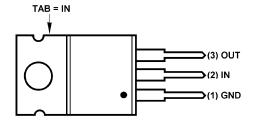


Figure 23. Power Package TO-220 (NDE) (Front View)



Schematic Diagrams

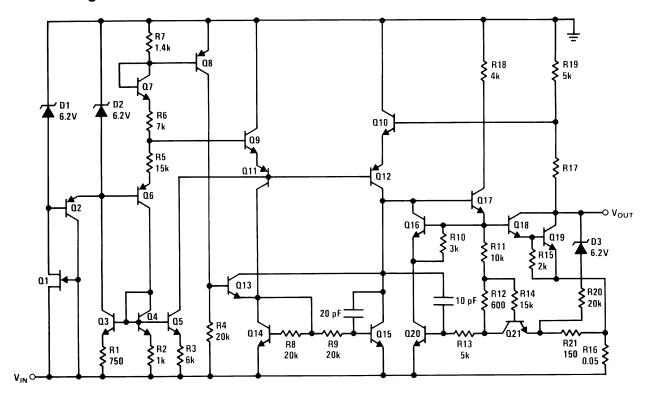


Figure 24. -5V

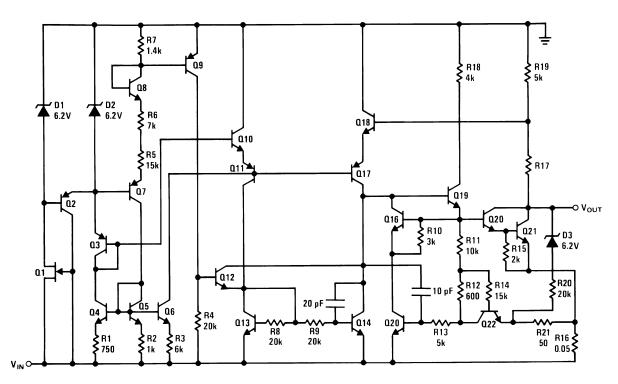


Figure 25. -12V and -15V





REVISION HISTORY

Cł	Changes from Revision B (April 2013) to Revision C							
•	Changed layout of National Data Sheet to TI format		16					





1-Nov-2013

PACKAGING INFORMATION

Orderable Device	Status	Package Type		Pins	_		Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
LM120H-12	ACTIVE	ТО	NDT	3	500	Green (RoHS & no Sb/Br)	AU	Level-1-NA-UNLIM	-55 to 150	LM120H-12P+	Samples
LM120H-12/NOPB	ACTIVE	ТО	NDT	3	500	Green (RoHS & no Sb/Br)	AU	Level-1-NA-UNLIM	-55 to 150	LM120H-12P+	Samples
LM120H-15	ACTIVE	ТО	NDT	3	500	Green (RoHS & no Sb/Br)	AU	Level-1-NA-UNLIM	-55 to 150	LM120H-15P+	Samples
LM120H-15/NOPB	ACTIVE	ТО	NDT	3	500	Green (RoHS & no Sb/Br)	AU	Level-1-NA-UNLIM	-55 to 150	LM120H-15P+	Samples
LM120H-5.0	ACTIVE	ТО	NDT	3	500	Green (RoHS & no Sb/Br)	AU	Level-1-NA-UNLIM	-55 to 150	LM120H-5.0P+	Samples
LM120H-5.0/NOPB	ACTIVE	ТО	NDT	3	500	Green (RoHS & no Sb/Br)	AU	Level-1-NA-UNLIM	-55 to 150	LM120H-5.0P+	Samples
LM320T-15	NRND	TO-220	NDE	3	45	TBD	Call TI	Call TI	0 to 125	LM320T -15 P+	
LM320T-15/NOPB	ACTIVE	TO-220	NDE	3	45	Green (RoHS & no Sb/Br)	CU SN	Level-1-NA-UNLIM	0 to 125	LM320T -15 P+	Samples

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes. **Pb-Free** (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

⁽³⁾ MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.



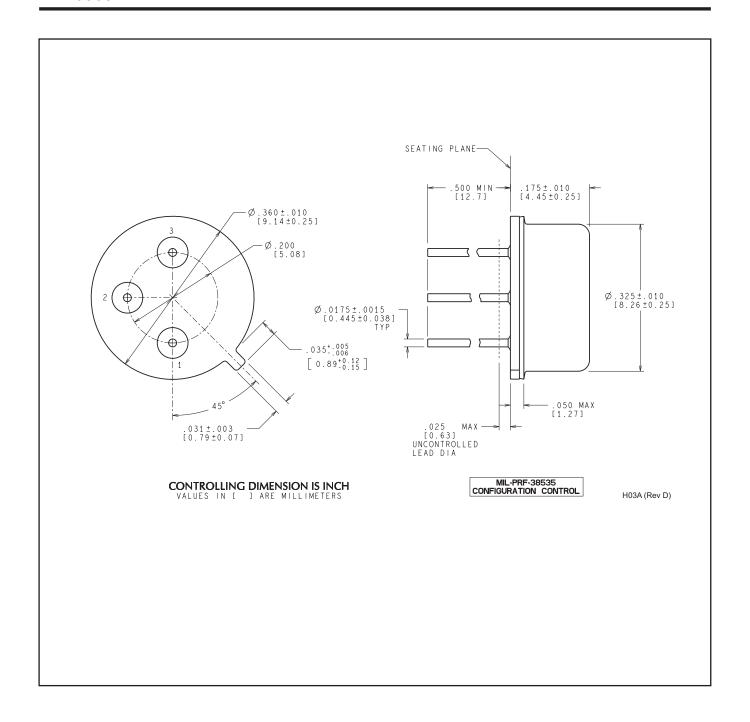
PACKAGE OPTION ADDENDUM

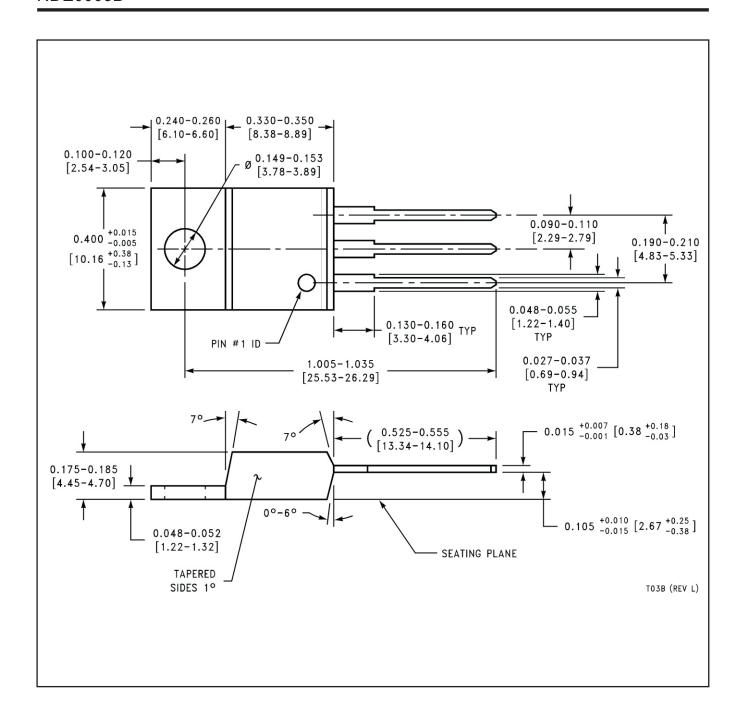
1-Nov-2013

- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead/Ball Finish Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.





IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of significant portions of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI components or services with statements different from or beyond the parameters stated by TI for that component or service voids all express and any implied warranties for the associated TI component or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI's goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Only those TI components which TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have *not* been so designated is solely at the Buyer's risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

Products Applications

Audio www.ti.com/audio Automotive and Transportation www.ti.com/automotive Communications and Telecom **Amplifiers** amplifier.ti.com www.ti.com/communications **Data Converters** dataconverter.ti.com Computers and Peripherals www.ti.com/computers **DLP® Products** www.dlp.com Consumer Electronics www.ti.com/consumer-apps

DSP **Energy and Lighting** dsp.ti.com www.ti.com/energy Clocks and Timers www.ti.com/clocks Industrial www.ti.com/industrial Interface interface.ti.com Medical www.ti.com/medical logic.ti.com Logic Security www.ti.com/security

Power Mgmt power.ti.com Space, Avionics and Defense www.ti.com/space-avionics-defense

Microcontrollers <u>microcontroller.ti.com</u> Video and Imaging <u>www.ti.com/video</u>

RFID www.ti-rfid.com

OMAP Applications Processors www.ti.com/omap TI E2E Community e2e.ti.com

Wireless Connectivity <u>www.ti.com/wirelessconnectivity</u>