

CMOS 8-Bit Microcontroller

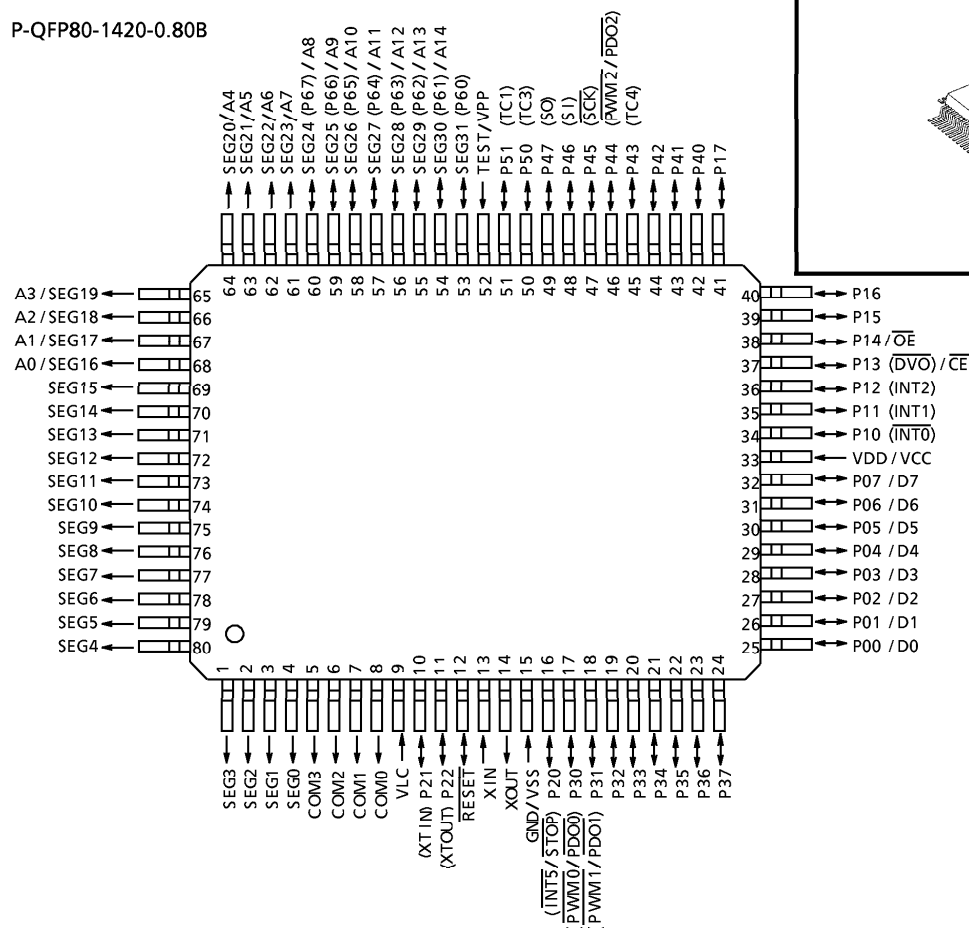
TMP87PH20F, TMP87PM20F

The 87PH20 is an One-Time PROM microcontroller with low-power 128K bits (16 Kbytes) electrically programmable read only memory, the 87PM20 is an One-Time PROM microcontroller with low-power 256K bits (32 Kbytes) electrically programmable read only memory for 87CC20/CH20/CK20A/CM20A system evaluation. The 87PH20/PM20 are pin-compatible with 87CC20/CH20/CK20A/CM20A. The operations possible with the 87CC20/CH20/CK20A/CM20A can be performed by writing programs to PROM. The 87PH20/PM20 can write and verify in the same way as the TC57256AD using an adaptor socket BM1138 and an EPROM programmer.

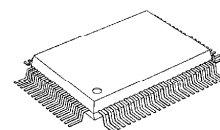
Part No.	ROM	RAM	Package	Adapter socket
TMP87PH20F	16 K × 8-bit	512 × 8-bit	P-QFP80-1420-0.80B	BM1138
TMP87PM20F	32 K × 8-bit	1 K × 8-bit		

Pin Assignments (Top View)

P-QFP80-1420-0.80B



P-QFP80-1420-0.80B

TMP87PH20F
TMP87PM20F

980910EBP2

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Pin Function

The 87PH20/PM20 have two modes: MCU and PROM.

(1) MCU mode

In this mode, the 87PH20/PM20 are pin-compatible with the 87CC20/CH20/CK20A/CM20A (fix the TEST / VPP pin at low level).

(2) PROM mode

Pin Name (PROM mode)	Input / Output	Functions	Pin Name (MCU mode)
A14 to A8	Input	PROM address inputs	P61 to P67
A7 to A0			SEG23 to SEG16
D7 to D0	I/O	PROM data input/outputs	P07 to P00
$\overline{CE}$	Input	Chip enable signal input (active low)	P13
$\overline{OE}$		Output enable signal input (active low)	P14
VPP	Power supply	+ 12.5 V / 5 V (Program supply voltage)	TEST
VCC		+ 5 V	VDD
GND		0 V	VSS
P37 to P30	I/O	Pull-up with resistance for input processing.	
P47 to P40			
P51 to P50			
P11		PROM mode setting pins. Fix at high level.	
P21			
P60			
P12, P10		PROM mode setting pins. Fix at low level.	
P22, P20			
P17 to P15			
$\overline{RESET}$			
XIN	Input	Connect an 8MHz oscillator to stabilize the internal state.	
XOUT	Output		
SEG15 to SEG0	Output	Open.	
COM3 to COM0			
VLC	LCD power supply		

OPERATIONAL DESCRIPTION

The following explains the 87PH20/PM20 hardware configuration and operation. The configuration and functions of the 87PH20/PM20 are the same as those of the 87CC20/CH20/CK20A/CM20A, except in that a one-time PROM is used instead of an on-chip mask ROM.

The 87PH20/PM20 are placed in the single-clock mode during reset. To use the dual-clock mode, the low-frequency oscillator should be turned on by executing [SET (SYSCR2). XTEN] instruction at the beginning of the program.

1. OPERATION MODE

The 87PH20/PM20 have two modes: MCU and PROM.

1.1 MCU Mode

The MCU mode is activated by fixing the TEST / VPP pin at low level.

In the MCU mode, operation is the same as with the 87CC20/CH20/CK20A/CM20A (the TEST / VPP pin cannot be used open because it has no built-in pull-down resistance).

1.1.1 Program Memory

The 87PH20 has a 16K × 8-bit (addresses C000_H - FFFF_H in the MCU mode, addresses 4000_H - 7FFF_H in the PROM mode), the 87PM20 has a 32K × 8-bit (addresses 8000_H - FFFF_H in the MCU mode, addresses 0000_H - 7FFF_H in the PROM mode) of program memory (OTP).

To use the 87PM20 as the system evaluation for the 87CK20A/CM20A, the program should be written to the program memory area as shown in figure 1-1.

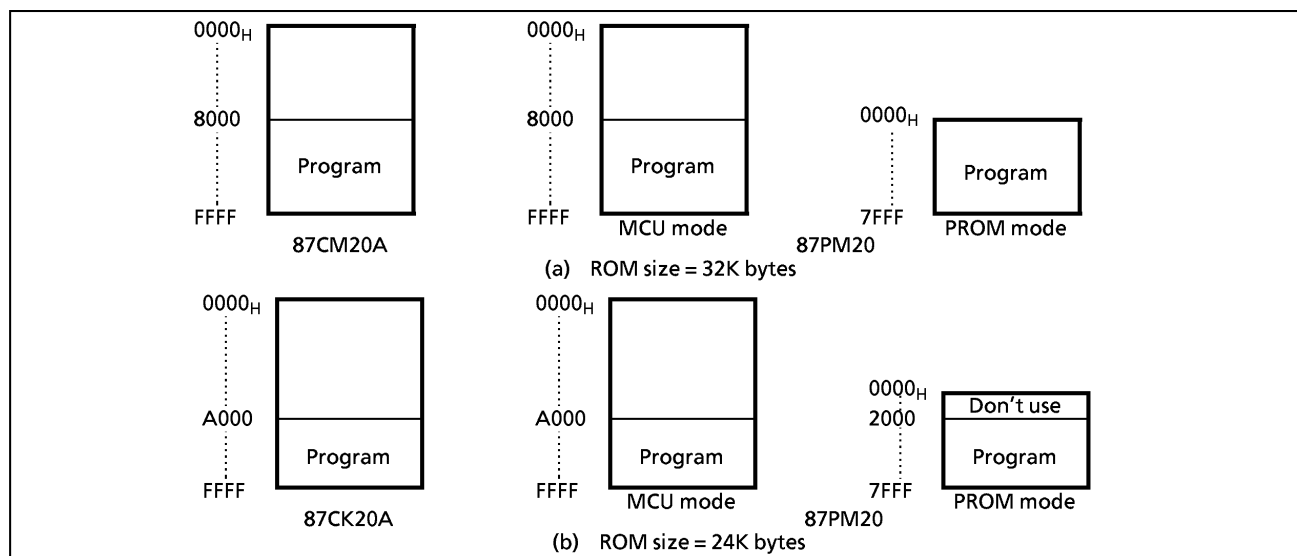


Figure 1-1. Program Memory Area (a), (b)

Electrical Characteristics

(1) 87PH20

Absolute Maximum Ratings

(V_{SS} = 0 V)

Parameter	Symbol	Condition	Ratings	Unit
Supply Voltage	V _{DD}		– 0.3 to 7	V
Program Voltage	V _{PP}	TEST/VPP pin	– 0.3 to 13.0	V
Input Voltage	V _{IN}		– 0.3 to V _{DD} + 0.3	V
Output Voltage	V _{OUT1}	Ports P0, P1, P21, P22, P6 and RESET	– 0.3 to V _{DD} + 0.3	V
	V _{OUT2}	Ports P20, P3, P4, P5	– 0.3 to 10	
Output Current (Per 1 pin)	I _{OUT1}	Ports P0, P1, P2, P4, P5, P6, P3 (except P30, P31)	3.2	mA
	I _{OUT2}	Only P30 and P31	30	
Output Current (Total)	Σ I _{OUT1}	Ports P0, P1, P2, P4, P5, P6, P3 (except P30, P31)	120	mA
	Σ I _{OUT2}	Only P30, P31	60	
Power Dissipation [T _{opr} = 70°C]	PD		350	mW
Soldering Temperature (time)	T _{sld}		260 (10 s)	°C
Storage Temperature	T _{stg}		– 55 to 125	°C
Operating Temperature	T _{opr}		– 30 to 70	°C

Note: The absolute maximum ratings are rated values which must not be exceeded during operation, even for an instant. Any one of the ratings must not be exceeded. If any absolute maximum rating is exceeded, a device may break down or its performance may be degraded, causing it to catch fire or explode resulting in injury to the user. Thus, when designing products which include this device, ensure that no absolute maximum rating value will ever be exceeded.

Recommended Operating Conditions

(V_{SS} = 0 V, T_{opr} = – 30 to 70°C)

Parameter	Symbol	Pins	Conditions		Min	Max	Unit
Supply Voltage	V _{DD}		fc = 8 MHz	NORMAL 1, 2 mode	4.5	6.0	V
				IDLE1, 2 mode			
			fc = 4.2 MHz	NORMAL1, 2 mode	2.7		
				IDLE1, 2 mode			
			fs = 32.768 kHz	SLOW mode	2.0		
				SLEEP mode			
	STOP mode						
Input High Voltage	V _{IH1}	Except hysteresis input	V _{DD} ≥ 4.5 V		V _{DD} × 0.70	V _{DD}	V
	V _{IH2}	Hysteresis input			V _{DD} × 0.75		
	V _{IH3}		V _{DD} < 4.5 V	V _{DD} × 0.90			
Input Low Voltage	V _{IL1}	Except hysteresis input	V _{DD} ≥ 4.5 V		0	V _{DD} × 0.30	V
	V _{IL2}	Hysteresis input				V _{DD} × 0.25	
	V _{IL3}		V _{DD} < 4.5 V	V _{DD} × 0.10			
Clock Frequency	fc	XIN, XOUT	V _{DD} = 4.5 to 6 V		0.4	8.0	MHz
			V _{DD} = 2.7 to 6 V			4.2	
	fs	XTIN, XTOUT			30.0	34.0	kHz

Note 1: The recommended operating conditions for a device are operating conditions under which it can be guaranteed that the device will operate as specified. If the device is used under operating conditions other than the recommended operating conditions (supply voltage, operating temperature range, specified AC/DC values etc.), malfunction may occur. Thus, when designing products which include this device, ensure that the recommended operating conditions for the device are always adhered to.

Note 2: Clock frequency fc: Supply voltage range is specified in NORMAL 1/2 mode.

D.C.Characteristics

(V_{SS} = 0 V, Topr = – 30 to 70°C)

Parameter	Symbol	Pins	Conditions	Min	Typ.	Max	Unit
Hysteresis Voltage	V _{HS}	Hysteresis input		–	0.9	–	V
Input Current	I _{IN1}	TEST	V _{DD} = 5.5 V V _{IN} = 5.5 V / 0 V	–	–	± 2	μA
	I _{IN2}	Open drain port and tri-state port					
	I _{IN3}	RESET, STOP					
Input Resistance	R _{IN2}	RESET		100	220	450	kΩ
Output Leakage Current	I _{LO}	Open drain port and	V _{DD} = 5.5 V, V _{OUT} = 5.5 V	–	–	2	μA
		tri-state port	V _{DD} = 5.5 V, V _{OUT} = 5.5 V / 0 V	–	–	± 2	
Output High Voltage	V _{OH2}	Tri- state port	V _{DD} = 4.5 V, I _{OH} = – 0.7 mA	4.1	–	–	V
Output Low Voltage	V _{OL}	Except XOUT and P30, P31	V _{DD} = 4.5 V, I _{OL} = 1.6 mA	–	–	0.4	V
Output Low Current	I _{OL3}	Only P30, P31	V _{DD} = 4.5 V, V _{OL} = 1.0 V	–	20	–	mA
Supply Current in NORMAL 1, 2 mode	I _{DD}		V _{DD} = 5.5 V, V _{IN} = 5.3 V / 0.2 V f _c = 8 MHz f _s = 32.768 kHz	–	8	14	mA
Supply Current in IDLE 1, 2 mode				–	4	6	mA
Supply Current in NORMAL 1, 2 mode			V _{DD} = 3.0 V, V _{IN} = 2.8 V / 0.2 V f _c = 4.19 MHz f _s = 32.768 kHz	–	2.5	3.5	mA
Supply Current in IDLE 1, 2 mode				–	1.5	2.0	mA
Supply Current in SLOW mode			V _{DD} = 3.0 V f _s = 32.768 kHz V _{IN} = 2.8 V / 0.2 V LCD driver is not enable	–	30	60	μA
Supply Current in SLEEP mode				–	15	30	μA
Supply Current in STOP mode			V _{DD} = 5.5 V V _{IN} = 5.3 V / 0.2 V	–	0.5	10	μA
Segment Output Low Resistance	R _{OS1}	SEG31 to SEG0	V _{DD} = 5 V V _{DD} – V _{LC} = 3 V	–	20	–	kΩ
Common Output Low Resistance	R _{OC1}	COM3 to COM0					
Segment Output High Resistance	R _{OS2}	SEG31 to SEG0		–	200	–	kΩ
Common Output High Resistance	R _{OC2}	COM3 to COM0					
Segment /Common Output Voltage	V _{O 2/3}	SEG31 to SEG0 and COM3 to COM0 pins		3.8	4.0	4.2	V
	V _{O 1/2}			3.3	3.5	3.7	
	V _{O 1/3}			2.8	3.0	3.2	

Note 1: Typical values show those at Topr = 25°C, V_{DD} = 5 V.

Note 2: Input Current I_{IN1}, I_{IN3}; The current through pull-up or pull-down resistor is not included.

Note 3: Output resistance R_{OS} and R_{OC} indicate "on" when switching levels.

Note 4: V_{O2/3} indicates an output current at the 2/3 level when operating in the 1/4 or 1/3 duty mode.

Note 5: V_{O1/2} indicates an output current at the 1/2 level when operating in the 1/2 duty or static mode.

Note 6: V_{O1/3} indicates an output current at the 1/3 level when operating in the 1/4 or 1/3 duty mode.

Note 7: When you use a liquid crystal display (LCD), it is necessary to give carefull consideration to the value of the output resistor R_{OS 1/2}, R_{OC 1/2}.

Note 8: R_{OS1}, R_{OC1}: On time of the lower output resistor is 2⁶/f_c, 2/f_s [s]

Note 9: R_{OS2}, R_{OC2}: On time of the higher output resistor is 1/(n/f_f) (1/n duty, f_f: frame frequency)

A.C. Characteristics

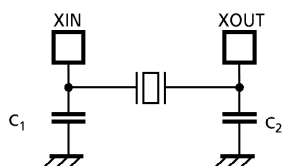
(V_{SS} = 0 V, V_{DD} = 4.5 to 6.0 V, T_{opr} = – 30 to 70°C)

Parameter	Symbol	Conditions	Min	Typ.	Max	Unit
Machine Cycle Time	t _{cy}	In NORMAL1, 2 modes	0.5	–	10	μs
		In IDLE 1, 2 modes				
		In SLOW mode	117.6	–	133.3	
		In SLEEP mode				
High Level Clock Pulse Width	t _{WCH}	For external clock operation (XIN input), f _c = 8MHz	50	–	–	ns
Low Level Clock Pulse Width	t _{WCL}					
High Level Clock Pulse Width	t _{WSH}	For external clock operation (XTIN input), f _s = 32.768kHz	14.7	–	–	μs
Low Level Clock Pulse Width	t _{WSL}					
Frequency of TC1 input	t _{TC1}	Frequency Measurement mode	–	–	f _c	MHz

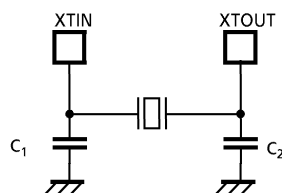
Recommended Oscillating Condition

(V_{SS} = 0 V, V_{DD} = 4.5 to 6.0 V, T_{opr} = – 30 to 70°C)

Parameter	Oscillator	Frequency	Recommended Oscillator	Recommended Condition	
				C ₁	C ₂
High-frequency	Ceramic Resonator	8 MHz	KYOCERA KBR8.0M	30pF	30pF
		4 MHz	KYOCERA KBR4.0MS MURATA CSA4.00MG		
	Crystal Oscillator	8 MHz	TOYOCOM 210B 8.0000	20pF	20pF
		4 MHz	TOYOCOM 204B 4.0000		
Low-frequency	Crystal Oscillator	32.768 kHz	NDK MX-38T	15pF	15pF



(1) High-frequency



(2) Low-frequency

Note: An electrical shield by metal shield plate on the surface of IC package should be recommendable in order to prevent the device from the high electric fieldstress applied from CRT (Cathode Ray Tube) for continuous reliable operation.

Electrical Characteristics

(2) 87PM20

Absolute Maximum Ratings

(V_{SS} = 0 V)

Parameter	Symbol	Condition	Ratings	Unit
Supply Voltage	V _{DD}		– 0.3 to 6.5	V
Program Voltage	V _{PP}	TEST/VPP pin	– 0.3 to 13.0	V
Input Voltage	V _{IN}		– 0.3 to V _{DD} + 0.3	V
Output Voltage	V _{OUT1}	Ports P0, P1, P21, P22, P6 and RESET	– 0.3 to V _{DD} + 0.3	V
	V _{OUT2}	Ports P20, P3, P4, P5	– 0.3 to 5.5	
Output Current (Per 1 pin)	I _{OUT1}	Ports P0, P1, P2, P4, P5, P6, P3 (except P30, P31)	3.2	mA
	I _{OUT2}	Only P30 and P31	30	
Output Current (Total)	Σ I _{OUT1}	Ports P0, P1, P2, P4, P5, P6, P3 (except P30, P31)	120	mA
	Σ I _{OUT2}	Only P30, P31	60	
Power Dissipation [T _{opr} = 70°C]	PD		350	mW
Soldering Temperature (time)	T _{sld}		260 (10 s)	°C
Storage Temperature	T _{stg}		– 55 to 125	°C
Operating Temperature	T _{opr}		– 30 to 70	°C

Note: The absolute maximum ratings are rated values which must not be exceeded during operation, even for an instant. Any one of the ratings must not be exceeded. If any absolute maximum rating is exceeded, a device may break down or its performance may be degraded, causing it to catch fire or explode resulting in injury to the user. Thus, when designing products which include this device, ensure that no absolute maximum rating value will ever be exceeded.

Recommended Operating Conditions

(V_{SS} = 0 V, T_{opr} = – 30 to 70°C)

Parameter	Symbol	Pins	Conditions		Min	Max	Unit
Supply Voltage	V _{DD}		fc = 8 MHz	NORMAL 1, 2 mode	4.5	5.5	V
				IDLE1, 2 mode			
			fc = 4.2 MHz	NORMAL1, 2 mode	2.7		
				IDLE1, 2 mode			
			fs = 32.768 kHz	SLOW mode			
				SLEEP mode			
	STOP mode	2.0					
Input High Voltage	V _{IH1}	Except hysteresis input	V _{DD} ≥ 4.5 V		V _{DD} × 0.70	V _{DD}	V
	V _{IH2}	Hysteresis input			V _{DD} × 0.75		
	V _{IH3}				V _{DD} < 4.5 V		
Input Low Voltage	V _{IL1}	Except hysteresis input	V _{DD} ≥ 4.5 V		0	V _{DD} × 0.30	V
	V _{IL2}	Hysteresis input				V _{DD} × 0.25	
	V _{IL3}					V _{DD} < 4.5 V	
Clock Frequency	fc	XIN, XOUT	V _{DD} = 4.5 to 5.5 V		0.4	8.0	MHz
			V _{DD} = 2.7 to 5.5 V			4.2	
	fs	XTIN, XTOUT			30.0	34.0	kHz

Note 1: The recommended operating conditions for a device are operating conditions under which it can be guaranteed that the device will operate as specified. If the device is used under operating conditions other than the recommended operating conditions (supply voltage, operating temperature range, specified AC/DC values etc.), malfunction may occur. Thus, when designing products which include this device, ensure that the recommended operating conditions for the device are always adhered to.

Note 2: Clock frequency fc: Supply voltage range is specified in NORMAL 1/2 mode & IDLE 1/2 mode.

D.C.Characteristics

(V_{SS} = 0 V, T_{opr} = – 30 to 70°C)

Parameter	Symbol	Pins	Conditions	Min	Typ.	Max	Unit
Hysteresis Voltage	V _{HS}	Hysteresis input		–	0.9	–	V
Input Current	I _{IN1}	TEST	V _{DD} = 5.5 V V _{IN} = 5.5 V / 0 V	–	–	± 2	μA
	I _{IN2}	Open drain port and tri-state port					
	I _{IN3}	RESET, STOP					
Input Resistance	R _{IN2}	RESET		100	220	450	kΩ
Output Leakage Current	I _{LO}	Open drain port	V _{DD} = 5.5 V, V _{OUT} = 5.5 V	–	–	2	μA
		tri-state port	V _{DD} = 5.5 V, V _{OUT} = 5.5 V / 0 V	–	–	± 2	
Output High Voltage	V _{OH2}	Tri- state port	V _{DD} = 4.5 V, I _{OH} = – 0.7 mA	4.1	–	–	V
Output Low Voltage	V _{OL}	Except XOUT and P30, P31	V _{DD} = 4.5 V, I _{OL} = 1.6 mA	–	–	0.4	V
Output Low Current	I _{OL3}	Only P30, P31	V _{DD} = 4.5 V, V _{OL} = 1.0 V	–	20	–	mA
Supply Current in NORMAL 1, 2 mode	I _{DD}		V _{DD} = 5.5 V, V _{IN} = 5.3 V / 0.2 V fc = 8 MHz fs = 32.768 kHz	–	12	18	mA
Supply Current in IDLE 1, 2 mode				–	4.5	6	mA
Supply Current in NORMAL 1, 2 mode			V _{DD} = 3.0 V, V _{IN} = 2.8 V / 0.2 V fc = 4.19 MHz fs = 32.768 kHz	–	3.5	4.5	mA
Supply Current in IDLE 1, 2 mode				–	1.5	2.0	mA
Supply Current in SLOW mode			V _{DD} = 3.0 V fs = 32.768 kHz V _{IN} = 2.8 V / 0.2 V LCD driver is not enable	–	30	60	μA
Supply Current in SLEEP mode				–	15	30	μA
Supply Current in STOP mode			V _{DD} = 5.5 V V _{IN} = 5.3 V / 0.2 V	–	0.5	10	μA
Segment Output Low Resistance	R _{OS1}	SEG31 to SEG0	V _{DD} = 5 V V _{DD} – V _{LC} = 3 V	–	20	–	kΩ
Common Output Low Resistance	R _{OC1}	COM3 to COM0					
Segment Output High Resistance	R _{OS2}	SEG31 to SEG0		–	200	–	kΩ
Common Output High Resistance	R _{OC2}	COM3 to COM0					
Segment /Common Output Voltage	V _{O 2/3}	SEG31 to SEG0 and COM3 to COM0 pins		3.8	4.0	4.2	V
	V _{O 1/2}			3.3	3.5	3.7	
	V _{O 1/3}			2.8	3.0	3.2	

Note 1: Typical values show those at T_{opr} = 25°C, V_{DD} = 5 V.

Note 2: Input Current ; The current through pull-up or pull-down resistor is not included.

Note 3: Output resistance R_{OS} and R_{OC} indicate "on" when switching levels.

Note 4: V_{O2/3} indicates an output current at the 2/3 level when operating in the 1/4 or 1/3 duty mode.

Note 5: V_{O1/2} indicates an output current at the 1/2 level when operating in the 1/2 duty or static mode.

Note 6: V_{O1/3} indicates an output current at the 1/3 level when operating in the 1/4 or 1/3 duty mode.

Note 7: When you use a liquid crystal display (LCD), it is necessary to give carefull consideration to the value of the output resistor R_{OS 1/2}, R_{OC 1/2}.

Note 8: R_{OS1}, R_{OC1}: On time of the lower output resistor is 26/f_c, 2/f_s [s]

Note 9: R_{OS2}, R_{OC2}: On time of the higher output resistor is 1/(n/f_c) (1/n duty, f_c: frame frequency)

A.C. Characteristics

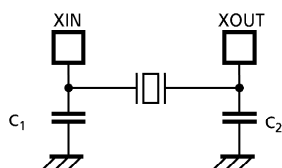
(V_{SS} = 0 V, V_{DD} = 4.5 to 5.5 V, Topr = – 30 to 70°C)

Parameter	Symbol	Conditions	Min	Typ.	Max	Unit
Machine Cycle Time	t _{cy}	In NORMAL1, 2 mode	0.5	–	10	μs
		In IDLE 1, 2 mode				
		In SLOW mode	117.6	–	133.3	
		In SLEEP mode				
High Level Clock Pulse Width	t _{WCH}	For external clock operation (XIN input), f _c = 8 MHz	50	–	–	ns
Low Level Clock Pulse Width	t _{WCL}					
High Level Clock Pulse Width	t _{WSH}	For external clock operation (XTIN input), f _s = 32.768 kHz	14.7	–	–	μs
Low Level Clock Pulse Width	t _{WSL}					
Frequency of TC1 input	t _{TC1}	Frequency Measurement mode	–	–	f _c	MHz

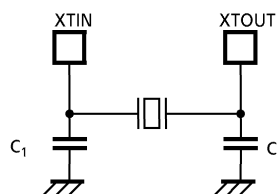
Recommended Oscillating Condition

(V_{SS} = 0 V, V_{DD} = 4.5 to 5.5 V, Topr = – 30 to 70°C)

Parameter	Oscillator	Frequency	Recommended Oscillator	Recommended Condition	
				C ₁	C ₂
High-frequency	Ceramic Resonator	8 MHz	KYOCERA KBR8.0M	30pF	30pF
		4 MHz	KYOCERA KBR4.0MS MURATA CSA4.00MG		
	Crystal Oscillator	8 MHz	TOYOCOM 210B 8.0000	20pF	20pF
		4 MHz	TOYOCOM 204B 4.0000		
Low-frequency	Crystal Oscillator	32.768 kHz	NDK MX-38T	15pF	15pF



(1) High-frequency



(2) Low-frequency

Note: An electrical shield by metal shield plate on the surface of IC package should be recommendable in order to prevent the device from the high electric fieldstress applied from CRT (Cathode Ray Tube) for continuous reliable operation.

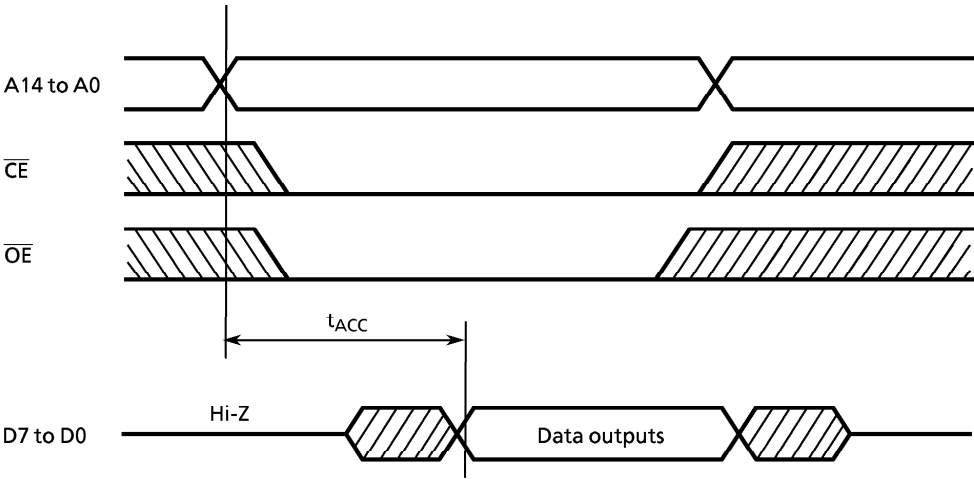
D.C./A.C. Characteristics (PROM mode)

(V_{SS} = 0 V)

(1) Read Operation (T_{opr} = – 30 to 70°C)

Parameter	Symbol	Condition	Min	Typ.	Max	Unit
Input High Voltage	V _{IH4}		V _{CC} × 0.7	–	V _{CC}	V
Input Low Voltage	V _{IL4}		0	–	V _{CC} × 0.12	V
Power Supply Voltage	V _{CC}		4.75	–	6.0	V
Program Power Supply Voltage	V _{PP}					
Address Access Time	t _{ACC}	V _{CC} = 5.0 ± 0.25 V	–	1.5 t _{cyc} + 300	–	ns

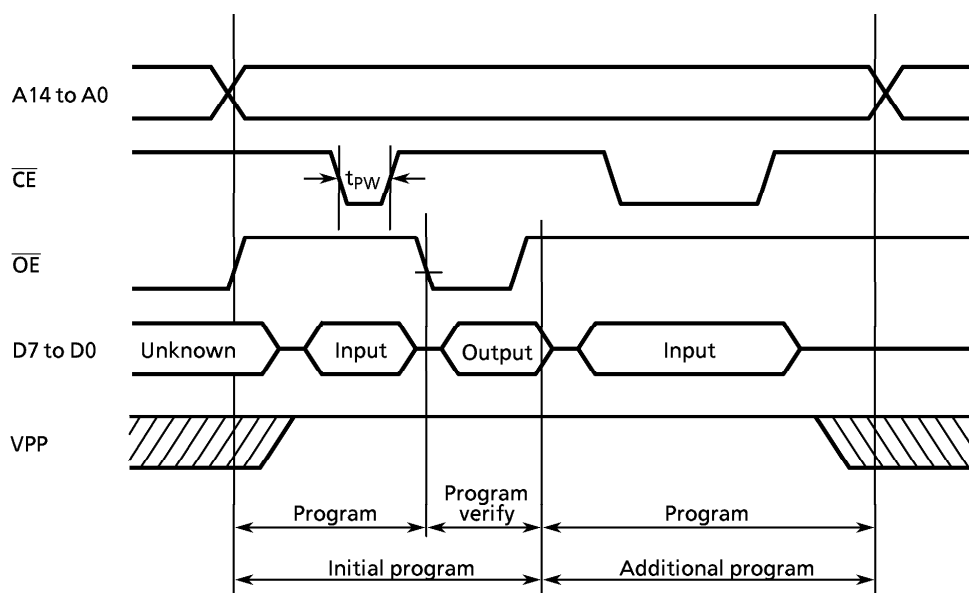
Note: t_{cyc} = 500 ns at 8 MHz



Timing Waveforms of Read Operation

(2) High-Speed Programming Operation ($T_{opr} = 25 \pm 5^{\circ}\text{C}$)

Parameter	Symbol	Condition	Min	Typ.	Max	Unit
Input High Voltage	V_{IH4}		$V_{CC} \times 0.7$	–	V_{CC}	V
Input Low Voltage	V_{IL4}		0	–	$V_{CC} \times 0.12$	V
Power Supply Voltage	V_{CC}		5.75	6.0	6.0	V
Program Power Supply Voltage	V_{PP}		12.0	12.5	13.0	V
Initial Program Pulse Width	t_{PW}	$V_{CC} = 6.0 \text{ V} \pm 0.25 \text{ V}$ $V_{PP} = 12.5 \text{ V} \pm 0.25 \text{ V}$	0.95	1.0	1.05	ms



Timing Waveforms of Programming Operation

Note 1: When V_{CC} power supply is turned on or after, V_{PP} must be increased.

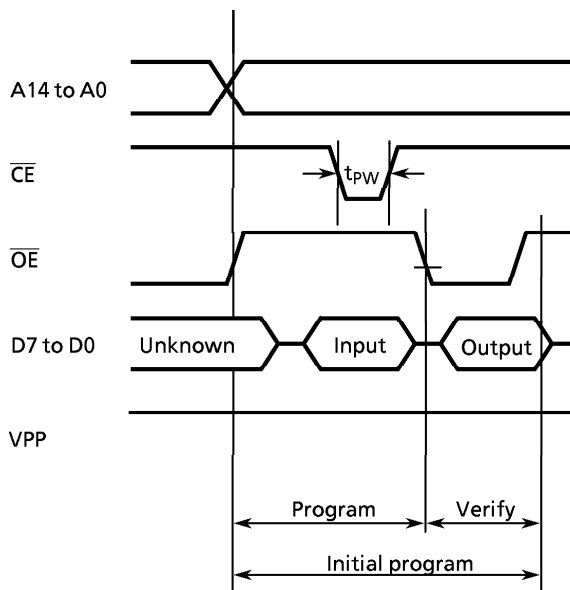
When V_{CC} power supply is turned off or before, V_{PP} must be decreased.

Note 2: The device must not be set to the EPROM programmer or picked up from it under applying the program voltage ($12.5 \text{ V} \pm 0.5 \text{ V}$) to the V_{PP} pin as the device is damaged.

Note 3: Be sure to execute the recommended programming mode with the recommended programming adaptor. If a mode or an adaptor except the above, the misoperation sometimes occurs.

(3) Program Operation (High speed write mode - II) ($T_{opr} = 25 \pm 5^{\circ}\text{C}$)

Parameter	Symbol	Condition	Min	Typ.	Max	Unit
Input High Voltage	V_{IH4}		$V_{CC} \times 0.7$	–	V_{CC}	V
Input Low Voltage	V_{IL4}		0	–	$V_{CC} \times 0.12$	V
Supply Voltage	V_{CC}		6.00	6.25	6.50	V
Program Supply Voltage	V_{PP}		12.50	12.75	13.0	V
Initial Program Pulse Width	t_{PW}	$V_{CC} = 6.25 \text{ V} \pm 0.25 \text{ V}$, $V_{PP} = 12.75 \text{ V} \pm 0.25 \text{ V}$	0.095	0.1	0.105	ms



Note 1: When V_{CC} power supply is turned on or after, V_{PP} must be increased.

When V_{CC} power supply is turned off or before, V_{PP} must be decreased.

Note 2: The device must not be set to the EPROM programmer or picked up from it under applying the program voltage ($12.75 \text{ V} \pm 0.25 \text{ V}$) to the V_{PP} pin as the device is damaged.

Note 3: Be sure to execute the recommended programing mode with the recommended programing adaptor. If a mode or an adaptor except the above, the misoperation sometimes occurs.

