

TOSHIBA Photo-IC Silicon Epitaxial Planar

# TPS855(F)

Luminosity Adjustment for TV Screens, CRT Monitors and Liquid-crystal Display Monitors  
Other Equipment Requiring Luminosity Adjustment

The TPS855(F) is a linear-output photo-IC which incorporates a photodiode and a current amp circuit in a single chip. This photo-IC is current output type, so can set up output voltage freely by arbitrary load resistance.

- High sensitivity : $I_L = 280 \mu A$  (typ.)  
@ $E_V = 100 \text{ lx}$  Using the fluorescent light
- Little fluctuation in light current  
: 1.67 times width ( $\pm 25\%$  typ.)
- Excellent illumination output linearity
- Open-emitter output
- Side-view package
- Environmentally friendly silicon used as chip material instead of CdS  
Suitable as a substitute for CdS-based products

## Absolute Maximum Ratings ( $T_a = 25^\circ\text{C}$ )

Characteristics	Symbol	Rating	Unit
Supply voltage	$V_{CC}$	-0.5 to 7	V
Output voltage	$V_{OUT}$	$\leq V_{CC}$	V
Light current	$I_L$	10	mA
Permissible power dissipation	P	150	mW
Operating temperature range	$T_{opr}$	-25 to 85	$^\circ\text{C}$
Storage temperature range	$T_{stg}$	-40 to 100	$^\circ\text{C}$
Soldering temperature range (5s) (Note 1)	$T_{sol}$	260	$^\circ\text{C}$

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Solder under the lead stopper.

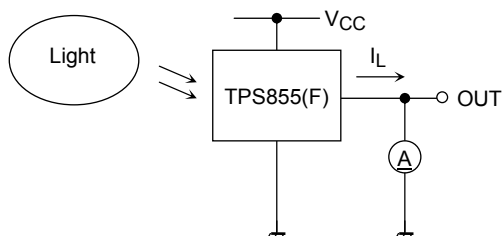
## Electrical and Optical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Supply voltage		$V_{CC}$	—	2.7	—	5.5	V
Supply current		$I_{CC}$	$V_{CC} = 5\text{ V}$ , $E_V = 1000\text{ lx}$ $R_L = 250\ \Omega$ (Note 2)	—	4.5	—	mA
Light current (1)		$I_L (1)$	$V_{CC} = 5\text{ V}$ , $E_V = 100\text{ lx}$ (Note 2), (Note 4)	—	365	—	$\mu\text{A}$
Light current (2)		$I_L (2)$	$V_{CC} = 5\text{ V}$ , $E_V = 10\text{ lx}$ (Note 3), (Note 4)	21	28	35	$\mu\text{A}$
Light current (3)		$I_L (3)$	$V_{CC} = 5\text{ V}$ , $E_V = 100\text{ lx}$ (Note 3), (Note 4)	210	280	350	$\mu\text{A}$
Light current ratio		$\frac{I_L (1)}{I_L (3)}$	—	—	1.3	1.7	
Dark current		$I_{LEAK}$	$V_{CC} = 5.5\text{ V}$ , $E_V = 0$	—	—	0.5	$\mu\text{A}$
Saturation output voltage		$V_O$	$V_{CC} = 5\text{ V}$ , $R_L = 75\text{ k}\Omega$ , $E_V = 100\text{ lx}$ (Note 3)	4.2	4.35	—	V
Peak sensitivity wavelength		$\lambda_p$	—	—	640	—	nm
Switching time	Rise time	$t_r$	$V_{CC} = 5\text{ V}$ , $R_L = 5\text{ k}\Omega$ (Note 5)	—	0.2	—	ms
	Fall time	$t_f$		—	0.6	—	

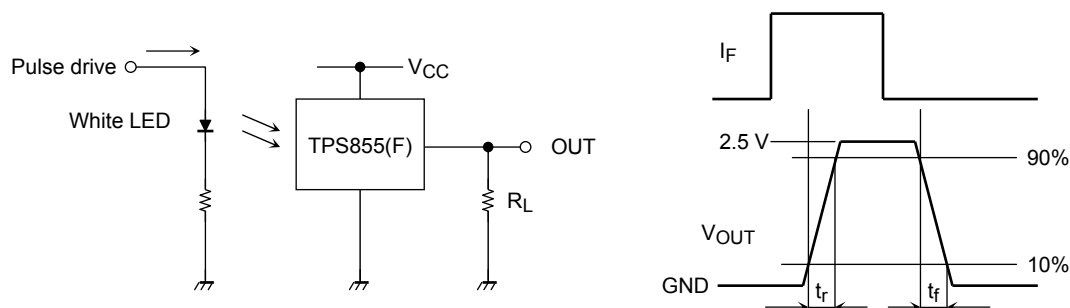
Note 2: CIE standard A light source is used (color temperature = 2856K, approximated incandescence light)

Note 3: Fluorescence light is used as light source. However, white LED is substituted in a mass-production process.

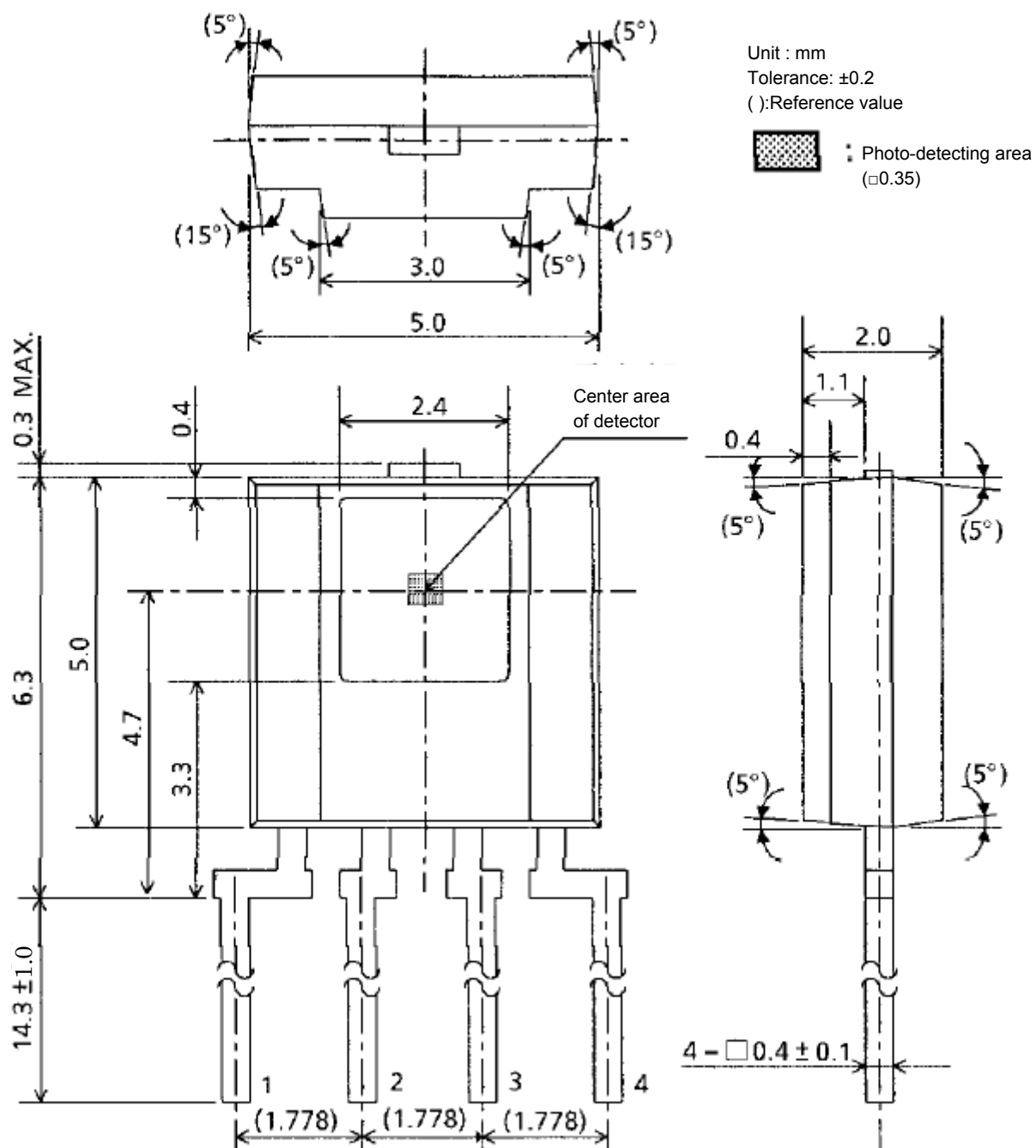
Note 4: Light current measurement circuit



Note 5: Rise time/fall time measurement method

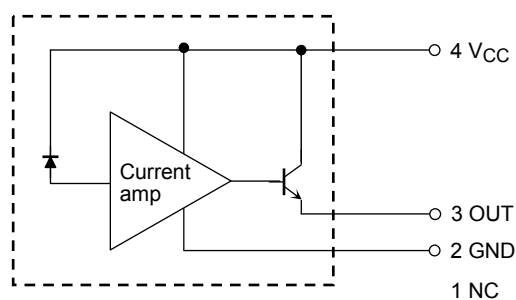


## Package Dimensions: TOSHIBA 0-5K1



Weight: 0.20 g (typ.)

## Block Diagram

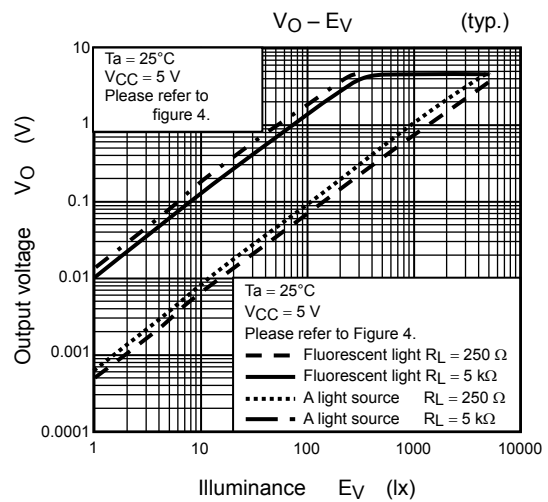
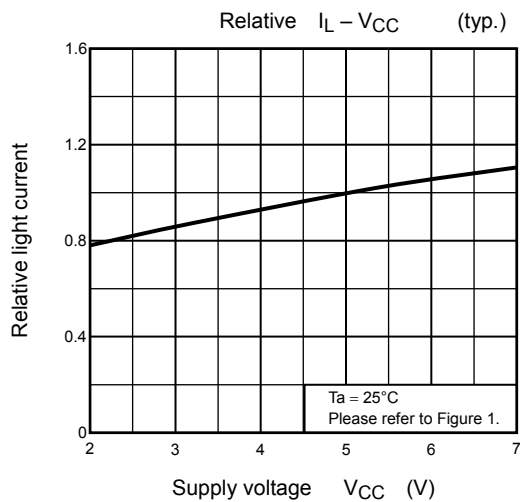
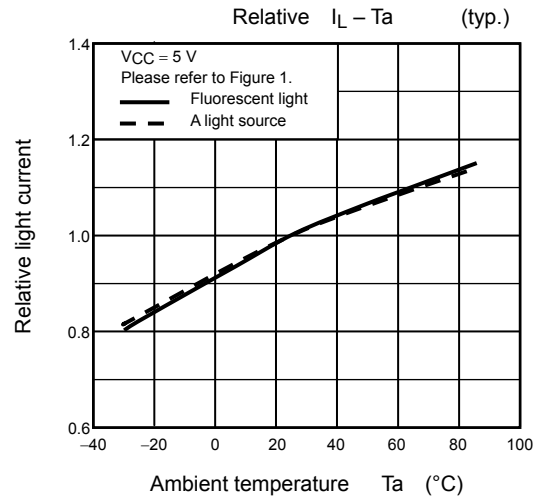
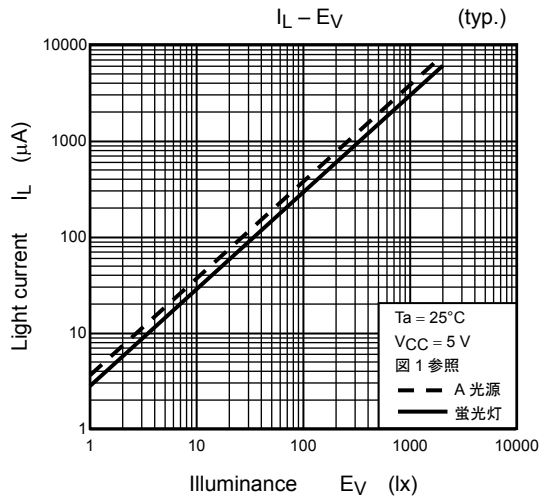
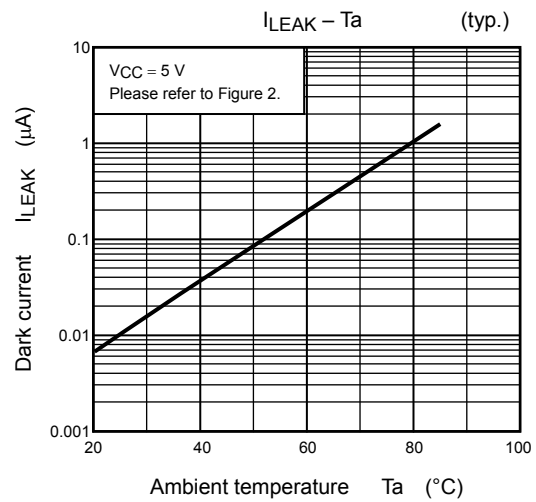
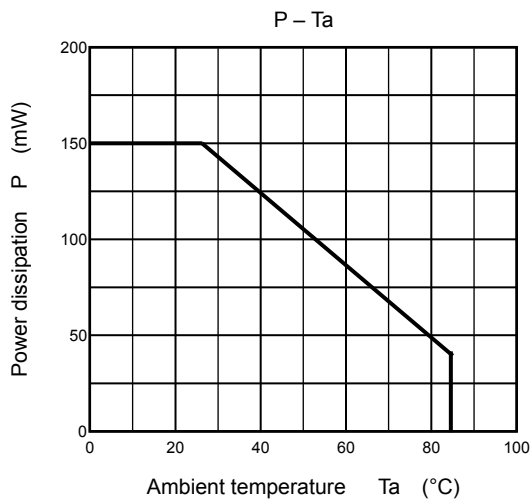


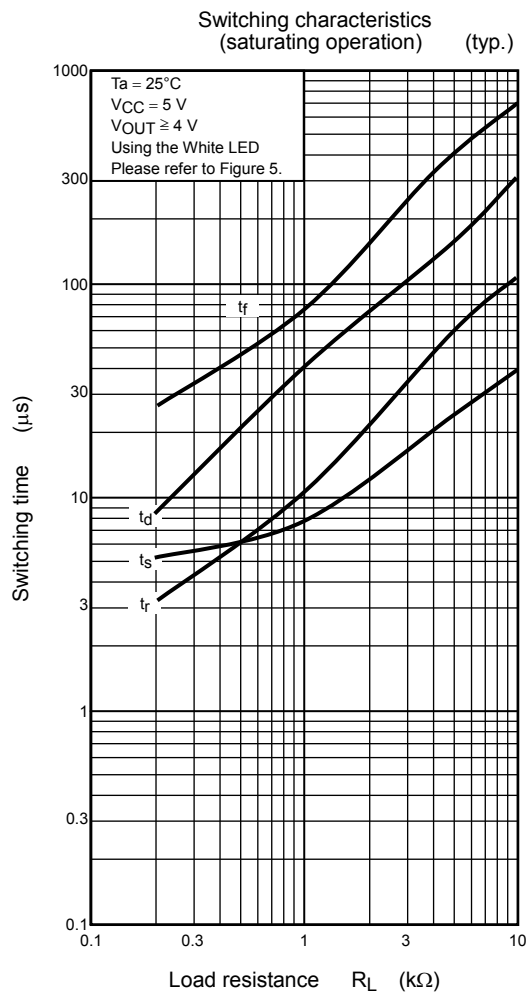
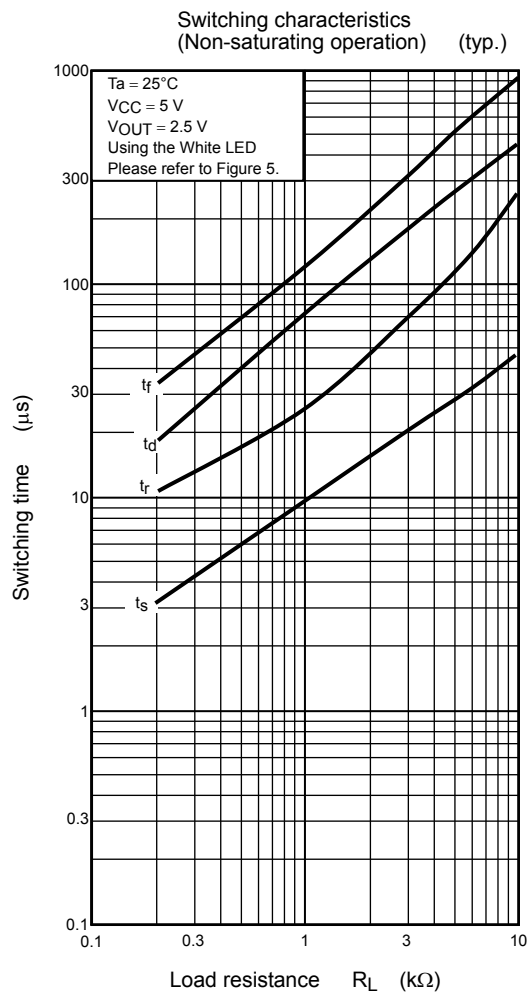
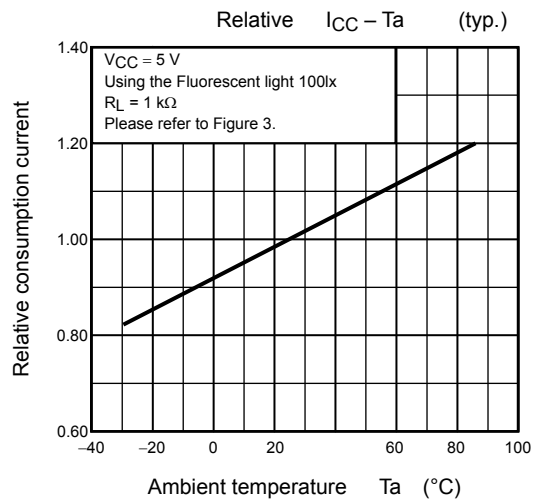
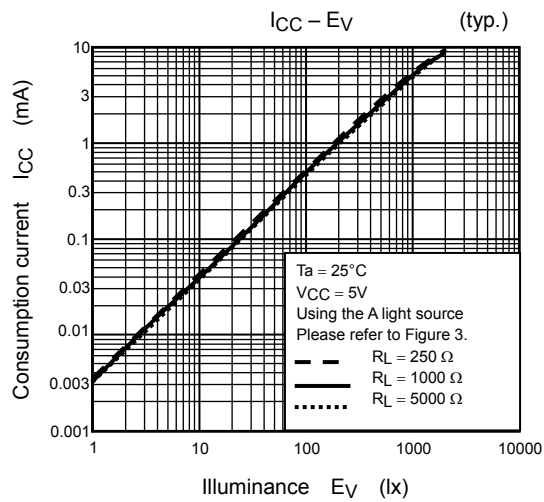
**Handling Precautions**

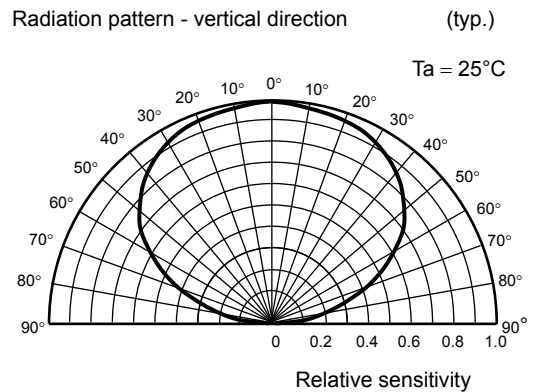
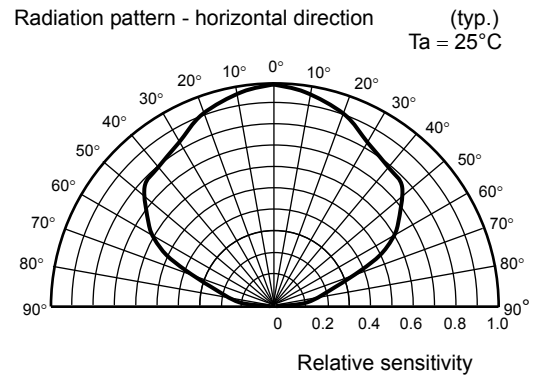
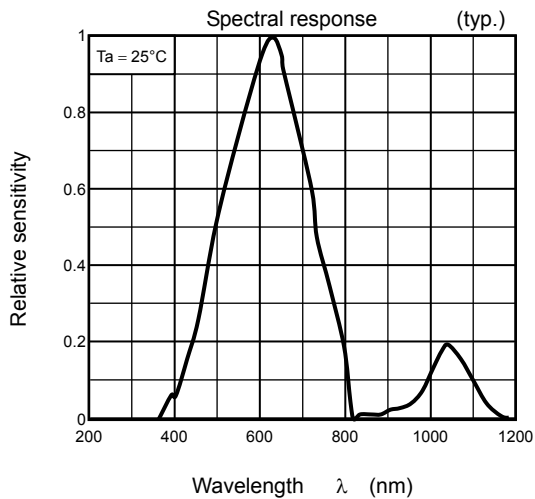
At power-on in darkness, the internal circuit takes about 50 ms to stabilize. During this period the output signal is unstable and may change. Please take this into account.

**Mounting Precautions**

- (1) When forming the leads, bend each lead under the lead stopper. Soldering must be performed after the leads have been formed.
- (2) Soldering must be performed under the stopper.
- (3) To stabilize the power line, insert a bypass capacitor of up to  $0.01\ \mu\text{F}$  between  $V_{CC}$  and GND, close to the device.







## Measurement Circuits

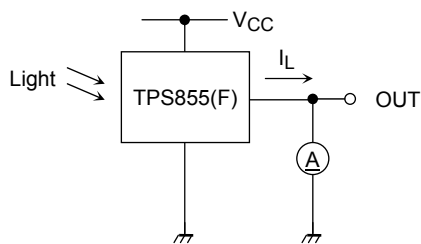


Figure 1 Light current measurement circuit

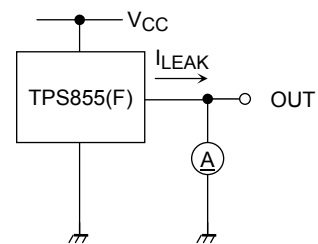


Figure 2 Dark current measurement circuit

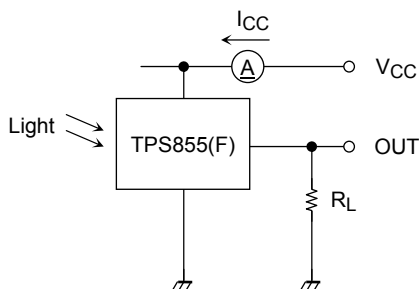


Figure 3 Consumption current measurement circuit

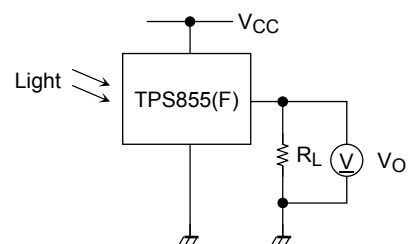


Figure 3 Output voltage measurement circuit

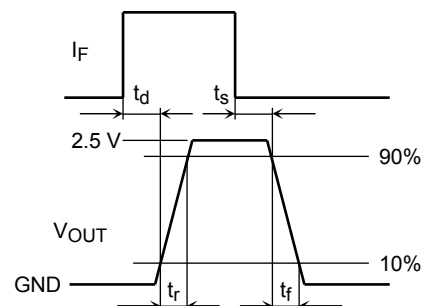
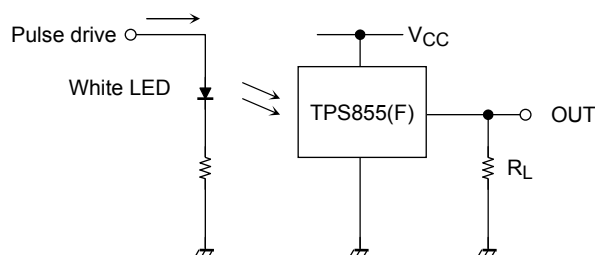


Figure 5 Switching measurement circuit and waveform

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