

TSM2301B 20V P-Channel MOSFET



SOT-23

1 2

Pin Definition:

1. Gate 2. Source

PRODUCT SUMMARY

V _{DS} (V)	$R_{DS(on)}(m\Omega)$	I _D (A)
-20	100 @ V _{GS} = -4.5V	-2.8
	150 @ V _{GS} = -2.5V	-2.0
	190 @ V _{GS} = -1.8V	-2.0

Features

- Advance Trench Process Technology
- High Density Cell Design for Ultra Low On-resistance

Application

- Load Switch
- PA Switch

G°

Ordering Information

Part No.	Package	Packing
TSM2301BCX RF	SOT-23	3Kpcs / 7" Reel

P-Channel MOSFET

Block Diagram

Absolute Maximum Rating (Ta = 25°C unless otherwise noted)

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V_{DS}	-20	V	
Gate-Source Voltage		V_{GS}	±8	V	
Continuous Drain Current, V _{GS} @4.5V		I _D	-2.8	А	
Pulsed Drain Current, V _{GS} @4.5V	94.5V I _{DM}		-8	А	
Continuous Source Current (Diode Co	nduction) ^{a,b}	Is	-0.72	А	
Continuous Source Current (Diode Conc Maximum Power Dissipation	Ta = 25°C	- P _D	0.9	W	
	Ta = 75°C		0.57		
Operating Junction Temperature	nction Temperature		+150	°C	
Operating Junction and Storage Temperature Range		T _J , T _{STG}	- 55 to +150	°C	

Thermal Performance

Parameter	Symbol	Limit	Unit
Lead Temperature (1/8" from case)	TL	5	S
Junction to Ambient Thermal Resistance (PCB mounted)	R⊖ _{JA}	120	°C/W

Notes:

- a. Pulse width limited by the Maximum junction temperature
- b. Surface Mounted on FR4 Board, t ≤ 5 sec.
- c. Surface Mounted on FR4 Board,



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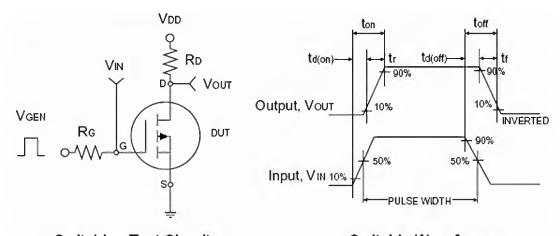
Pb Rohs COMPLIANCE

Electrical Specifications (Ta = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = -250uA$	BV _{DSS}	-20			V
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	$V_{GS(TH)}$	-0.45		-0.95	V
Gate Body Leakage	$V_{GS} = \pm 8V, V_{DS} = 0V$	I _{GSS}			±100	nA
Zero Gate Voltage Drain Current	$V_{DS} = -9.6V, V_{GS} = 0V$	I _{DSS}			-1.0	μA
On-State Drain Current ^a	V _{DS} ≥-10V, V _{GS} = -5V	I _{D(ON)}	-6			Α
	$V_{GS} = -4.5V, I_D = -2.8A$			80	100	
Drain-Source On-State Resistance ^a	$V_{GS} = -2.5V$, $I_D = -2.0A$	R _{DS(ON)}		110	150	mΩ
	$V_{GS} = -1.8V, I_D = -2.0A$			150	190	
Forward Transconductance ^a	$V_{DS} = -5V, I_{D} = -4A$	g _{fs}		6.5		S
Diode Forward Voltage	I _S = -0.75A, V _{GS} = 0V	V_{SD}		- 0.8	-1.2	V
Dynamic ^b		•				
Total Gate Charge	$V_{DS} = -6V, I_{D} = -2.8A,$ $V_{GS} = -4.5V$	Q_g		5.8		
Gate-Source Charge		Q_{gs}		0.85		nC
Gate-Drain Charge	V _{GS} 4.5 V	Q_{gd}		1.7		
Input Capacitance	\	C _{iss}		415		
Output Capacitance	$V_{DS} = -6V, V_{GS} = 0V,$ f = 1.0MHz	Coss		223		pF
Reverse Transfer Capacitance] I = 1.0IVIDZ	C _{rss}		87		
Switching ^c						
Turn-On Delay Time	$V_{DD} = -6V, R_L = 6\Omega,$ $V_{DD} = -1A, V_{GEN} = -4.5V,$	t _{d(on)}		13		
Turn-On Rise Time		t _r		36		,,
Turn-Off Delay Time		t _{d(off)}		42		nS
Turn-Off Fall Time	$R_{G} = 6\Omega$	t _f		34		

Notes:

- a. pulse test: PW ≤300µS, duty cycle ≤2%
- b. For DESIGN AID ONLY, not subject to production testing.
- b. Switching time is essentially independent of operating temperature.



Switching Test Circuit

Switchin Waveforms

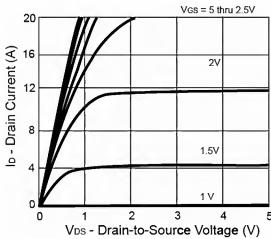


20V P-Channel MOSFET

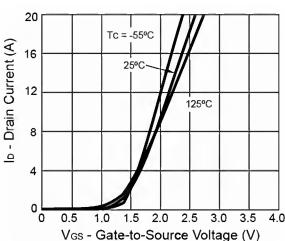


Electrical Characteristics Curve (Ta = 25°C, unless otherwise noted)

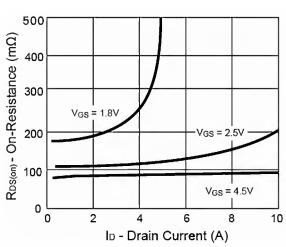
Output Characteristics



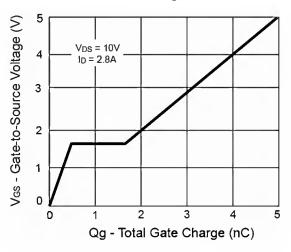
Transfer Characteristics



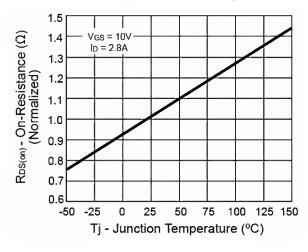
On-Resistance vs. Drain Current



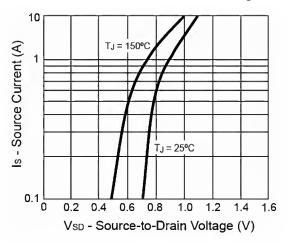
Gate Charge



On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage



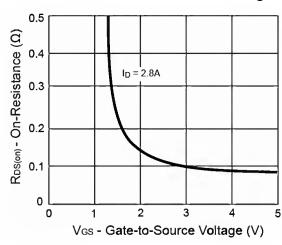


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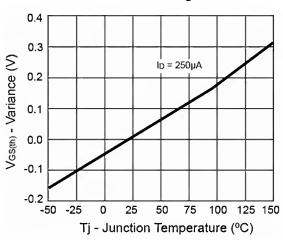


Electrical Characteristics Curve (Ta = 25°C, unless otherwise noted)

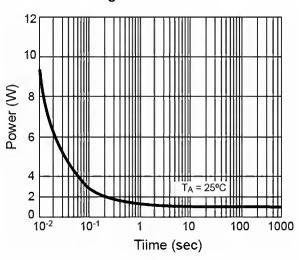
On-Resistance vs. Gate-Source Voltage



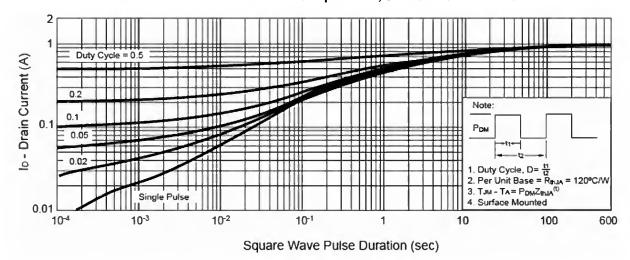
Threshold Voltage



Single Pulse Power



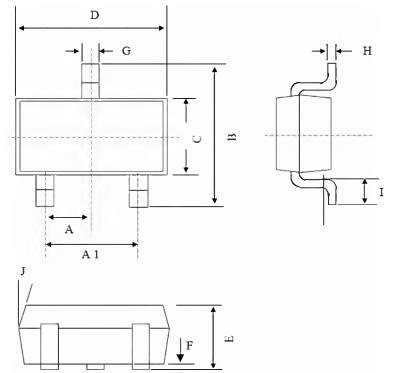
Normalized Thermal Transient Impedance, Junction-to-Ambient





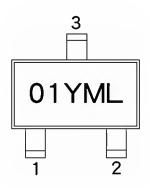


SOT-23 Mechanical Drawing



SOT-23 DIMENSION					
DIM	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX.	
Α	0.95	BSC	0.037 BSC		
A1	1.9 I	BSC	0.074 BSC		
В	2.60	3.00	0.102	0.118	
С	1.40	1.70	0.055	0.067	
D	2.80	3.10	0.110	0.122	
E	1.00	1.30	0.039	0.051	
F	0.00	0.10	0.000	0.004	
G	0.35	0.50	0.014	0.020	
Н	0.10	0.20	0.004	0.008	
Ī	0.30	0.60	0.012	0.024	
J	5°	10°	5°	10°	

Marking Diagram



01 = Device Code

Y = Year Code

M = Month Code

(A=Jan, B=Feb, C=Mar, D=Apl, E=May, F=Jun, G=Jul, H=Aug, I=Sep, J=Oct, K=Nov, L=Dec)

L = Lot Code



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