

L, S Band SPDT GaAs MMIC Switch

UPG153TB UPG155TB

FEATURES

LOW INSERTION LOSS:

LINS = 0.5 dB TYP (UPG153TB), 0.6 dB TYP (UPG155TB) at VCONT = +3.0 V/0 V, f = 1 GHz

• HIGH LINEARITY SWITCHING:

Pin (0.1 dB) = +29.0 dBm TYP (UPG153TB) Pin (0.1 dB) = +30.5 dBm TYP (UPG155TB) at VCONT = +3.0 V/0 V, f = 2 GHz

• SMALL 6 PIN MINI-MOLD PACKAGE:

Size: 2.0 x 1.25 x 0.9 mm

DESCRIPTION

The UPG153TB and UPG155TB are L-band SPDT (Single Pole Double Throw) GaAs FET switches for digital cellular or cordless telephone application. The devices can operate from 100 MHz to 2.5 GHz with low insertion loss. These devices are housed in an original 6 pin super mini-mold package similar to SOT363.

NEC's stringent quality assurance and test procedures assure the highest reliability and performance.

APPLICATION

- L, S-BAND DIGITAL CELLULAR OR CORDLESS TELEPHONE
- PCS, WLAN AND WLL APPLICATIONS

ELECTRICAL CHARACTERISTICS

(Unless otherwise specified, TA = 25°C, VCONT1 = 3 V, VCONT2 = 0 V or VCONT1 = 0 V, VCONT2 = 3 V; off chip DC blocking capacitor value, 51 pF)

PART NUMBER PACKAGE OUTLINE			UPG153TB S06			UPG155TB \$06		
PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX	MIN	TYP	MAX	
Insertion Loss at f = 0.1 to 1 GHz f = 2 GHz f = 2.5 GHz	dB dB dB		0.5 0.7 0.9	0.65 0.9		0.6 0.75 0.9	0.8 1.0	
Isolation at f = 0.1 to 2 GHz f = 2.5 GHz f = 1 GHz	dB dB dB	10 15	13 10 18.5		13 18	16 10 21.5		
Input Return Loss at f = 0.1 to 2 GHz	dB	11	15		11	15		
Input Return Loss at f = 0.1 to 2 GHz	dB	11	15		11	15		
Input Power at 0.1 dB Compression Point, f = 2 GHz	dBm		29			30.5		
Input Power at 1 dB Compression Point, f = 2 GHz	dBm	31	33		32	34		
Switching Speed	ns		30			30		
Control Current at VCONT = 3 V/0 V, no RF signal	μΑ		20	50		20	50	
	PACKAGE OUTLINE PARAMETERS AND CONDITIONS Insertion Loss at f = 0.1 to 1 GHz f = 2 GHz f = 2.5 GHz Isolation at f = 0.1 to 2 GHz f = 2.5 GHz Input Return Loss at f = 0.1 to 2 GHz Input Return Loss at f = 0.1 to 2 GHz Input Return Loss at f = 0.1 to 2 GHz Input Power at 0.1 dB Compression Point, f = 2 GHz Input Power at 1 dB Compression Point, f = 2 GHz Switching Speed	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	PACKAGE OUTLINE PARAMETERS AND CONDITIONS UNITS MIN TYP MAX MIN Insertion Loss at f = 0.1 to 1 GHz dB 0.5 0.65 f = 2 GHz dB 0.7 0.9 f = 2.5 GHz dB 10 13 13 f = 2.5 GHz dB 10 13 13 f = 1 GHz dB 15 18.5 18 Input Return Loss at f = 0.1 to 2 GHz dB 11 15 11 Input Power at 0.1 dB Compression Point, f = 2 GHz dBm 29 11 Input Power at 1 dB Compression Point, f = 2 GHz dBm 31 33 32 Switching Speed ns 30 30 30	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	

Note:

- 1. It is necessary to use DC blocking capacitors for the RF input and RF output. The value of DC blocking capacitors should be chosen to accommodate the frequency of operation. The range of recommended DC blocking capacitor value is less than 100 pF.
- 2. The distance between IC's GND pin and ground pattern of substrate should be as short as possible to avoid parasitics.

UPG153TB, UPG155TB

ABSOLUTE MAXIMUM RATINGS¹ (TA = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
VCONT1, 2	Control Voltage 1, 2 ²	V	-6 to +6
PIN	Input Power UPG155TB UPG153TB	dBm	+34 +33
Ртот	Total Power Dissipation ³	W	0.15
TA	Operating Temperature	°C	-45 to +85
Тѕтс	Storage Temperature	°C	-55 to +150

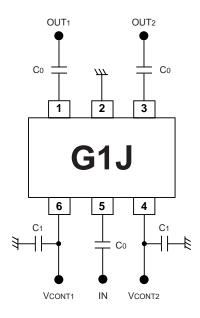
Notes:

- Operation in excess of any one of these conditions may result in permanent damage.
- 2. 2.5 V ≤ I VCONT1 VCONT2 I ≤ 6 V
- Mounted on a 50 x 50 x 1.6 mm double copper clad epoxy glass PWB, TA = +85 °C.

RECOMMENDED OPERATING CONDITIONS (TA = 25°C)

			UPG153TB			UPG155TB		
SYMBOLS	PARAMETERS	UNITS	MIN	TYP	MAX	MIN	TYP	MAX
VCONT	Control Voltage (High)	V	+2.5	+3	+5.3	+2.7	+3	+5.3
VCONT	Control Voltage (Low)	V	-0.2	0	+0.2	-0.2	0	+0.2

TEST CIRCUIT¹



Note:

1. (Ta = 25°C, VCONT1 = +3 V, VCONT2 = 0 V, VCONT1 = 0 V, VCONT2 = +3 V, f = 2 GHz; off chip DC blocking capacitors value: C0 = 51 pF, C1 = 1000 pF (Bypass))

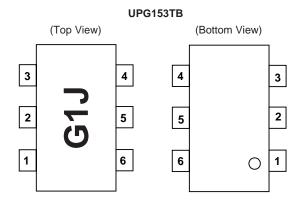
TRUTH TABLE OF SWITCHING BY CONDITION OF CONTROL VOLTAGE

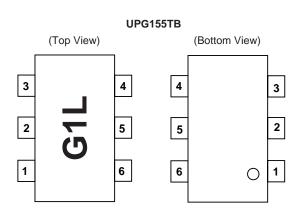
		VCONT1				
		Vcont(H)	Vo	CONT(L)	
Vcont2	Vcont(H)	IN—	OUT1 Note 1 OUT1	IN—	●— OUT1 ●— OUT2	
333.112	Vcont(L)	IN—	● OUT1	IN—	● OUT1● OUT2	Note 1

Note:

PIN CONNECTION DIAGRAM

PIN NO.	CONNECTION
1	OUT1
2	GND
3	OUT2
4	VCONT2
5	IN
6	VCONT1

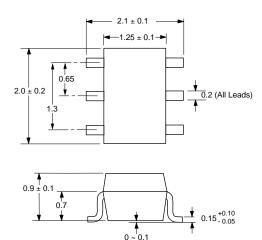




^{1.} In case of VCONT1 = VCONT2 = High or VCONT1 = VCONT2 = Low (that is same control voltage for both pins), input signal (Pin 5) is output to OUT1 (Pin 1) and OUT2 (Pin 3).

OUTLINE DIMENSIONS (Units in mm)

PACKAGE OUTLINE S06



Note:

All dimensions are typical unless otherwise specified.

ORDERING INFORMATION

PART NUMBER	QUANTITY
UPG153TB-E3	3 kpcs/Reel
UPG155TB-E3	3 kpcs/Reel

Note:

Embossed Tape, 8 mm wide,

Pins 1, 2, 3 face tape perforation side.

Life Support Applications

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