

Product Summary

BV_{DSS} (@ T_J Max)	$R_{DS(ON)}$ Max	I_D $T_c = +25^\circ C$
650V	2.5Ω @ $V_{GS} = 10V$	3.0A

Description and Applications

This new generation MOSFET has been designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Control
- Backlighting
- DC-DC Converters
- Power Management Functions

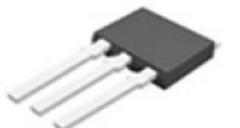
Features and Benefits

- Low On-Resistance
- High BV_{DSS} Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

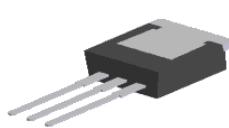
Mechanical Data

- Case: TO251 and TO251 (Type TH)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.33 grams (Approximate)

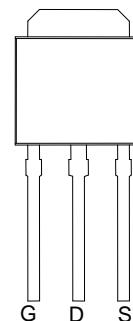
TO251 and TO251 (Type TH)



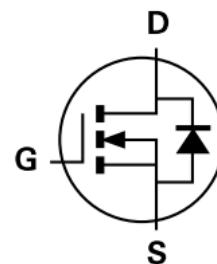
Top View



Bottom View



Top View
Pin Configuration



Internal Schematic

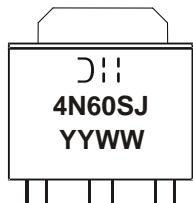
Ordering Information (Note 4)

Part Number	Case	Packaging
DMG4N60SJ3	TO251	75 pieces / Tube
DMG4N60SJ3	TO251 (Type TH)	75 pieces / Tube

Notes:

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



Manufacturer's Marking
4N60SJ = Product Type Marking Code
YYWW = Date Code Marking
YY or YY = Last Two Digits of Year (ex: 16 = 2016)
WW or WW = Week Code (01 to 53)

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	600	V
Gate-Source Voltage			V_{GSS}	± 30	V
Continuous Drain Current (Note 5) $V_{GS} = 10\text{V}$	Steady State	$T_C = +25^\circ\text{C}$ $T_C = +100^\circ\text{C}$	I_D	3.0 1.9	A
Maximum Body Diode Forward Current (Note 5)			I_S	6.0	A
Pulsed Drain Current (10 μs pulse, Duty Cycle = 1%)			I_{DM}	6.0	A
Avalanche Current, $L = 60\text{mH}$ (Note 7)			I_{AS}	1.7	A
Avalanche Energy, $L = 60\text{mH}$ (Note 7)			E_{AS}	90	mJ

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	$T_C = +25^\circ\text{C}$	P_D	41	W
	$T_C = +100^\circ\text{C}$		16	
Thermal Resistance, Junction to Ambient (Note 6)		$R_{\theta JA}$	47	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction to Case (Note 5)		$R_{\theta JC}$	3.0	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV_{DSS}	600	—	—	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1	μA	$V_{DS} = 600\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	100	nA	$V_{GS} = \pm 30\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(\text{TH})}$	2.5	—	4.5	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(\text{ON})}$	—	2.0	2.5	Ω	$V_{GS} = 10\text{V}, I_D = 2\text{A}$
Diode Forward Voltage	V_{SD}	—	—	1.4	V	$V_{GS} = 0\text{V}, I_S = 1\text{A}$
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C_{iss}	—	532	—	pF	$V_{DS} = 25\text{V}, f = 1.0\text{MHz}, V_{GS} = 0$
Output Capacitance	C_{oss}	—	47	—		
Reverse Transfer Capacitance	C_{rss}	—	4	—		
Gate Resistance	R_G	—	3.3	—	Ω	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Total Gate Charge ($V_{GS} = 10\text{V}$)	Q_g	—	14.3	—		
Gate-Source Charge	Q_{gs}	—	3.3	—		
Gate-Drain Charge	Q_{gd}	—	6.9	—	nC	$V_{DD} = 480\text{V}, I_D = 4\text{A}, V_{GS} = 10\text{V}$
Turn-On Delay Time	$t_{D(\text{ON})}$	—	14	—		
Turn-On Rise Time	t_R	—	34	—		
Turn-Off Delay Time	$t_{D(\text{OFF})}$	—	32	—		
Turn-Off Fall Time	t_F	—	25	—	ns	$V_{DD} = 300\text{V}, R_G = 25\Omega, I_D = 4\text{A}, V_{GS} = 10\text{V}$
Body Diode Reverse Recovery Time	t_{RR}	—	229	—		
Body Diode Reverse Recovery Charge	Q_{RR}	—	1564	—	nC	$dl/dt = 100\text{A}/\mu\text{s}, V_{DS} = 100\text{V}, I_F = 4\text{A}$

Notes:

5. Device mounted on infinite heatsink.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
7. Guaranteed by design. Not subject to production testing.
8. Short duration pulse test used to minimize self-heating effect.

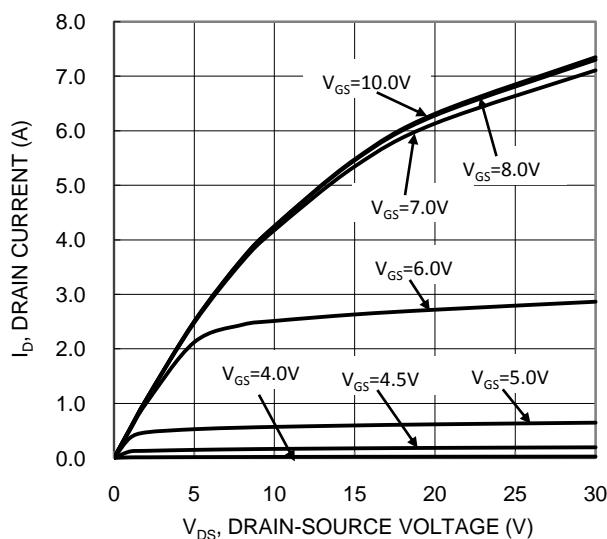


Figure 1. Typical Output Characteristic

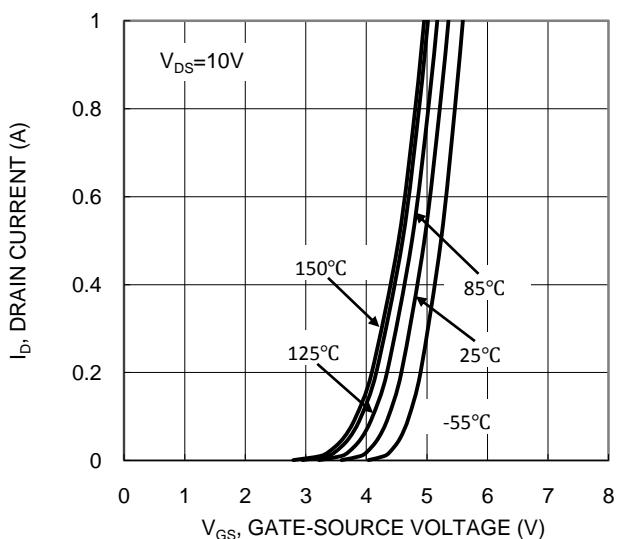


Figure 2. Typical Transfer Characteristic

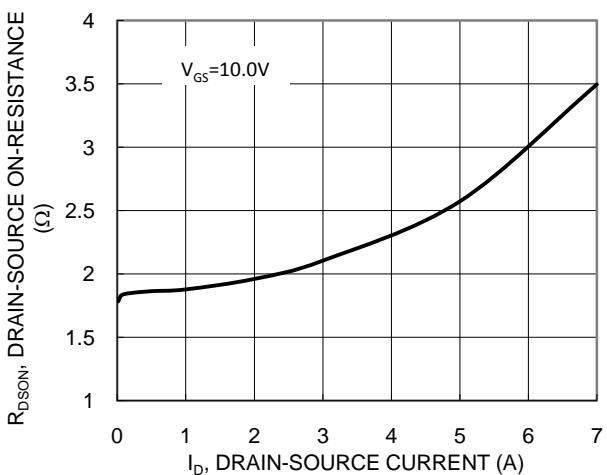


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

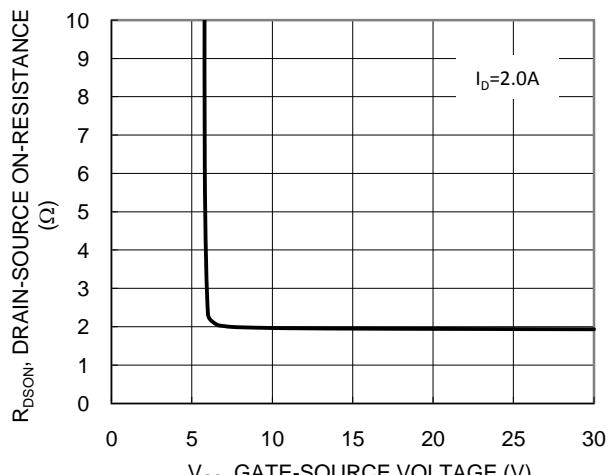


Figure 4. Typical Transfer Characteristic

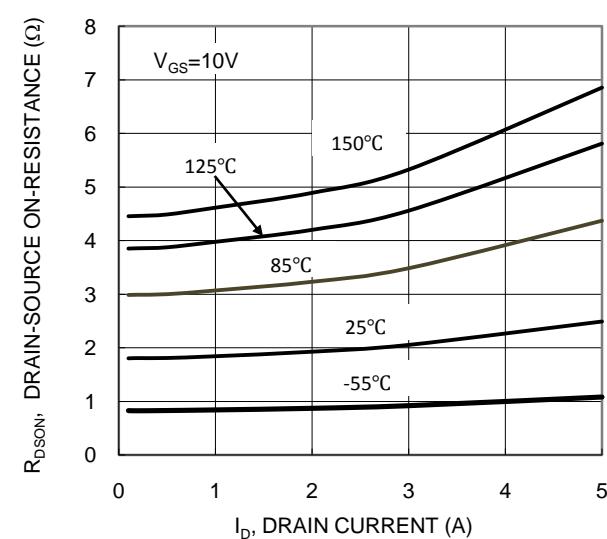


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

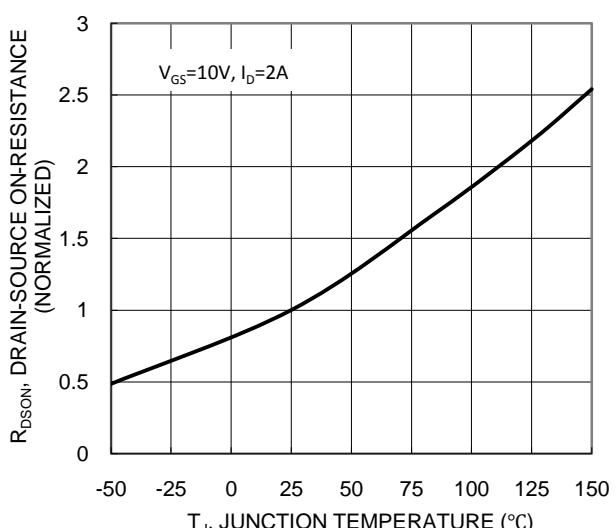
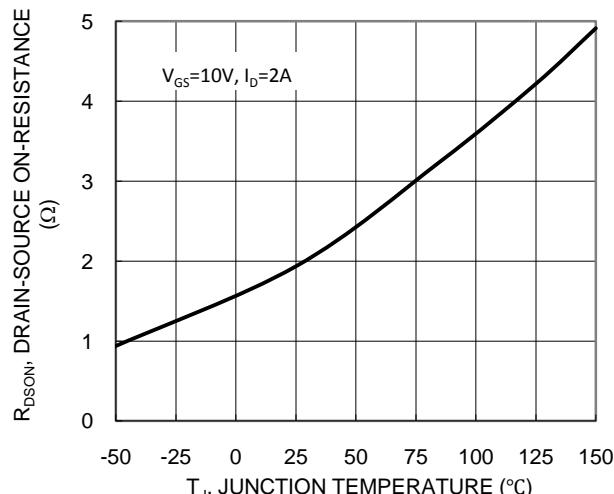
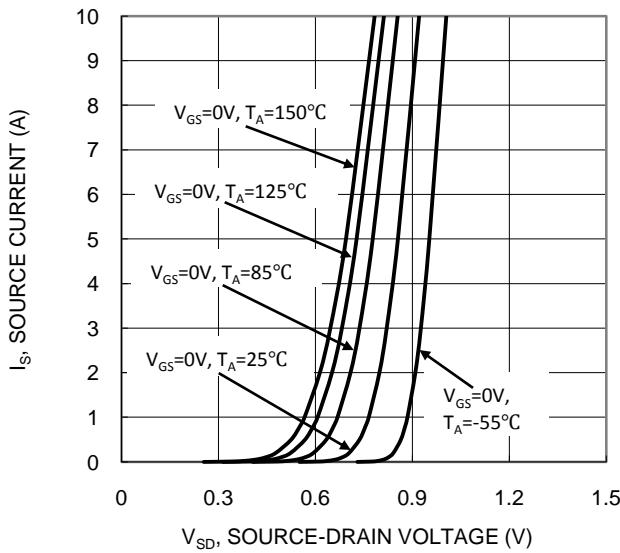


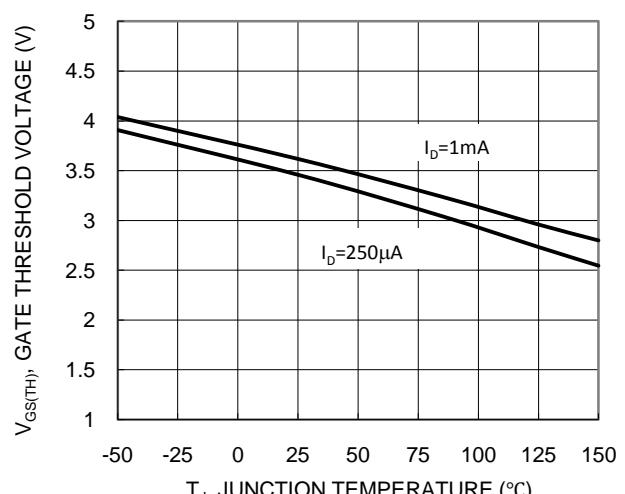
Figure 6. On-Resistance Variation with Temperature



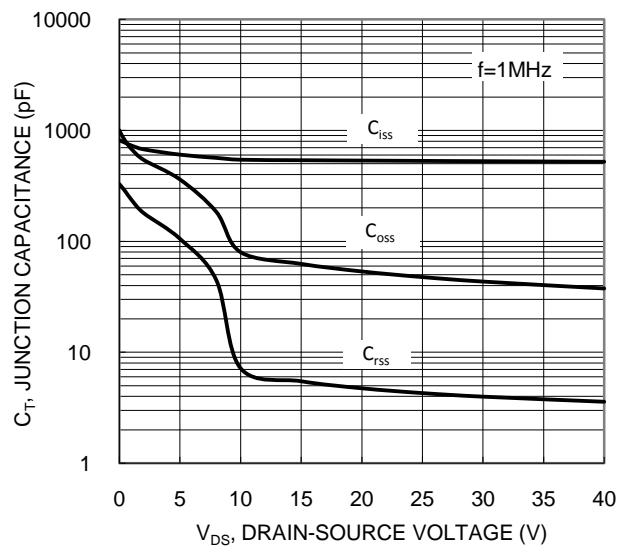
$V_{GS} = 10V, I_D = 2A$



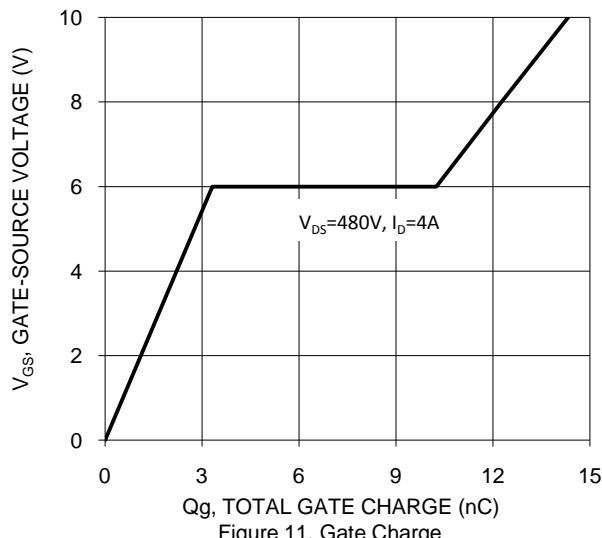
$V_{GS} = 0V, T_A = 150^\circ C$
 $V_{GS} = 0V, T_A = 125^\circ C$
 $V_{GS} = 0V, T_A = 85^\circ C$
 $V_{GS} = 0V, T_A = 25^\circ C$
 $V_{GS} = 0V, T_A = -55^\circ C$



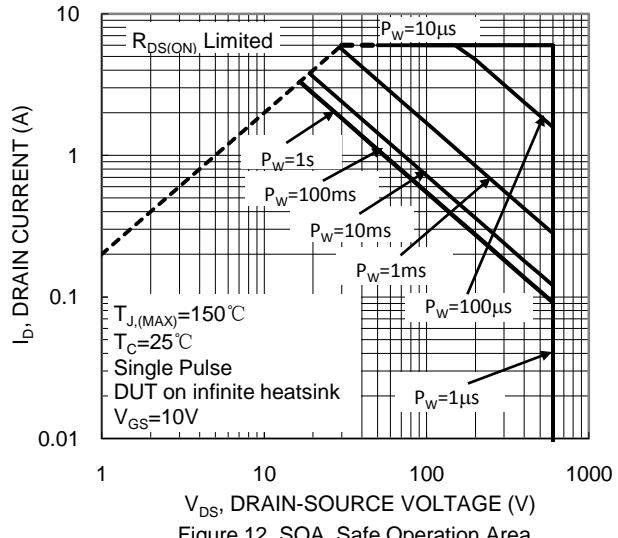
$I_D = 1mA$
 $I_D = 250\mu A$



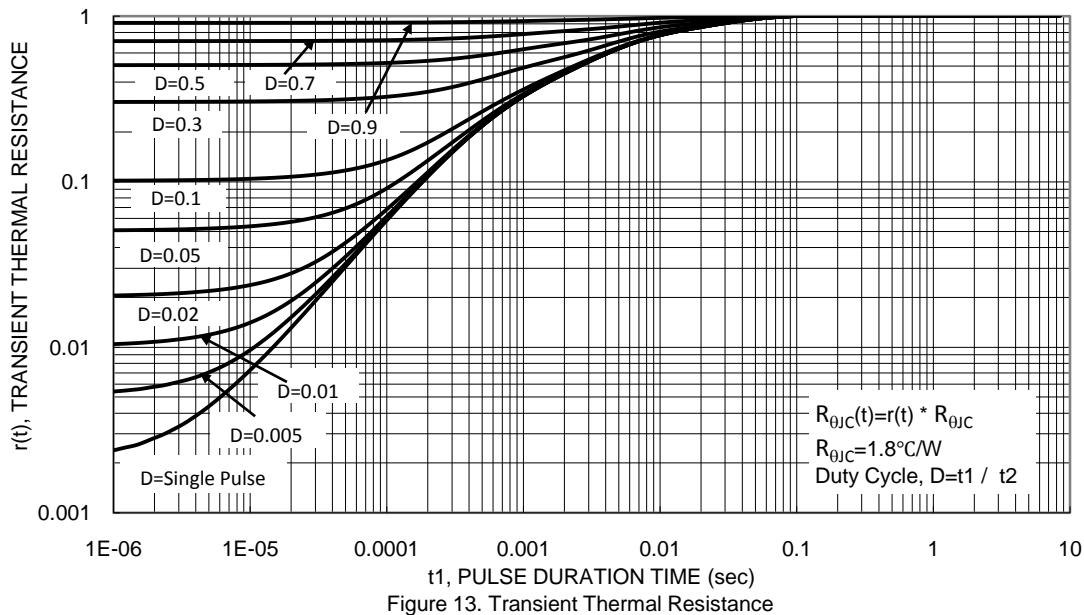
$f = 1MHz$
 C_{iss}
 C_{oss}
 C_{rss}



$V_{DS} = 480V, I_D = 4A$



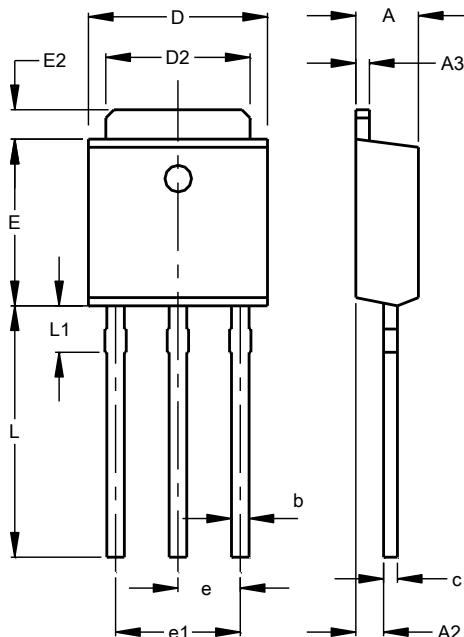
$T_{J(MAX)} = 150^\circ C$
 $T_C = 25^\circ C$
Single Pulse
DUT on infinite heatsink
 $V_{GS} = 10V$



Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

(1) Package Type: TO251



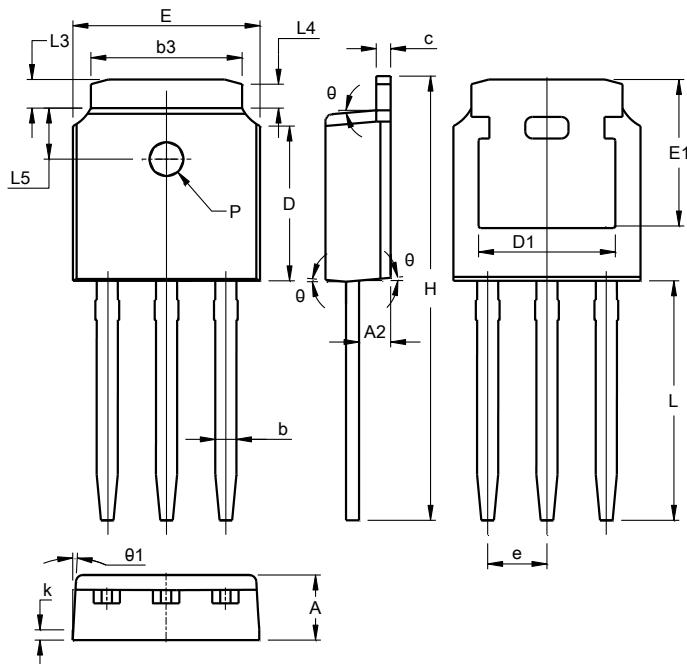
TO251		
Dim	Min	Max
A	2.20	2.40
A2	0.95	1.15
A3	0.45	0.55
b	0.50	0.70
c	0.45	0.55
D	6.45	6.75
D2	5.20	5.40
E	5.95	6.25
E2	0.95	1.25
e	2.24	2.34
e1	4.43	4.73
L	9.00	9.40
L1	1.30	1.70

All Dimensions in mm

Package Outline Dimensions (Cont.)

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

(2) Package Type: TO251 (Type TH)



TO251 (Type TH)			
Dim	Min	Max	Typ
A	2.20	2.40	2.30
A2	0.97	1.17	1.07
b	0.68	0.90	0.78
b3	5.20	5.50	5.33
c	0.43	0.63	0.53
D	5.98	6.22	6.10
D1	5.30	REF	
e	2.286	BSC	
E	6.40	6.80	6.60
E1	4.63	5.03	4.83
H	16.22	16.82	16.52
k	0.40	REF	
L	9.15	9.65	9.40
L3	0.88	1.28	1.02
L4	0.75	REF	
L5	1.65	1.95	1.80
θ	5°	9°	7°
θ1	5°	9°	7°

All Dimensions in mm

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