





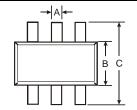
## **DUAL NPN SMALL SIGNAL SURFACE MOUNT TRANSIS**

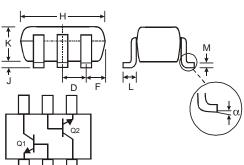
## **Features**

- **Epitaxial Planar Die Construction**
- Ideal for Medium Power Amplification and Switching
- Lead Free/RoHS Compliant (Note 3)
- "Green" Device, Note 4 and 5

### Mechanical Data

- Case: SOT-26
- Case Material: Molded Plastic, "Green" Molding Compound, Note 5. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Copper leadframe).
- Marking Information: K3M, See Page 3
- Ordering & Date Code Information: See Page 3
- Weight: 0.008 grams (approximate)





	SOT-26										
Dim	Min	Max	Тур								
Α	0.35	0.50	0.38								
В	1.50	1.70	1.60								
С	2.70	3.00	2.80								
D	_		0.95 0.55								
F	_										
Н	2.90	3.10	3.00								
J	0.013	0.10	0.05								
K	1.00	1.30	1.10								
L	0.35	0.55	0.40								
M	0.10	0.20	0.15								
α	0°	8°									
All [	All Dimensions in mm										

## **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	300	V
Collector-Emitter Voltage	V <sub>CEO</sub>	300	V
Emitter-Base Voltage	V <sub>EBO</sub>	6.0	V
Collector Current (Note 1) (Note 2)	I <sub>C</sub>	500	mA
Power Dissipation (Note 1)	P <sub>d</sub>	300	mW
Thermal Resistance, Junction to Ambient (Note 1)	$R_{ hetaJA}$	417	°C/W
Operating and Storage Temperature Range	T <sub>i</sub> , T <sub>STG</sub>	-55 to +150	°C

Notes:

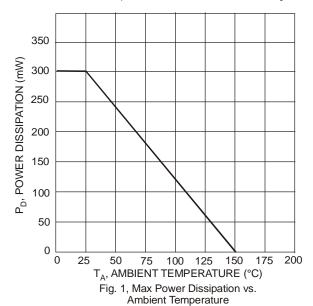
- Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- When operated under collector-emitter saturation conditions within the safe operating area defined by the thermal resistance rating (R<sub>NJA</sub>), power dissipation rating (P<sub>d</sub>) and power derating curve (Figure 1).
- No purposefully added lead.
- Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
- Product manufactured with Date Code 0627 (week 27, 2006) and newer are built with Green Molding Compound. Product manufactured prior to Date Code 0627 are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.



#### **Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)	, .,	J			
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	300	_	V	$I_C = 100 \mu A, I_E = 0$
Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	300	_	V	$I_C = 1.0 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	6.0	_	V	$I_E = 100 \mu A, I_C = 0$
Collector Cutoff Current	I <sub>CBO</sub>		100	nA	$V_{CB} = 200V, I_{E} = 0$
Collector Cutoff Current	I <sub>EBO</sub>		100	nA	$V_{CE} = 6.0V, I_{C} = 0$
ON CHARACTERISTICS (Note 6)					
		25			$I_C = 1.0 \text{mA}, V_{CE} = 10 \text{V}$
DC Current Gain	h <sub>FE</sub>	40	_	_	$I_C = 10mA, V_{CE} = 10V$
		40			$I_C = 30mA$ , $V_{CE} = 10V$
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>		0.5	V	$I_C = 20 \text{mA}, I_B = 2.0 \text{mA}$
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>		0.9	V	$I_C = 20mA$ , $I_B = 2.0mA$
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	$C_cb$		3.0	pF	$V_{CB} = 20V, f = 1.0MHz, I_{E} = 0$
Current Gain-Bandwidth Product	f⊤	50		MHz	$V_{CE} = 20V, I_{C} = 10mA,$ f = 100MHz

Notes: 6. Short duration pulse test used to minimize self-heating effect.



10,000 = V<sub>CE</sub> = 5V 1,000 h<sub>FE</sub>, DC CURRENT GAIN 100 10 1 100 1,000 I<sub>C</sub>, COLLECTOR CURRENT (mA) Fig. 3, DC Current Gain vs. Collector Current

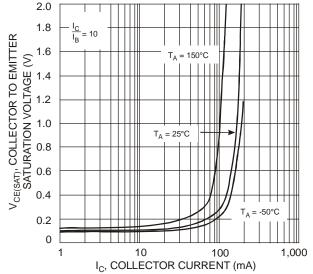


Fig. 2, Collector Emitter Saturation Voltage vs. Collector Current

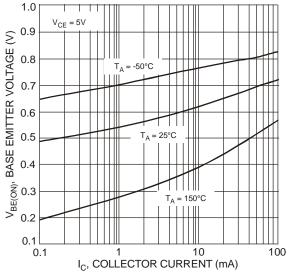
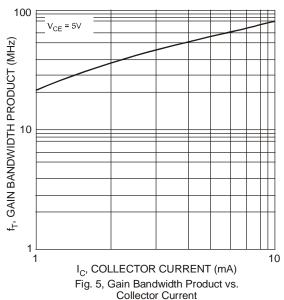


Fig. 4, Base Emitter Voltage vs. Collector Current



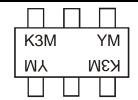


## Ordering Information (Note 5 & 7)

Device	Packaging	Shipping
MMDTA42-7-F	SOT-26	3000/Tape & Reel

Notes: 7. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

# **Marking Information**



K3M = Product Type Marking Code

YM = Date Code Marking Y = Year ex: P = 2003

M = Month ex: 9 = September

Data Code Key

Date Gode Ney										
Year	2004	2005	2006	2007	2008	2009	2010	2011	2012	
Code	R	S	Т	U	V	W	X	Υ	Z	

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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