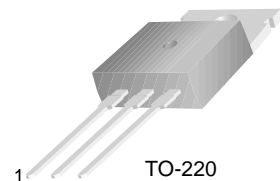


# KSD880

## Low Frequency Power Amplifier

- Complement to KSB834



1.Base 2.Collector 3.Emitter

## NPN Epitaxial Silicon Transistor

### Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
$V_{CBO}$	Collector-Base Voltage	60	V
$V_{CEO}$	Collector-Emitter Voltage	60	V
$V_{EBO}$	Emitter-Base Voltage	7	V
$I_C$	Collector Current	3	A
$I_B$	Base Current	0.3	A
$P_C$	Collector Dissipation ( $T_C=25^\circ\text{C}$ )	30	W
$T_J$	Junction Temperature	150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature	- 55 ~ 150	$^\circ\text{C}$

### Electrical Characteristics $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
$I_{CBO}$	Collector Cut-off Current	$V_{CB} = 60\text{V}, I_E = 0$			100	$\mu\text{A}$
$I_{EBO}$	Emitter Cut-off Current	$V_{EB} = 7\text{V}, I_C = 0$			100	$\mu\text{A}$
$BV_{CEO}$	Collector-Emitter Breakdown Voltage	$I_C = 50\text{mA}, I_B = 0$	60			V
$h_{FE1}$ $h_{FE2}$	DC Current Gain	$V_{CE} = 5\text{V}, I_C = 0.5\text{A}$ $V_{CE} = 5\text{V}, I_C = 3\text{A}$	60 20		300	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = 3\text{A}, I_B = 0.3\text{A}$		0.4	1	V
$V_{BE(on)}$	Base-Emitter On Voltage	$V_{CE} = 5\text{V}, I_C = 0.5\text{A}$		0.7	1	V
$f_T$	Current Gain Bandwidth Product	$V_{CE} = 5\text{V}, I_C = 0.5\text{A}$		3		MHz
$C_{ob}$	Output Capacitance	$V_{CB} = 10\text{V}, I_E = 0, f = 1\text{MHz}$		70		pF
$t_{ON}$	Turn ON Time	$V_{CC} = 30\text{V}, I_C = 1\text{A}$ $I_{B1} = - I_{B2} = 0.2\text{A}$ $R_L = 30\Omega$		0.8		$\mu\text{s}$
$t_{STG}$	Storage Time			1.5		$\mu\text{s}$
$t_F$	Fall Time			0.8		$\mu\text{s}$

### $h_{FE}$ Classification

Classification	O	Y	G
$h_{FE1}$	60 ~ 120	100 ~ 200	150 ~ 300

# Typical Characteristics

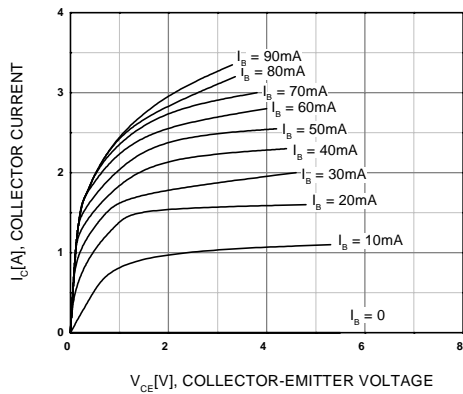


Figure 1. Static Characteristic

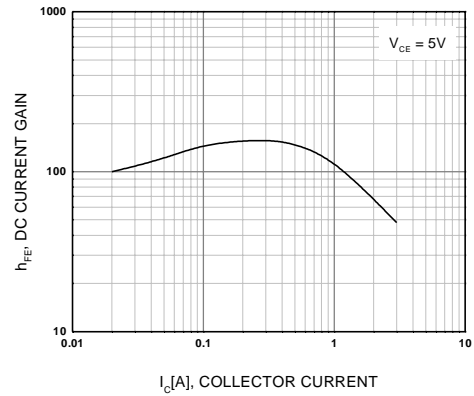


Figure 2. DC current Gain

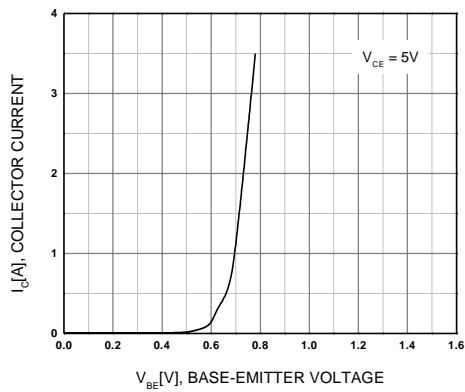


Figure 3. Base-Emitter On Voltage

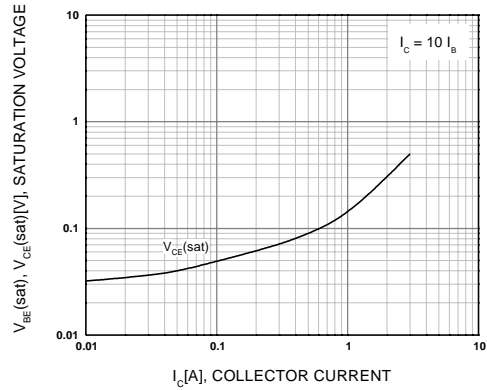


Figure 4. Collector-Emitter Saturation Voltage vs Collector Current

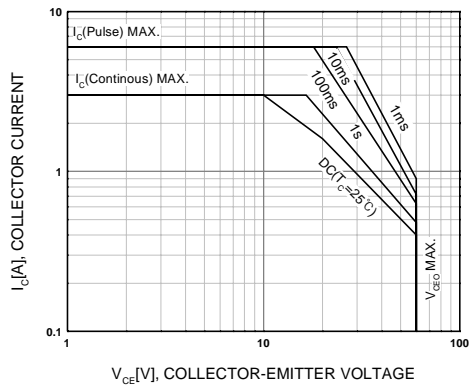


Figure 5. Safe Operating Area

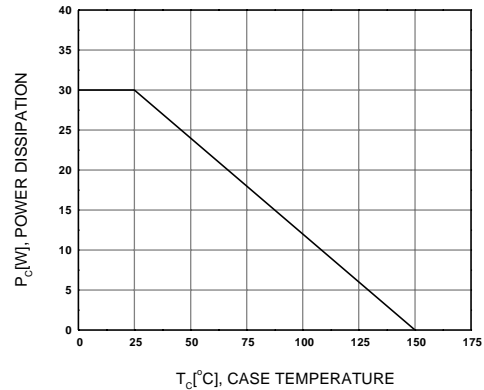


Figure 6. Power Derating

**KSD880**

Technical drawing of a 10-pin D-sub connector, showing three views: front, side, and top.

**Front View Dimensions:**

- Overall Width:  $9.90 \pm 0.20$
- Pin Diameter:  $\varnothing 3.60 \pm 0.10$
- Pin Spacing (Typical):  $2.54 \text{ TYP}$  [ $2.54 \pm 0.20$ ]
- Pin Height:  $1.30 \pm 0.10$
- Overall Height:  $18.95 \text{ MAX.}$
- Pin Length:  $10.08 \pm 0.30$
- Pin Tip Radius:  $0.80 \pm 0.10$
- Pin Tip Angle:  $45^\circ$
- Pin Tip Width:  $1.27 \pm 0.10$
- Pin Tip Height:  $1.52 \pm 0.10$
- Pin Tip Thickness:  $1.00$
- Pin Tip Width (Typical):  $1.46$
- Pin Tip Height (Typical):  $9.20 \pm 0.20$
- Pin Tip Width (Typical):  $1.70$
- Pin Tip Height (Typical):  $1.30 \pm 0.10$

**Side View Dimensions:**

- Overall Width:  $4.50 \pm 0.20$
- Pin Height:  $1.30 \pm 0.10$
- Pin Length:  $10.08 \pm 0.30$

**Top View Dimensions:**

- Overall Width:  $10.00 \pm 0.20$

©2000 Fairchild Semiconductor International

Rev. A, February 2000

## TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™  
Bottomless™  
CoolFET™  
CROSSVOLT™  
E<sup>2</sup>CMOS™  
FACT™  
FACT Quiet Series™  
FAST®  
FASTr™  
GTO™

HiSeC™  
ISOPLANAR™  
MICROWIRE™  
POP™  
PowerTrench®  
QFET™  
QS™  
Quiet Series™  
SuperSOT™-3  
SuperSOT™-6

SuperSOT™-8  
SyncFET™  
TinyLogic™  
UHC™  
VCX™

## DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

## LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR INTERNATIONAL.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

## PRODUCT STATUS DEFINITIONS

### Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

# Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Fairchild Semiconductor:

[KSD880OPATU](#) [KSD880Y](#) [KSD880Y\\_Q](#)