

PmodSD™ Reference Manual

Revision: December 18, 2009

Note: This document applies to REV B of the board.



215 E Main Suite D | Pullman, WA 99163
(509) 334 6306 Voice and Fax

Overview

The PmodSD Secure Digital memory card module provides a convenient SD card slot for use with Diligent system and microcontroller boards.

Features include:

- a 2x6 pin header
- an SD card slot

Functional Description

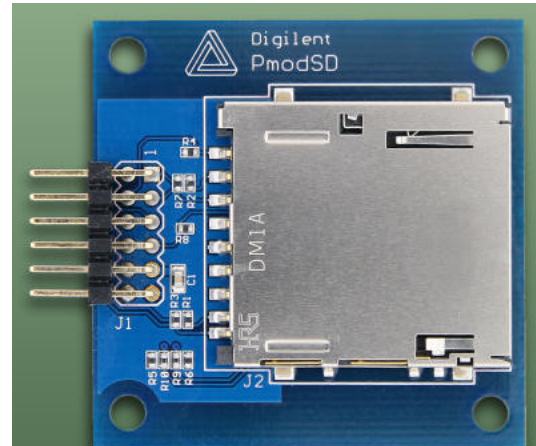
The PmodSD connects to all communication and power signals from a standard SD card and brings them out to the 2x6 pin header on the board.

The pinout placement on the PmodSD is designed to readily communicate with a Diligent system board via SPI. The SPI interface standard uses four signal lines: SS (slave select), MOSI (master out slave in), MISO (master in slave out), and SCK (serial clock).

SD cards can also be driven in a native mode that also uses the DAT1 and DAT2 signals as well as the DAT0, DAT3, CMD, and CLK signals.

The WP signal can be set by a switch on the SD card to prevent the host from writing or erasing data on the card. The CD signal can be used by the host to indicate that a card is in the SD slot and is detectable.

For more information on the necessary commands to communicate with an SD card, see the card's data sheet.



J1 connector	1 SS	CS/DAT3	SD card slot
	2 MOSI	DI/CMD	
	3 MISO	DO/DAT0	
	4 SCK	CLK	
	5 GND	VSSI	
	↓		
	6 VCC	VDD	
	7 DAT1	DAT1	
	8 DAT2	DAT2	
	9 CD	CD	
	10 WP	WP	
	11 GND	VSSI	
	↓		
	12 VCC	VDD	