



PSMN7R0-100PS

N-channel 100V 6.8 mΩ standard level MOSFET in TO220.

17 October 2013

Product data sheet

1. General description

Standard level N-channel MOSFET in TO220 package qualified to 175°C. This product is designed and qualified for use in a wide range of industrial, communications and domestic equipment.

2. Features and benefits

- High efficiency due to low switching and conduction losses
- Improved dynamic avalanche performance
- Suitable for standard level gate drive

3. Applications

- DC-to-DC converters
- Load switching
- Motor control
- Server power supplies

4. Quick reference data

Table 1. Quick reference data

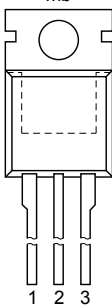
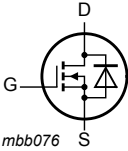
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	-	100	V
I _D	drain current	T _{mb} = 25 °C; V _{GS} = 10 V; Fig. 1	[1]	-	-	100	A
P _{tot}	total power dissipation	T _{mb} = 25 °C; Fig. 2		-	-	269	W
T _j	junction temperature			-55	-	175	°C
Static characteristics							
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 15 A; T _j = 100 °C; Fig. 12		-	-	12	mΩ
		V _{GS} = 10 V; I _D = 15 A; T _j = 25 °C; Fig. 13		-	5.4	6.8	mΩ
Dynamic characteristics							
Q _{GD}	gate-drain charge	V _{GS} = 10 V; I _D = 25 A; V _{DS} = 50 V; Fig. 15 ; Fig. 14		-	36	-	nC
Q _{G(tot)}	total gate charge	V _{GS} = 10 V; I _D = 25 A; V _{DS} = 50 V; Fig. 14 ; Fig. 15		-	125	-	nC

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Avalanche ruggedness						
$E_{DS(AL)S}$	non-repetitive drain-source avalanche energy	$V_{GS} = 10\text{ V}$; $T_{j(\text{init})} = 25\text{ °C}$; $I_D = 100\text{ A}$; $V_{sup} = 100\text{ V}$; unclamped; $R_{GS} = 50\text{ }\Omega$	-	-	316	mJ

[1] Continuous current is limited by package

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	 TO-220AB (SOT78)	 mbb076
2	D	drain		
3	S	source		
mb	D	mounting base; connected to drain		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PSMN7R0-100PS	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

7. Marking

Table 4. Marking codes

Type number	Marking code
PSMN7R0-100PS	PSMN7R0-100PS

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage	$T_j \geq 25\text{ °C}; T_j \leq 175\text{ °C}$		-	100	V
V_{DGR}	drain-gate voltage	$T_j \leq 175\text{ °C}; T_j \geq 25\text{ °C}; R_{GS} = 20\text{ k}\Omega$		-	100	V
V_{GS}	gate-source voltage			-20	20	V
I_D	drain current	$V_{GS} = 10\text{ V}; T_{mb} = 100\text{ °C}; \text{Fig. 1}$		-	85	A
		$V_{GS} = 10\text{ V}; T_{mb} = 25\text{ °C}; \text{Fig. 1}$	[1]	-	100	A
I_{DM}	peak drain current	pulsed; $t_p \leq 10\text{ }\mu\text{s}; T_{mb} = 25\text{ °C}; \text{Fig. 3}$		-	475	A
P_{tot}	total power dissipation	$T_{mb} = 25\text{ °C}; \text{Fig. 2}$		-	269	W
T_{stg}	storage temperature			-55	175	°C
T_j	junction temperature			-55	175	°C
$T_{sld(M)}$	peak soldering temperature			-	260	°C
Source-drain diode						
I_S	source current	$T_{mb} = 25\text{ °C}$	[1]	-	100	A
I_{SM}	peak source current	pulsed; $t_p \leq 10\text{ }\mu\text{s}; T_{mb} = 25\text{ °C}$		-	475	A
Avalanche ruggedness						
$E_{DS(AL)S}$	non-repetitive drain-source avalanche energy	$V_{GS} = 10\text{ V}; T_{j(\text{init})} = 25\text{ °C}; I_D = 100\text{ A}; V_{sup} = 100\text{ V}; \text{unclamped}; R_{GS} = 50\text{ }\Omega$		-	316	mJ

[1] Continuous current is limited by package

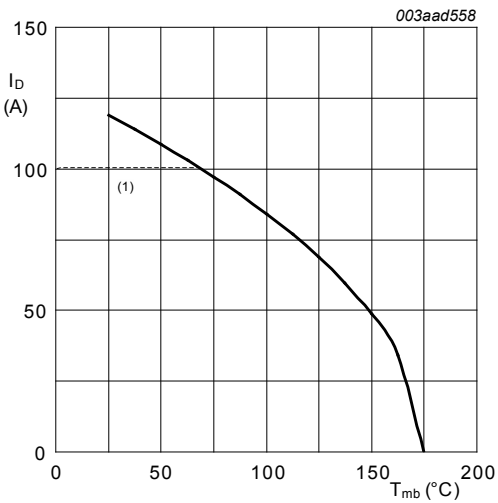


Fig. 1. Continuous drain current as a function of mounting base temperature

$V_{GS} \geq 10\text{ V}$; (1) capped at 100 A due to package.

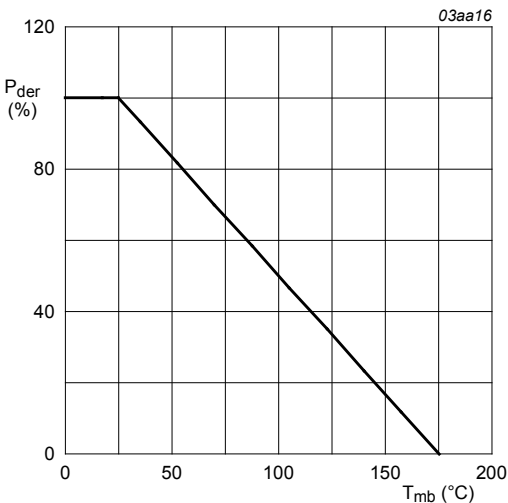


Fig. 2. Normalized total power dissipation as a function of mounting base temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}\text{C})}} \times 100\%$$

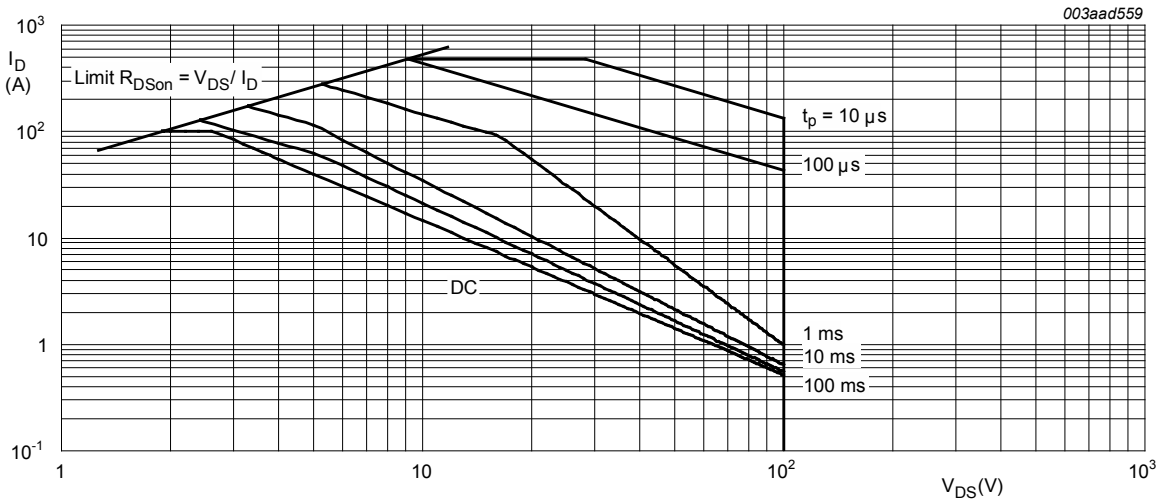


Fig. 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

$T_{mb} = 25^{\circ}\text{C}$; I_{DM} is single pulse

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	Fig. 4	-	0.3	0.56	K/W

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	vertical in free air	-	60	-	K/W

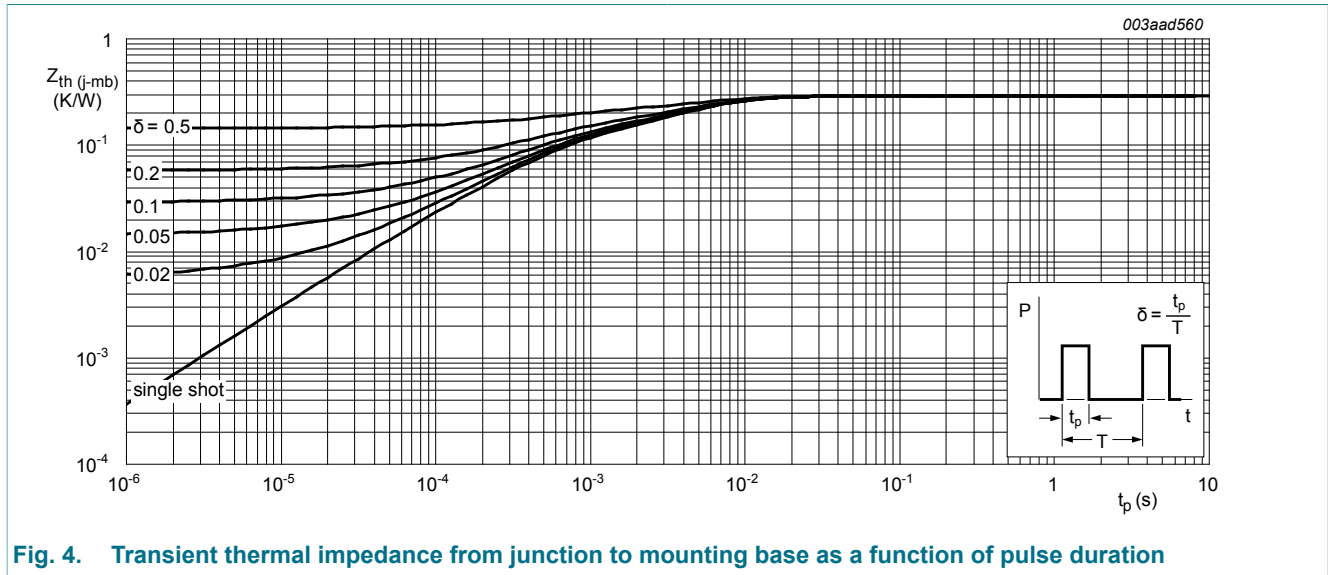


Fig. 4. Transient thermal impedance from junction to mounting base as a function of pulse duration

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = -55 \text{ }^\circ\text{C}$	90	-	-	V
		$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$	100	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ }^\circ\text{C};$ Fig. 10	1	-	-	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ }^\circ\text{C};$ Fig. 11; Fig. 10	2	3	4	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ }^\circ\text{C};$ Fig. 10	-	-	4.6	V
I_{DSS}	drain leakage current	$V_{DS} = 100 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 125 \text{ }^\circ\text{C}$	-	-	150	μA
		$V_{DS} = 100 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$	-	0.08	5	μA
I_{GSS}	gate leakage current	$V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$	-	10	100	nA
		$V_{GS} = -20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$	-	10	100	nA
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 15 \text{ A}; T_j = 100 \text{ }^\circ\text{C};$ Fig. 12	-	-	12	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 15 \text{ A}; T_j = 175 \text{ }^\circ\text{C};$ Fig. 12	-	15	19	mΩ

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
		$V_{GS} = 10\text{ V}$; $I_D = 15\text{ A}$; $T_j = 25\text{ }^{\circ}\text{C}$; Fig. 13		-	5.4	6.8	mΩ
R _G	internal gate resistance (AC)	f = 1 MHz		-	0.74	-	Ω
Dynamic characteristics							
Q _{G(tot)}	total gate charge	$I_D = 25\text{ A}$; $V_{DS} = 50\text{ V}$; $V_{GS} = 10\text{ V}$; Fig. 14 ; Fig. 15		-	125	-	nC
		$I_D = 0\text{ A}$; $V_{DS} = 0\text{ V}$; $V_{GS} = 10\text{ V}$		-	100	-	nC
Q _{GS}	gate-source charge	$I_D = 25\text{ A}$; $V_{DS} = 50\text{ V}$; $V_{GS} = 10\text{ V}$; Fig. 15 ; Fig. 14		-	28	-	nC
Q _{GS(th)}	pre-threshold gate-source charge	$I_D = 25\text{ A}$; $V_{DS} = 50\text{ V}$; $V_{GS} = 10\text{ V}$; Fig. 15		-	19.4	-	nC
Q _{GS(th-pl)}	post-threshold gate-source charge			-	9	-	nC
Q _{GD}	gate-drain charge	$I_D = 25\text{ A}$; $V_{DS} = 50\text{ V}$; $V_{GS} = 10\text{ V}$; Fig. 15 ; Fig. 14		-	36	-	nC
V _{GS(pl)}	gate-source plateau voltage	$V_{DS} = 50\text{ V}$; Fig. 15 ; Fig. 14		-	4.3	-	V
C _{iss}	input capacitance	$V_{DS} = 50\text{ V}$; $V_{GS} = 0\text{ V}$; f = 1 MHz; $T_j = 25\text{ }^{\circ}\text{C}$; Fig. 16		-	6686	-	pF
C _{oss}	output capacitance			-	438	-	pF
C _{rss}	reverse transfer capacitance			-	272	-	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 50\text{ V}$; R _L = 2 Ω; $V_{GS} = 10\text{ V}$; R _{G(ext)} = 4.7 Ω; $T_j = 25\text{ }^{\circ}\text{C}$		-	34.6	-	ns
t _r	rise time			-	45.6	-	ns
t _{d(off)}	turn-off delay time			-	103.9	-	ns
t _f	fall time			-	49.5	-	ns
Source-drain diode							
V _{SD}	source-drain voltage	$I_S = 25\text{ A}$; $V_{GS} = 0\text{ V}$; $T_j = 25\text{ }^{\circ}\text{C}$; Fig. 17		-	0.8	1.2	V
t _{rr}	reverse recovery time	$I_S = 25\text{ A}$; di _S /dt = 100 A/μs; $V_{GS} = 0\text{ V}$; $V_{DS} = 50\text{ V}$		-	64	-	ns
Q _r	recovered charge			-	167	-	nC

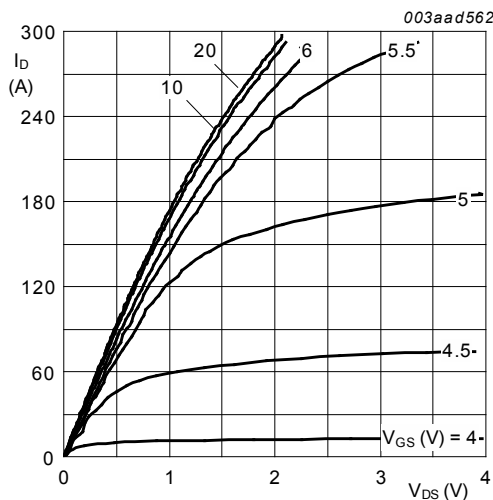


Fig. 5. Output characteristics: drain current as a function of drain-source voltage; typical values

$T_J = 25^{\circ}\text{C}$

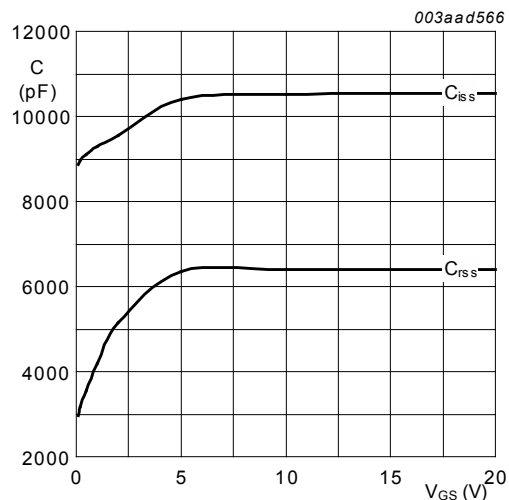


Fig. 6. Input and reverse transfer capacitances as a function of gate-source voltage; typical values

$V_{DS} = 0\text{V}; f = 1\text{MHz}$

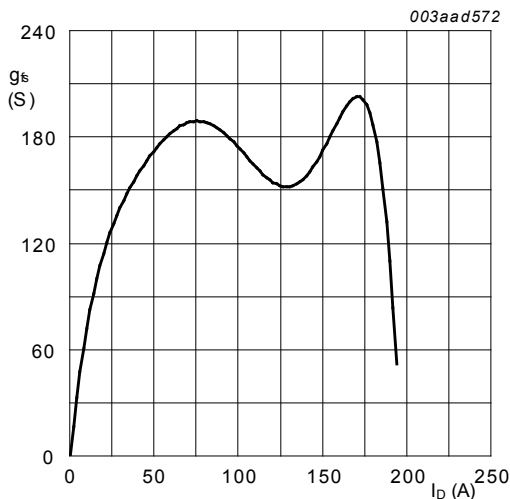


Fig. 7. Forward transconductance as a function of drain current; typical values

$T_J = 25^{\circ}\text{C}; V_{DS} = 15\text{V}$

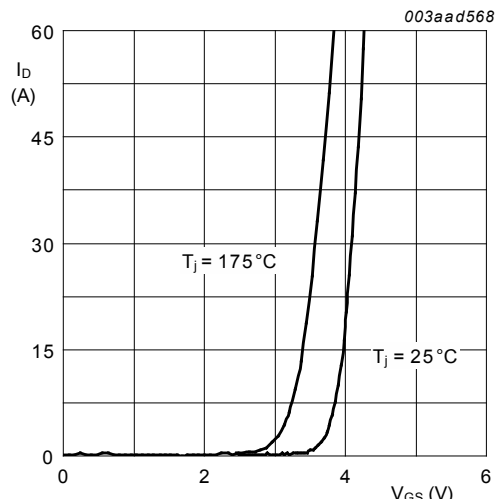


Fig. 8. Transfer characteristics: drain current as a function of gate-source voltage; typical values

$V_{DS} > I_D \times R_{DS(on)}$

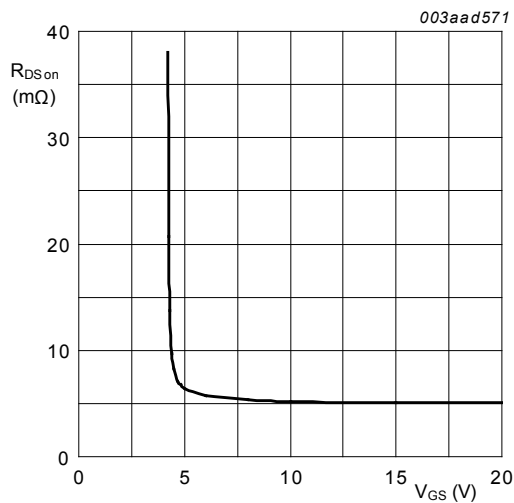


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

$T_J = 25\text{ }^{\circ}\text{C}; I_D = 15\text{ A}$

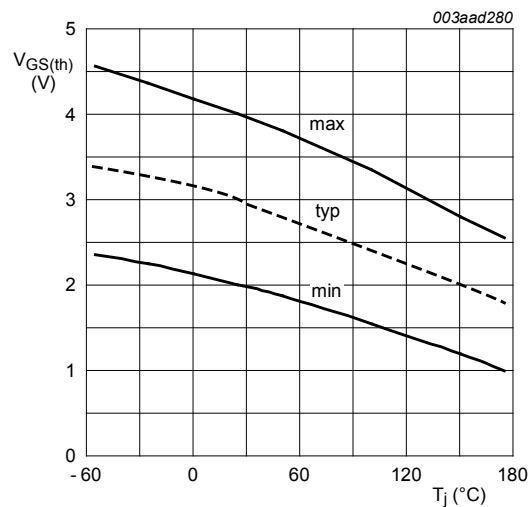


Fig. 10. Gate-source threshold voltage as a function of junction temperature

$I_D = 1\text{ mA}; V_{DS} = V_{GS}$

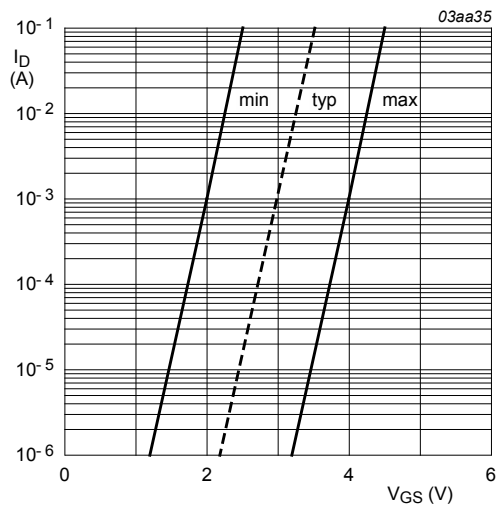


Fig. 11. Sub-threshold drain current as a function of gate-source voltage

$T_J = 25\text{ }^{\circ}\text{C}; V_{DS} = 5\text{ V}$

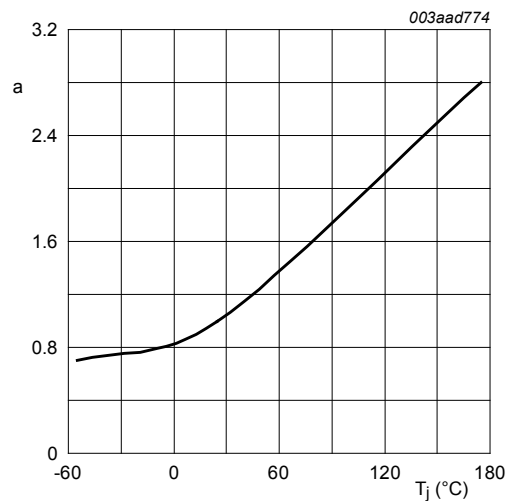


Fig. 12. Normalized drain-source on-state resistance factor as a function of junction temperature

$$a = \frac{R_{DS(on)}}{R_{DS(on)25\text{ }^{\circ}\text{C}}}$$

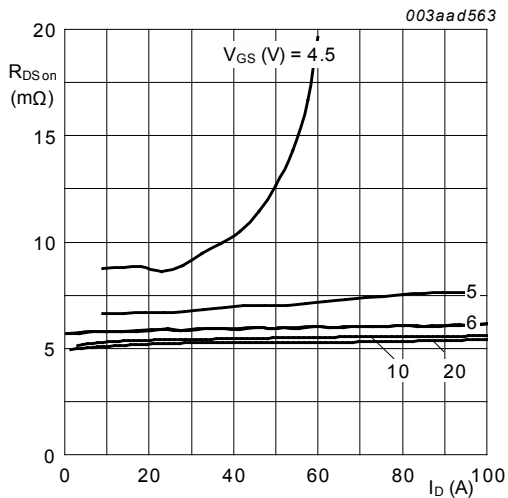


Fig. 13. Drain-source on-state resistance as a function of drain current; typical values

$T_j = 25^\circ\text{C}$

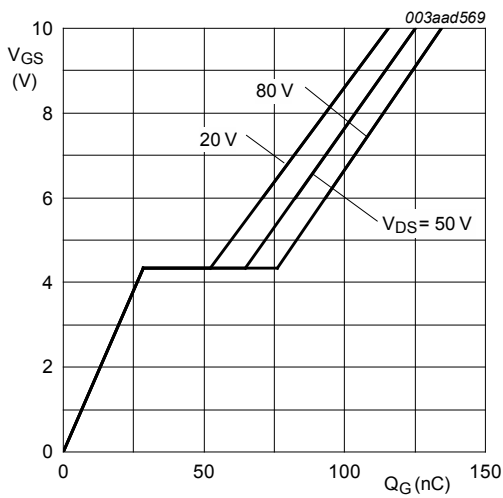


Fig. 14. Gate-source voltage as a function of gate charge; typical values

$T_j = 25^\circ\text{C}; I_D = 25\text{ A}$

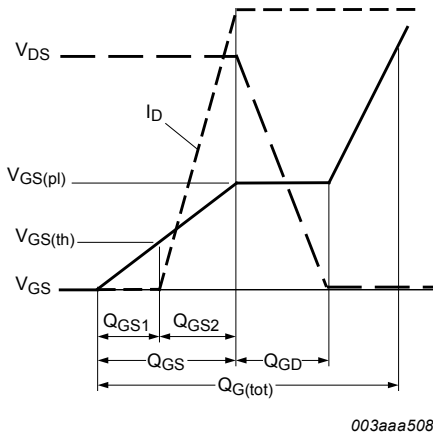


Fig. 15. Gate charge waveform definitions

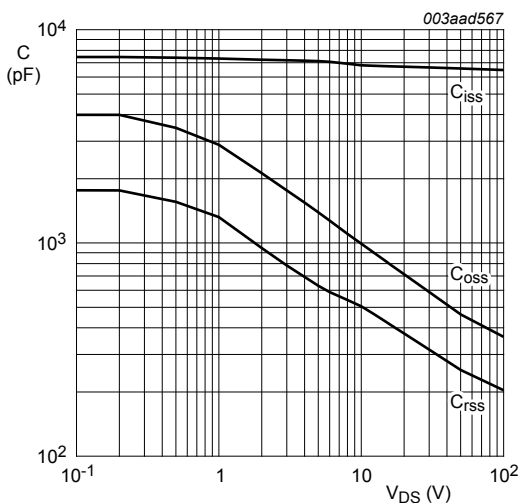


Fig. 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

$V_{GS} = 0\text{ V}; f = 1\text{ MHz}$

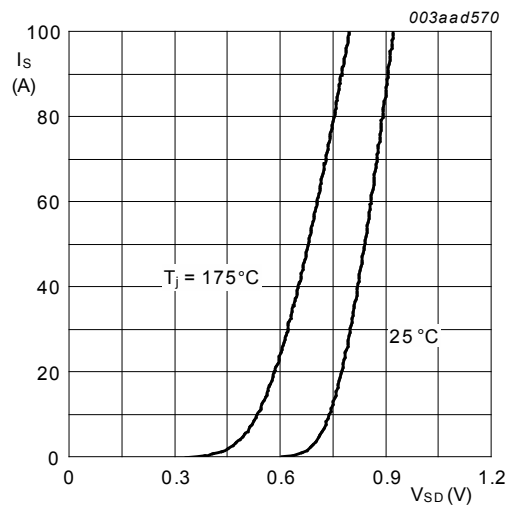


Fig. 17. Source (diode forward) current as a function of source-drain (diode forward) voltage; typical values

$V_{GS} = 0\text{ V}$

11. Package outline

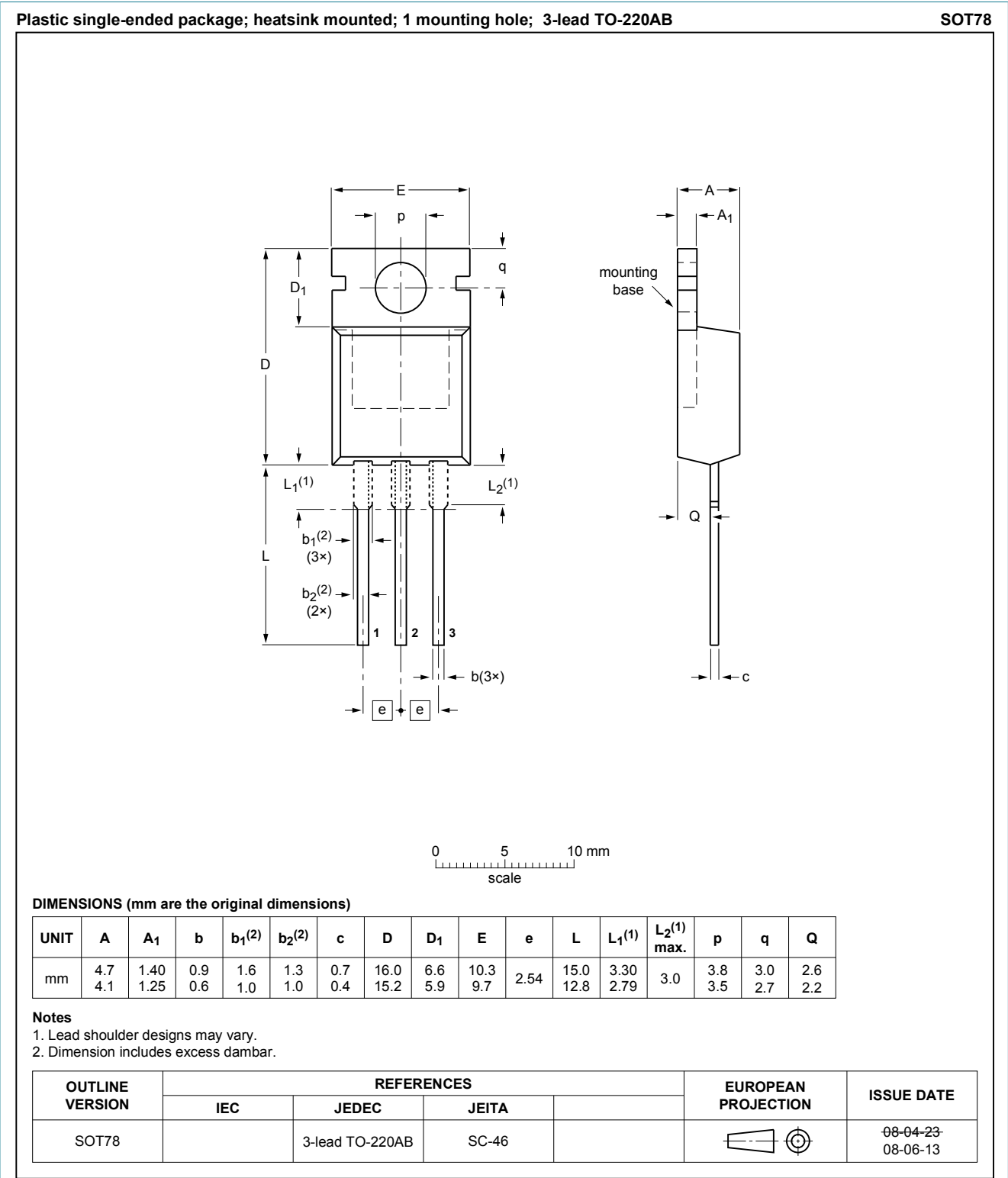


Fig. 18. Package outline TO-220AB (SOT78)

12. Legal information

12.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Date of release: 17 October 2013