

HD74AC153

Dual 4-Input Multiplexer

REJ03D0251-0200Z (Previous ADE-205-370 (Z)) Rev.2.00 Jul.16.2004

Description

The HD74AC153 is a high-speed dual 4-input multiplexer with common select inputs and individual enable inputs for each section. It can select two lines of data from four sources. The two buffered outputs present data in the true (noninverted) form. In addition to multiplexer operation, the HD74AC153 can act as a function generator and generate any two functions of three variables.

Features

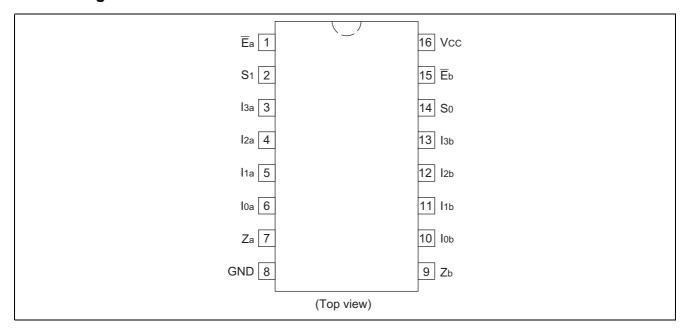
- Outputs Source/Sink 24 mA
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74AC153FPEL	SOP-16 pin (JEITA)	FP-16DAV	FP	EL (2,000 pcs/reel)
HD74AC153RPEL	SOP-16 pin (JEDEC)	FP-16DNV	RP	EL (2,500 pcs/reel)

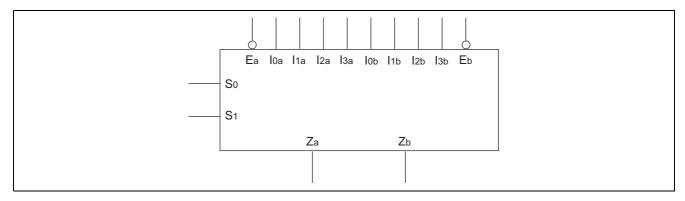
Notes: 1. Please consult the sales office for the above package availability.

2. The packages with lead-free pins are distinguished from the conventional products by adding V at the end of the package code.

Pin Arrangement



Logic Symbol



Pin Names

 $\begin{array}{lll} I_{0a} \text{ to } I_{3a} & \text{Side A Data Inputs} \\ I_{0b} \text{ to } I_{3b} & \text{Side B Data Inputs} \\ S_0, S_1 & \text{Common Select Inputs} \\ \overline{E}_a & \text{Side A Enable Input} \\ \overline{E}_b & \text{Side B Enable Input} \\ Z_a & \text{Side A Output} \\ Z_b & \text{Side B Output} \end{array}$

Functional Description

The HD74AC153 is a dual 4-input multiplexer. It can select two bits of data from up to four sources under the control of the common Select inputs (S_0, S_1) . The two 4-input multiplexer circuits have individual active-Low Enables $(\overline{E}_a, \overline{E}_b)$ which can be used to strobe the outputs independently. When the Enables $(\overline{E}_a, \overline{E}_b)$ are High, the corresponding outputs (Z_a, Z_b) are forced Low. The HD74AC153 is the logic implementation of a 2-pole, 4-position switch, where the position of the switch is determined by the logic levels supplied to the two Select inputs. The logic equations for the outputs are shown below.

$$\begin{split} Z_a &= \overline{E}_a \bullet (I_{0a} \bullet \overline{S}_1 \bullet \overline{S}_0 + I_{1a} \bullet \overline{S}_1 \bullet S_0 + I_{2a} \bullet S_1 \bullet \overline{S}_0 + I_{3a} \bullet S_1 \bullet S_0) \\ Z_b &= \overline{E}_b \bullet (I_{0b} \bullet \overline{S}_1 \bullet \overline{S}_0 + I_{1b} \bullet \overline{S}_1 \bullet S_0 + I_{2b} \bullet S_1 \bullet \overline{S}_0 + I_{3b} \bullet S_1 \bullet S_0) \end{split}$$

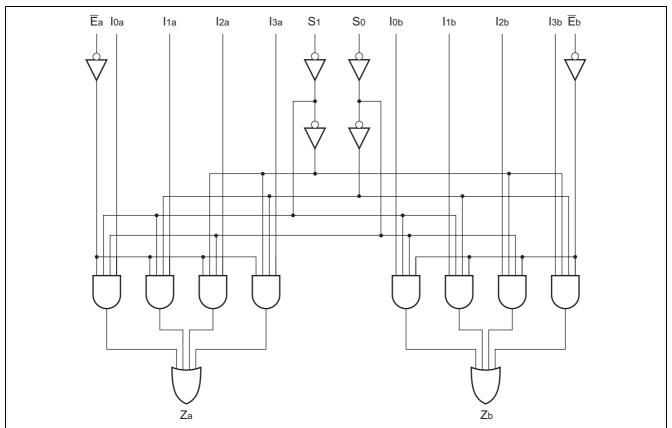
Truth Table

Selec	t Inputs		Output				
S ₀	S ₁	Ē	I _o	I ₁	l ₂	l ₃	Z
X	Х	Н	Х	Х	Х	Х	L
L	L	L	L	Х	Х	Х	L
L	L	L	Н	Х	Х	Х	Н
Н	L	L	Х	L	Х	Х	L
Н	L	L	Х	Н	Х	Х	Н
L	Н	L	Х	Х	L	Х	L
L	Н	L	Х	Х	Н	Х	Н
Н	Н	L	Х	Х	Х	L	L
Н	Н	L	Х	Х	Х	Н	Н

H: High Voltage LevelL: Low Voltage Level

X: Immaterial

Logic Diagram



Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

Absolute Maximum Ratings

ltem	Symbol	Ratings	Unit	Condition
Supply voltage	V _{CC}	-0.5 to 7	V	
DC input diode current	I _{IK}	-20	mA	$V_1 = -0.5V$
		20	mA	V _I = Vcc+0.5V
DC input voltage	V _I	-0.5 to Vcc+0.5	V	
DC output diode current	I _{OK}	-50	mA	$V_{O} = -0.5V$
		50	mA	$V_O = Vcc+0.5V$
DC output voltage	Vo	-0.5 to Vcc+0.5	V	
DC output source or sink current	I _o	±50	mA	
DC V _{CC} or ground current per output pin	I _{CC} , I _{GND}	±50	mA	
Storage temperature	Tstg	-65 to +150	°C	

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Condition
Supply voltage	V _{cc}	2 to 6	V	
Input and output voltage	V _I , V _O	0 to V _{CC}	V	
Operating temperature	Та	-40 to +85	°C	
Input rise and fall time	tr, tf	8	ns/V	V _{CC} = 3.0V
(except Schmitt inputs)				$V_{CC} = 4.5 \text{ V}$
V_{IN} 30% to 70% V_{CC}				V _{CC} = 5.5 V

DC Characteristics

Item	Sym- bol	Vcc (V)	7	Га = 25°(C	Ta = -40 to +85°C		Unit	Condition
			min.	typ.	max.	min.	max.		
Input Voltage	V _{IH}	3.0	2.1	1.5	_	2.1	_	٧	$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$
		4.5	3.15	2.25		3.15			
		5.5	3.85	2.75		3.85			
	V _{IL}	3.0	_	1.50	0.9	_	0.9		$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$
		4.5	_	2.25	1.35	_	1.35		
		5.5	_	2.75	1.65	_	1.65		
Output voltage	V_{OH}	3.0	2.9	2.99	_	2.9	_	٧	$V_{IN} = V_{IL}$ or V_{IH}
		4.5	4.4	4.49	_	4.4	_		$I_{OUT} = -50 \mu A$
		5.5	5.4	5.49	_	5.4	_		
		3.0	2.58	_	_	2.48	_		$V_{IN} = V_{IL} \text{ or } V_{IH}$ $I_{OH} = -12 \text{ mA}$
		4.5	3.94	_	_	3.80	_		$I_{OH} = -24 \text{ mA}$
		5.5	4.94	_	_	4.80	_		$I_{OH} = -24 \text{ mA}$
	V _{OL}	3.0	_	0.002	0.1	_	0.1		$V_{IN} = V_{IL}$ or V_{IH}
		4.5	_	0.001	0.1	_	0.1		$I_{OUT} = 50 \mu A$
		5.5	_	0.001	0.1	_	0.1		
		3.0	_	_	0.32	_	0.37		$V_{IN} = V_{IL} \text{ or } V_{IH}$ $I_{OL} = 12 \text{ mA}$
		4.5	_	_	0.32	_	0.37		$I_{OL} = 24 \text{ mA}$
		5.5	_	_	0.32		0.37		$I_{OL} = 24 \text{ mA}$
Input leakage current	I _{IN}	5.5	_	_	±0.1	_	±1.0	μΑ	$V_{IN} = V_{CC}$ or GND
Dynamic output	I _{OLD}	5.5	_	_		86	_	mA	V _{OLD} = 1.1 V
current*	I _{OHD}	5.5		_	_	-75	_	mA	V _{OHD} = 3.85 V
Quiescent supply current	I _{CC}	5.5	_	_	8.0	_	80	μА	$V_{IN} = V_{CC}$ or ground

^{*}Maximum test duration 2.0 ms, one output loaded at a time.

AC Characteristics: HD74AC153

			Ta = +25°C C _L = 50 pF			Ta = -40°C to +85°C $C_L = 50 \text{ pF}$		
Item	Symbol	V _{cc} (V)*1	Min	Тур	Max	Min	Max	Unit
Propagation delay	t _{PLH}	3.3	1.0	9.5	15.0	1.0	17.5	ns
S_n to Z_n		5.0	1.0	6.5	11.0	1.0	12.5	
Propagation delay	t _{PHL}	3.3	1.0	8.5	14.5	1.0	16.5	ns
S_n to Z_n		5.0	1.0	6.5	11.0	1.0	12.0	
Propagation delay	t _{PLH}	3.3	1.0	8.0	13.5	1.0	16.0	ns
\overline{E}_n to Z_n		5.0	1.0	5.5	9.5	1.0	11.0	
Propagation delay	t _{PHL}	3.3	1.0	7.0	11.0	1.0	12.5	ns
\overline{E}_n to Z_n		5.0	1.0	5.0	8.0	1.0	9.0	
Propagation delay	t _{PLH}	3.3	1.0	7.5	12.5	1.0	14.5	ns
I_n to Z_n		5.0	1.0	5.5	9.0	1.0	10.5	
Propagation delay	t _{PHL}	3.3	1.0	7.0	11.5	1.0	13.0	ns
I_n to Z_n		5.0	1.0	5.0	8.5	1.0	10.0	

Note: 1. Voltage Range 3.3 is $3.3 \text{ V} \pm 0.3 \text{ V}$ Voltage Range 5.0 is $5.0 \text{ V} \pm 0.5 \text{ V}$

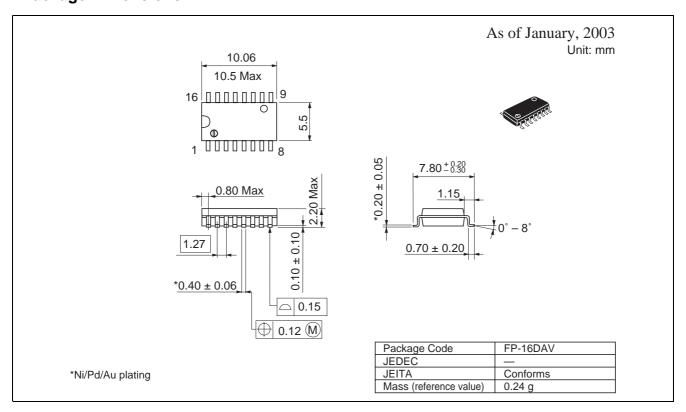


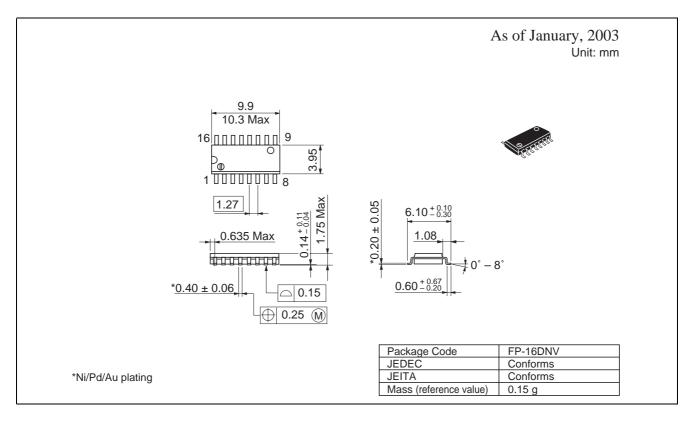
HD74AC153

Capacitance

Item	Symbol	Тур	Unit	Condition
Input capacitance	C _{IN}	4.5	pF	V _{CC} = 5.5 V
Power dissipation capacitance	C_{PD}	65.0	pF	$V_{CC} = 5.0 \text{ V}$

Package Dimensions





Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Keep safety first in your circuit designs!

1. Renesas Technology Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.

Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. or a third party.

2. Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corp. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.

- therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.

 The information described here may contain technical inaccuracies or typographical errors.

 Renesas Technology Corp. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.

 Please also pay attention to information published by Renesas Technology Corp. by various means, including the Renesas Technology Corp. Semiconductor home page (http://www.renesas.com).

 4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corp. assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.

 5. Renesas Technology Corp. semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
- use.
 6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials.
 7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.
 Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
 8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.



RENESAS SALES OFFICES

http://www.renesas.com

Renesas Technology America, Inc. 450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500 Fax: <1> (408) 382-7501

Renesas Technology Europe Limited.

Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, United Kingdom Tel: <44> (1628) 585 100, Fax: <44> (1628) 585 900

Renesas Technology Europe GmbHDornacher Str. 3, D-85622 Feldkirchen, Germany
Tel: <49> (89) 380 70 0, Fax: <49> (89) 929 30 11

Renesas Technology Hong Kong Ltd. 7/F., North Tower, World Finance Centre, Harbour City, Canton Road, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2375-6836

Renesas Technology Taiwan Co., Ltd. FL 10, #99, Fu-Hsing N. Rd., Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd. 26/F., Ruijin Building, No.205 Maoming Road (S), Shanghai 200020, China Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.
1, Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001