

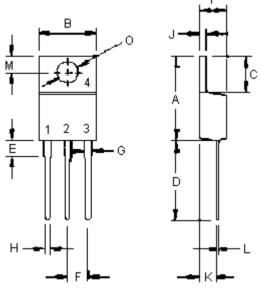
Features:

- Complementary Silicon Plastic
- Collector emitter sustaining voltage - $V_{CEO (sus)} = 100 \text{ V (minimum)} - TIP41C$
- Collector emitter saturation voltage $V_{CE (sat)} = 1.5 \text{ V (maximum)}$ at $I_{C} = 6 \text{ A}$ Current gain bandwidth product $f_{T} = 3 \text{ MHz (minimum)}$ at $I_{C} = 500 \text{ mA}$

Application:

Designed for use in general purpose power amplifier and switching applications





Dimensions -	Millim	netres	
Dimensions	Minimum	Maximum	
Α	14.68	15.31	
В	9.78	10.42	
С	5.01	6.52	
D	13.06	14.62	
Е	3.57	4.07	
F	2.42	3.66	
G	1.12	1.36	
Н	0.72	0.96	
I	4.22	4.98	
J	1.14	1.38	
K	2.2	2.97	
L	0.33	0.55	
М	2.48	2.98	
0	3.7	3.9	

Pin

- 1. Base
- 2. Collector
- 3. Emitter
- 4. Collector (Case)

Maximum Ratings

Characteristics	Symbol	TIP41C	Unit
Collector - emitter voltage	V _{CEO}	100	V
Collector - base voltage	V _{CBO}	100	V
Emitter - base voltage	V _{EBO}	5	V
Collector current - Continuous - Peak	I _C	6 10	А
Base current	I _B	2	А
Total power dissipation at T _c = 25°C derate above 25°C	P _D	65 0.52	W/°C
Operating and storage Junction temperature range	T _{J,} T _{STG}	-65 to +150	°C

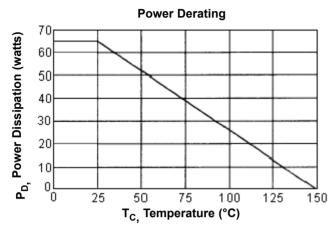
www.element14.com www.farnell.com www.newark.com





Thermal Characteristics

Characteristic	Symbol	Maximum	Unit	
Thermal resistance junction to case	$R_{ heta jc}$	1.92	°C/W	



Electrical Characteristics (T_c = 25°C Unless Otherwise noted)

Characteristics	Symbol	Minimum	Maximum	Units
Off Characteristics				
Collector - emitter sustaining voltage (1) $(I_C = 30 \text{ mA}, I_B = 0)$	V _{CEO (SUS)}	100	-	V
Collector cut off current (V _{CE} = 60 V, I _B = 0)	I _{CEO}	-	0.7	mA
Collector cut off current (V _{CE} = 100 V, V _{BE} = 0)	I _{CES}	-	0.4	mA
Emitter cut off current (V _{EB} = 5 V, I _C = 0)	I _{EBO}	-	1	mA
On Characteristics (1)				
DC current gain $(I_C = 0.3 \text{ A}; V_{CE} = 4 \text{ V})$ $(I_C = 3 \text{ A}; V_{CE} = 4 \text{ V})$	h _{FE}	30 15	75	-
Collector - emitter saturation voltage ($I_C = 6 \text{ A}$; $I_B = 600 \text{ mA}$)	V _{CE (sat)}	-	1.5	V
Base-emitter on voltage (I _C = 6 A; V _{CE} = 4 V)	V _{BE (on)}	-	2	V
Dynamic characteristics				
Current gain-bandwidth Product (2) ($I_C = 500 \text{ mA}$; $V_{CE} = 10 \text{ V}$, $f_{TEST} = 1 \text{ MHz}$)	f _⊤	3	-	MHz
Small signal current gain ($I_C = 500 \text{ mA}$; $V_{CE} = 10 \text{ V}$, $f = 1 \text{ kHz}$)	h _{fe}	20	-	-

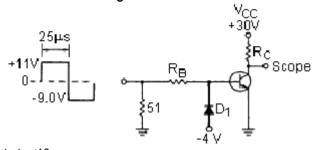
⁽¹⁾ Pulse test: Pulse width \leq 300 µs, duty cycle \leq 2%



⁽²⁾ $f_T = |h_{fe}| \cdot f_{TEST}$



Switching Time Test Circuit



 t_r , $t_f \le 10 \text{ ns}$ Duty cycle = 1 %

 $R_{\rm B}$ and $R_{\rm C}$ varied to obtain desired current levels $D_{\rm 1}$ must be fast recovery type eg: MBD5300 Used Above I_B to 100 mA MSD6100 Used Below I_B to 100 mA



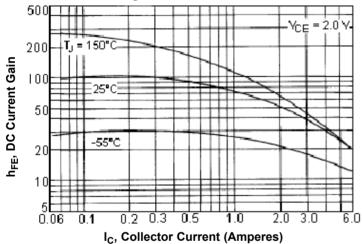
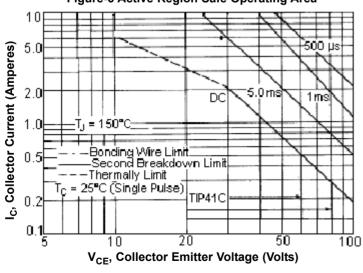
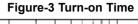


Figure-6 Active Region Safe Operating Area





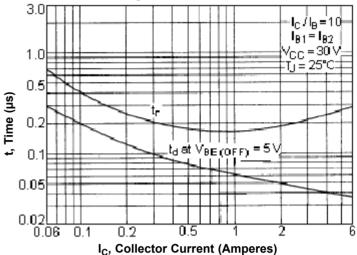
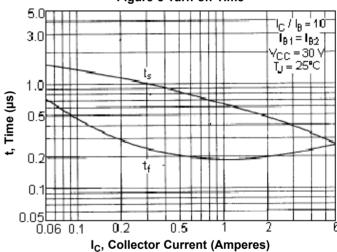


Figure-5 Turn-off Time

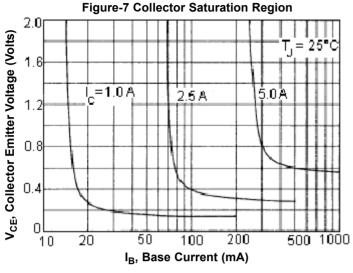


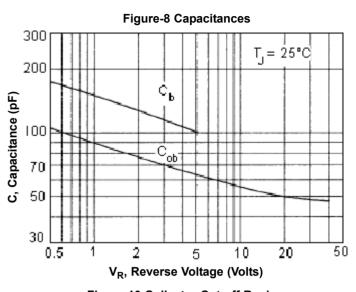
There are two limitation on the power handling ability of a transistor: average junction temperature and second breakdown safe operating area curves indicate $I_{\text{C}}\text{-}V_{\text{CE}}$ limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than curves indicate

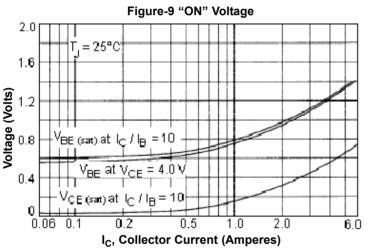
The data of FIG-6 curve is base on $T_{j~(pk)}$ =150 °C; T_{c} is variable depending on power level, second breakdown pulse limits are valid for duty cycles to 10% provided $T_{j~(pk)} \le 150$ °C, At high case temperatures, thermal limitation will reduce the power that can be handled to values less than the limitations imposed by second breakdown

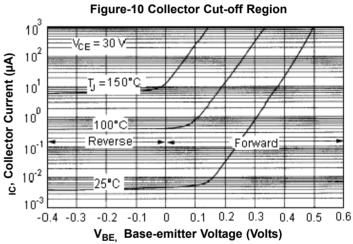












Part Number Table

Description	Part Number	
Silicon Plastic Power Transistor, NPN	TIP41C	

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