

FPM MODE DRAM MODULE

8M Bytes (2M x 32 bits)
FPM Mode Unbuffered 72 Pin SIMM
 based on 4 pcs 1M x 16 DRAM with LVTTL, 1K Refresh

GENERAL DESCRIPTION

The UG4M23201PTCT-6AT is a 2Mbits x 32 SIMM module. The UG4M23201PTCT-6AT is assembled using 4 pcs of 1M x 16 1K refresh DRAMs mounted on a 72-Pin printed circuit board.

FEATURES

- Single 5.0V $\pm$ 10% power supply
- Fast Page Module (FPM) capability
- Low power dissipation
- RAS only refresh cycles
- Hidden refresh cycles
- 1024 refresh cycles every 16 ms
- CAS-before-RAS Refresh capability
- TTL compatible inputs and output
- JEDEC standard PD Pin & pinout
- PCB:Height (1000mil),double sided component

ABSOLUTE MAXIMUM RATINGS

- Voltage Relative to GND -1.0 to + 7.0V
- Operating Temperature 0 to + 70°C
- Storage Temperature -55°C to + 150°C
- Short circuit Output Current 50mA
- Power Dissipation 4W

PERFORMANCE RANGE

Speed	tRAC	tCAC	tRC
-6AT	60ns	17ns	110ns

Part Identification

Part Number	Plating	Package	Ref.Cycle
UG4M23201PTCT	Tin	TSOP	1K

REVISION HISTORY

Sep 21, 2009

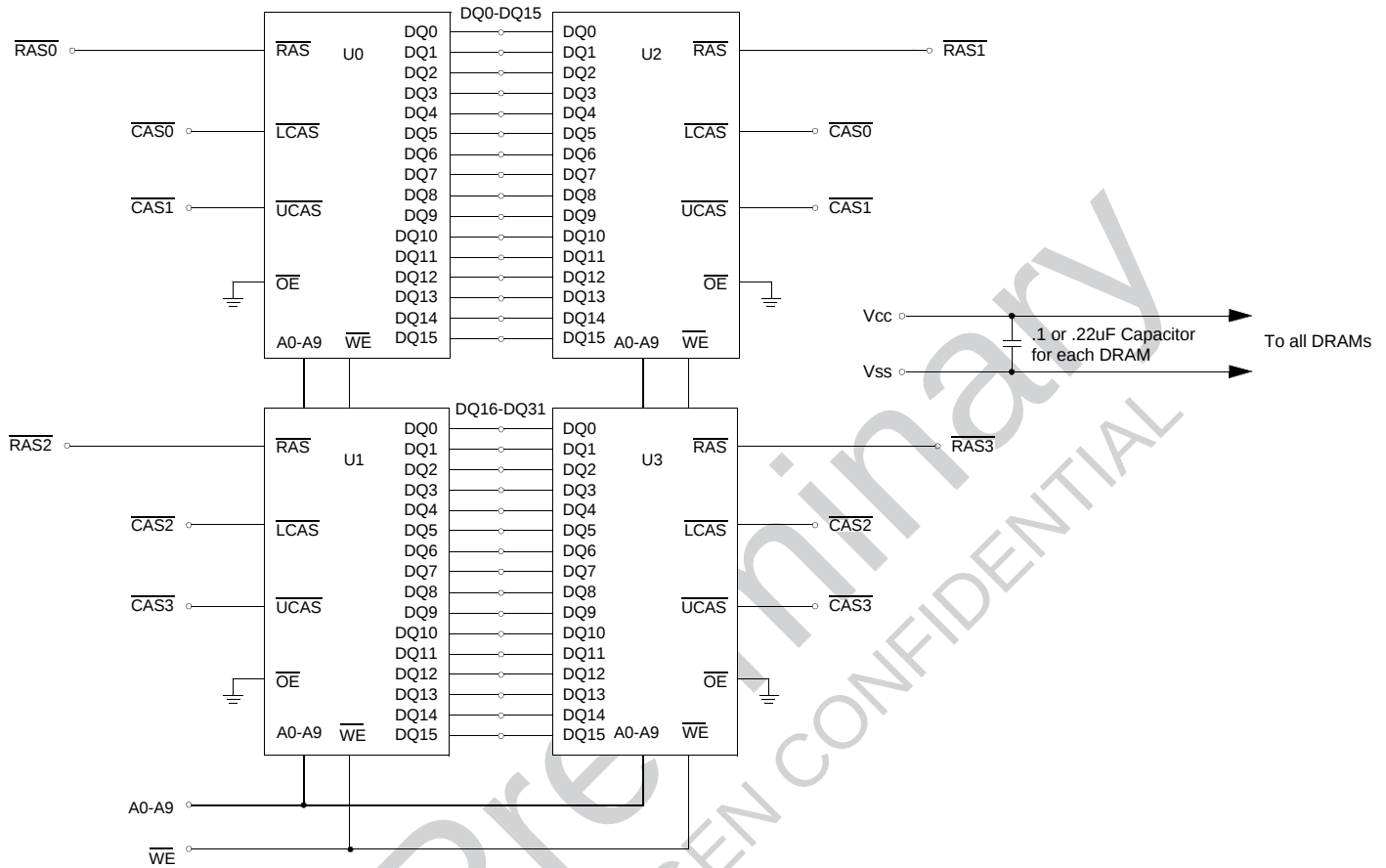
Rev - A Product brief released.

PIN ASSIGNMENT (Front View) 72-Pin SIMM

PIN	SYMBOL	PIN	SYMBOL	PIN	SYMBOL
1	VSS	25	DQ24	49	DQ9
2	DQ0	26	DQ7	50	DQ27
3	DQ18	27	DQ25	51	DQ10
4	DQ1	28	A7	52	DQ28
5	DQ19	29	NC	53	DQ11
6	DQ2	30	VCC	54	DQ29
7	DQ20	31	A8	55	DQ12
8	DQ3	32	A9	56	DQ30
9	DQ21	33	/RAS3	57	DQ13
10	VCC	34	/RAS2	58	DQ31
11	PD5	35	NC	59	VCC
12	A0	36	NC	60	DQ32
13	A1	37	NC	61	DQ14
14	A2	38	NC	62	DQ33
15	A3	39	VSS	63	DQ15
16	A4	40	/CAS0	64	DQ34
17	A5	41	/CAS2	65	DQ16
18	A6	42	/CAS3	66	NC
19	NC	43	/CAS1	67	PD1
20	DQ4	44	/RAS0	68	PD2
21	DQ22	45	/RAS1	69	PD3
22	DQ5	46	NC	70	PD4
23	DQ23	47	/WE	71	NC
24	DQ6	48	NC	72	VSS

NOTE: Pin symbols in parentheses are not used on these modules but may be used for other modules in this product family. They are for reference only.

Functional Block Diagram



Physical Dimension

