



English | 简体中文 | 日本語

Search Entire Site



Contact IDT | Investors | Press

About IDT

Products

Applications

Support

myIDT

Document Search | Package Search | Parametric Search | Cross Reference Search | Green & RoHS | Calculators | Thermal Data | Reliability & Quality | Military

Home > Products > Clock / Timing Devices > PC-Notebook-Server Clocks > Clock Synthesizer by Chipset Vendor > Desktop Chipsets > 952909 > 952909BKLFT

Add to myIDT [?]

You may also like...

952909BKLFT

Category: Desktop Chipsets

Generic Part: 952909

Market Group: PC CLOCK

Description: PC MAIN CLOCK
ICS952909 is a 56 pin clock chip for VIA PT880/PM880 style chipsets. When used with a fanout DDR buffer, such as the 93788, it provides all the necessary clock signals for such a system. Output Features: â€¢ 3 - 0.7V current-mode differential CPU pairs â€¢ 10 - PCI, 3 free running, 33MHz â€¢ 2 - REF, 14.318MHz â€¢ 3 - 3V66, 66.66MHz â€¢ 1 - 48MHz â€¢ 1 - 24/48MHz â€¢ 2 - 25MHz @ 2.5V

Time is money

Parameters

Package	VFQFPN 56 (NLG56)
Voltage	3.3 V
Package	VFQFPN 56
Speed	NA
Temperature	C
Status	Active
Sample	No
Minimum Order Quantity	1000
Factory Order Increment	1000

Distributor Inventory

No Pricing information is available from our Distributors at this time.

Documents

Type	Title	Size	Revision Date
Misc	PC Clocks Contact Info	61 KB	05/29/2007
Product Delete Notice	PDN# : K-07-06 To discontinue 952909BKLFT and 952909BKLFT and replaced with 952909CKLF and 952909CKLFT	47 KB	12/17/2007

Package

Description	VFQFP-N 8.0 X 8.0 X 0.9 MM - NO LEAD
Class	PLASTIC
Moisture Sensitivity Level (MSL)	3
Category	Green
Moisture Exposure Floor Life	168 hrs. @ <30°C/60%RH
Peak Reflow Temperature	260°C
Rebake Conditions	48 hrs.@125°C
Length	8.0
Mark	K
Width	8.0
Pitch	0.5
Thickness	0.85
Status	Active

Home | Site Map | Legal | Careers | Contact Us | Quality | RSS What is this?

Use of this website signifies your agreement to the acceptable use and privacy policy. Copyright © 1997-2008 Integrated Device Technology, Inc. All Rights Reserved.