# CM1753

# 1-Channel ESD Protector

### **Product Description**

The CM1753 provides robust ESD protection for sensitive parts that may be subjected to electrostatic discharge (ESD). The tiny form factor and single wirebond requirement enable it to be used in very confined spaces. This device is designed and characterized to safely dissipate ESD strikes of at least  $\pm 8$  kV, according to the MIL-STD-883 (Method 3015) specification for Human Body Model (HBM) ESD.

#### **Features**

- Compact Die Protects from ESD Discharges
- Almost No Conduction at Signal Amplitudes Smaller than -45 V
- ESD Protection Over ±8 kV Contact Discharge per MIL\_STD\_883 International ESD Standard

### **Applications**

- LED Lighting
- Modules
- Interface Circuits

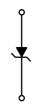


# ON Semiconductor®

http://onsemi.com

### **ELECTRICAL SCHEMATIC**

Au (Gold) bondpad on topside ("Signal" node mentioned in Electrical Specification table)



Bare Silicon on backside (Reference node)

### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

## CM1753

### **ORDERING INFORMATION**

Ordering Part Number	Topside Metal	Backside Metal	BG Thickness	Shipping Method
CM1753-1004YT	Au (Gold)	Bare Silicon	4 mils	Wafer Form

### **OPERATING CONDITIONS**

Parameter	Rating	Unit
Operating Temperature Range	-40 to +125	°C
Storage Temperature Range	-65 to +150	°C

## **ELECTRICAL OPERATING CHARACTERISTICS**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>LEAK</sub>	Leakage Current	V = −35 V, 25°C			100	nA
		V = -45 V, 25°C			500	nA
V <sub>CL</sub>	Signal Clamp Voltage Positive polarity on signal node ( $V_{CL+}$ ) Negative polarity on signal node ( $V_{CL-}$ )	$T_A = 25^{\circ}C;$ at 10 mA ( $I_{CL+}$ ) at -10 mA ( $I_{CL-}$ ) (Note 1)	0.4 -57.0	0.8 -52.0	1.5 -47.0	٧
V <sub>ESD</sub>	ESD Protection – withstand voltage: Human Body Model (MIL-STD-883, Method 3015)	T <sub>A</sub> = 25°C (Note 2)	±8			kV

V<sub>CL</sub> is measured with a –10 mA pulse at 1 ms.
This parameter is guaranteed by design.

#### CM1753

### **MECHANICAL DETAILS**

### **MECHANICAL SPECIFICATIONS**

Parameter	Condition	Unit
Composition	Silicon wafer, n+ doped	
Wafer Diameter	150	mm
Die shape	Square	
Length (Stepping Size)	270	μm
Width (Stepping Size)	270	μm
Thickness	100	μm
Top Pad Length	190	μm
Top Pad Width	190	μm
Top Pad Composition	Au (Gold)	
Back Metal (Backside)	None (Bare Silicon)	
Die (Stepping Size)	270	μm
Passivation Opening	60	μm
Active Size	160	μm
Active to PA Opening	25	μm

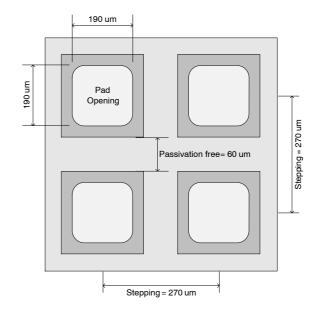


Figure 1. Wafer Array

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada Fax: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center

Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative