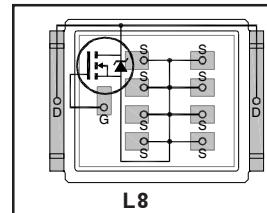


- Advanced Process Technology
- Optimized for Automotive Motor Drive, DC-DC and other Heavy Load Applications
- Exceptionally Small Footprint and Low Profile
- High Power Density
- Low Parasitic Parameters
- Dual Sided Cooling
- 175°C Operating Temperature
- Repetitive Avalanche Capability for Robustness and Reliability
- Lead Free, RoHS Compliant and Halogen Free
- Automotive Qualified *

Automotive DirectFET® Power MOSFET ②

$V_{(BR)DSS}$	75V
$R_{DS(on)}$ typ.	1.8mΩ
	max. 2.3mΩ
I_D (Silicon Limited)	160A
Q_g	200nC



DirectFET® ISOMETRIC

Applicable DirectFET® Outline and Substrate Outline ①

SB	SC		M2	M4		L4	L6	L8	
----	----	--	----	----	--	----	----	----	--

Description

The AUIRF7759L2TR(1) combines the latest Automotive HEXFET® Power MOSFET Silicon technology with the advanced DirectFET® packaging to achieve the lowest on-state resistance in a package that has the footprint of a DPak (TO-252AA) and only 0.7 mm profile. The DirectFET® package is compatible with existing layout geometries used in power applications, PCB assembly equipment and vapor phase, infra-red or convection soldering techniques, when application note AN-1035 is followed regarding the manufacturing methods and processes. The DirectFET® package allows dual sided cooling to maximize thermal transfer in automotive power systems.

This HEXFET® Power MOSFET is designed for applications where efficiency and power density are essential. The advanced DirectFET® packaging platform coupled with the latest silicon technology allows the AUIRF7759L2TR(1) to offer substantial system level savings and performance improvement specifically in motor drive, high frequency DC-DC and other heavy load applications on ICE, HEV and EV platforms. This MOSFET utilizes the latest processing techniques to achieve low on-resistance and low Q_g per silicon area. Additional features of this MOSFET are 175°C operating junction temperature and high repetitive peak current capability. These features combine to make this MOSFET a highly efficient, robust and reliable device for high current automotive applications.

	Parameter	Max.	Units
V_{DS}	Drain-to-Source Voltage	75	V
V_{GS}	Gate-to-Source Voltage	±20	
I_D @ $T_C = 25^\circ\text{C}$	Continuous Drain Current, V_{GS} @ 10V (Silicon Limited) ④	160	
I_D @ $T_C = 100^\circ\text{C}$	Continuous Drain Current, V_{GS} @ 10V (Silicon Limited) ④	113	
I_D @ $T_A = 25^\circ\text{C}$	Continuous Drain Current, V_{GS} @ 10V (Silicon Limited) ③	26	A
I_D @ $T_C = 25^\circ\text{C}$	Continuous Drain Current, V_{GS} @ 10V (Package Limited) ④	375	
I_{DM}	Pulsed Drain Current ⑤	640	
P_D @ $T_C = 25^\circ\text{C}$	Power Dissipation ④	125	
P_D @ $T_C = 100^\circ\text{C}$	Power Dissipation ④	63	
P_D @ $T_A = 25^\circ\text{C}$	Power Dissipation ①	3.3	
E_{AS}	Single Pulse Avalanche Energy ⑥	257	mJ
I_{AR}	Avalanche Current ⑤	See Fig.18a, 18b, 16, 17	A
E_{AR}	Repetitive Avalanche Energy ⑤		mJ
T_P	Peak Soldering Temperature	270	
T_J	Operating Junction and	-55 to + 175	°C
T_{STG}	Storage Temperature Range		

Thermal Resistance

	Parameter	Typ.	Max.	Units
R_{0JA}	Junction-to-Ambient ③	—	45	
R_{0JA}	Junction-to-Ambient ⑧	12.5	—	
R_{0JA}	Junction-to-Ambient ⑨	20	—	°C/W
R_{0J-Can}	Junction-to-Can ④⑩	—	1.2	
R_{0J-PCB}	Junction-to-PCB Mounted	—	0.5	
	Linear Derating Factor ④	0.83		W/°C

HEXFET® is a registered trademark of International Rectifier.

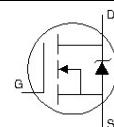
Static Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)

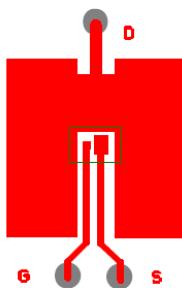
	Parameter	Min.	Typ.	Max.	Units	Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	75	—	—	V	$V_{\text{GS}} = 0\text{V}$, $I_D = 250\mu\text{A}$
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.02	—	V/ $^\circ\text{C}$	Reference to 25°C , $I_D = 2\text{mA}$
$R_{\text{DS(on)}}$	Static Drain-to-Source On-Resistance	—	1.8	2.3	$\text{m}\Omega$	$V_{\text{GS}} = 10\text{V}$, $I_D = 96\text{A}$ ⑦
$V_{\text{GS(th)}}$	Gate Threshold Voltage	2.0	3.0	4.0	V	
$\Delta V_{\text{GS(th)}}/\Delta T_J$	Gate Threshold Voltage Coefficient	—	-11	—	$\text{mV}/^\circ\text{C}$	
g_{fs}	Forward Transconductance	74	—	—	S	$V_{\text{DS}} = 25\text{V}$, $I_D = 96\text{A}$
I_{DSS}	Drain-to-Source Leakage Current	—	—	20	μA	$V_{\text{DS}} = 75\text{V}$, $V_{\text{GS}} = 0\text{V}$
		—	—	250		$V_{\text{DS}} = 60\text{V}$, $V_{\text{GS}} = 0\text{V}$, $T_J = 125^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage	—	—	100	nA	$V_{\text{GS}} = 20\text{V}$
	Gate-to-Source Reverse Leakage	—	—	-100		$V_{\text{GS}} = -20\text{V}$

Dynamic Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)

Q_g	Total Gate Charge	—	200	300	nC	
$Q_{\text{gs}1}$	Pre-V _{th} Gate-to-Source Charge	—	37	—		$V_{\text{DS}} = 38\text{V}$
$Q_{\text{gs}2}$	Post-V _{th} Gate-to-Source Charge	—	11	—		$V_{\text{GS}} = 10\text{V}$
Q_{gd}	Gate-to-Drain Charge	—	62	93		$I_D = 96\text{A}$
Q_{godr}	Gate Charge Overdrive	—	91	—		See Fig. 9
Q_{sw}	Switch Charge ($Q_{\text{gs}2} + Q_{\text{gd}}$)	—	73	—		
Q_{oss}	Output Charge	—	60	—	nC	$V_{\text{DS}} = 16\text{V}$, $V_{\text{GS}} = 0\text{V}$
R_G	Gate Resistance	—	1.1	—	Ω	
$t_{\text{d(on)}}$	Turn-On Delay Time	—	18	—	ns	$V_{\text{DD}} = 38\text{V}$, $V_{\text{GS}} = 10\text{V}$ ⑦
t_r	Rise Time	—	37	—		$I_D = 96\text{A}$
$t_{\text{d(off)}}$	Turn-Off Delay Time	—	80	—		$R_G = 1.8\Omega$
t_f	Fall Time	—	33	—		
C_{iss}	Input Capacitance	—	12222	—	pF	$V_{\text{GS}} = 0\text{V}$
C_{oss}	Output Capacitance	—	1465	—		$V_{\text{DS}} = 25\text{V}$
C_{rss}	Reverse Transfer Capacitance	—	609	—		$f = 1.0\text{MHz}$
C_{oss}	Output Capacitance	—	7457	—		$V_{\text{GS}} = 0\text{V}$, $V_{\text{DS}} = 1.0\text{V}$, $f = 1.0\text{MHz}$
C_{oss}	Output Capacitance	—	955	—		$V_{\text{GS}} = 0\text{V}$, $V_{\text{DS}} = 60\text{V}$, $f = 1.0\text{MHz}$

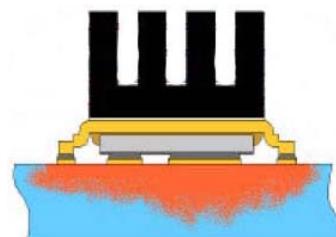
Diode Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)

	Parameter	Min.	Typ.	Max.	Units	Conditions
I_s	Continuous Source Current (Body Diode)	—	—	160	A	MOSFET symbol showing the integral reverse p-n junction diode.
I_{SM}	Pulsed Source Current (Body Diode) ⑤	—	—	640		
V_{SD}	Diode Forward Voltage	—	—	1.3	V	$T_J = 25^\circ\text{C}$, $I_s = 96\text{A}$, $V_{\text{GS}} = 0\text{V}$ ⑦
t_{rr}	Reverse Recovery Time	—	64	96	ns	$T_J = 25^\circ\text{C}$, $I_F = 96\text{A}$, $V_{\text{DD}} = 38\text{V}$
Q_{rr}	Reverse Recovery Charge	—	150	225	nC	$di/dt = 100\text{A}/\mu\text{s}$ ⑦

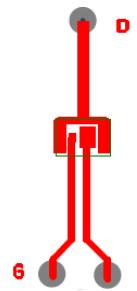


⑤ Surface mounted on 1 in. square Cu (still air).

Notes ① through ⑩ are on page 10



⑤ Mounted to a PCB with small clip heatsink (still air)



⑤ Mounted on minimum footprint full size board with metallized back and with small clip heatsink (still air)

Qualification Information[†]

		Automotive (per AEC-Q101) ^{††}	
Qualification Level		Comments: This part number(s) passed Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.	
Moisture Sensitivity Level		LARGE-CAN	MSL1
ESD	Machine Model	Class M4 (+/- 800V) (per AEC-Q101-002)	
	Human Body Model	Class H2 (+/- 6000V) (per AEC-Q101-001)	
	Charged Device Model	N/A (per AEC-Q101-005)	
RoHS Compliant		Yes	

[†] Qualification standards can be found at International Rectifier's web site: <http://www.irf.com/>

^{††} Exceptions (if any) to AEC-Q101 requirements are noted in the qualification report.

AUIRF7759L2TR/TR1

International
Rectifier

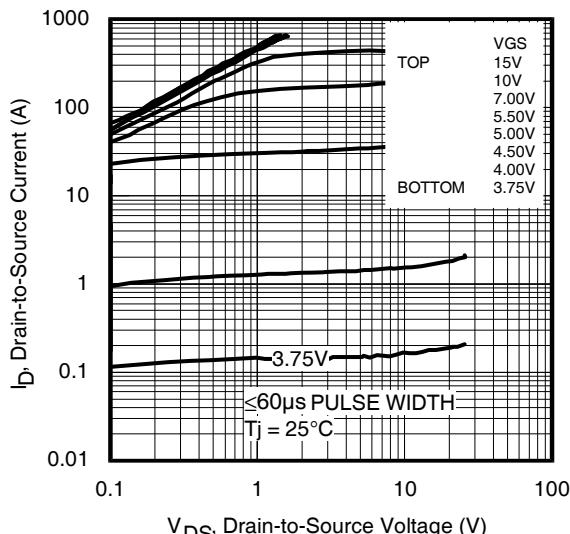


Fig 1. Typical Output Characteristics

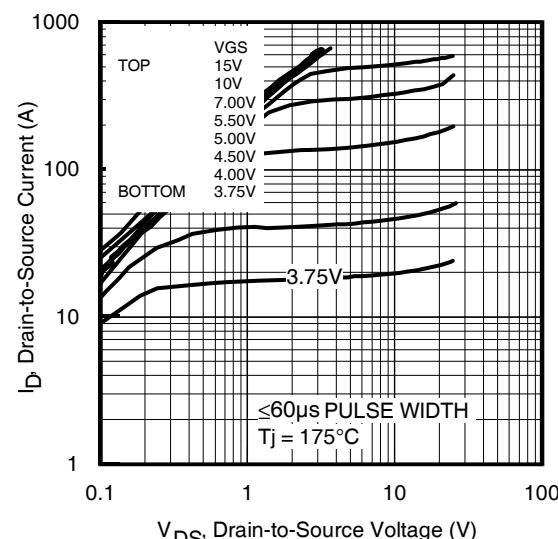


Fig 2. Typical Output Characteristics

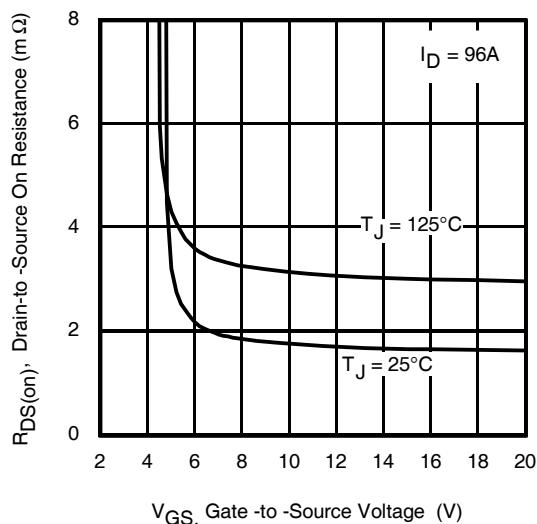


Fig 3. Typical On-Resistance vs. Gate Voltage

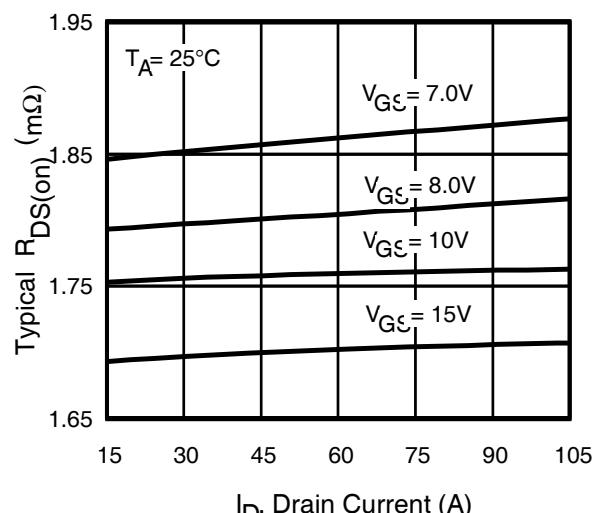


Fig 4. Typical On-Resistance vs. Drain Current

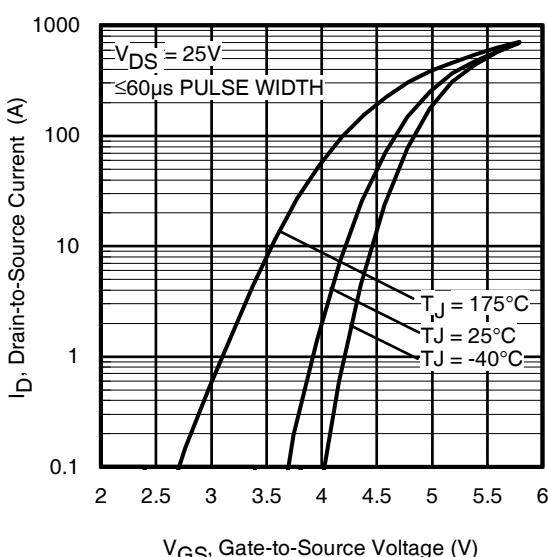


Fig 5. Typical Transfer Characteristics

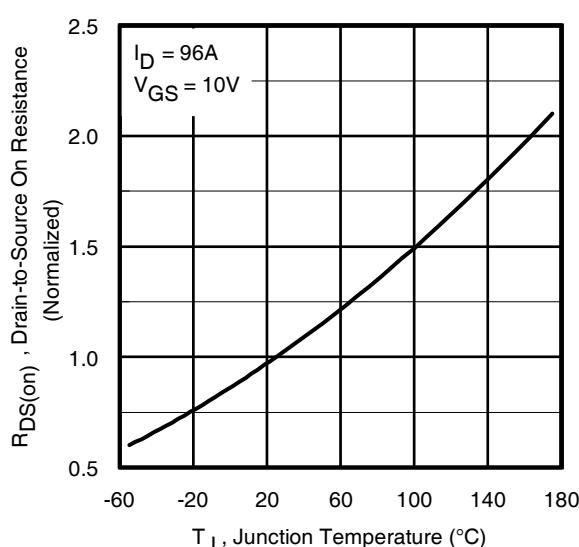


Fig 6. Normalized On-Resistance vs. Temperature

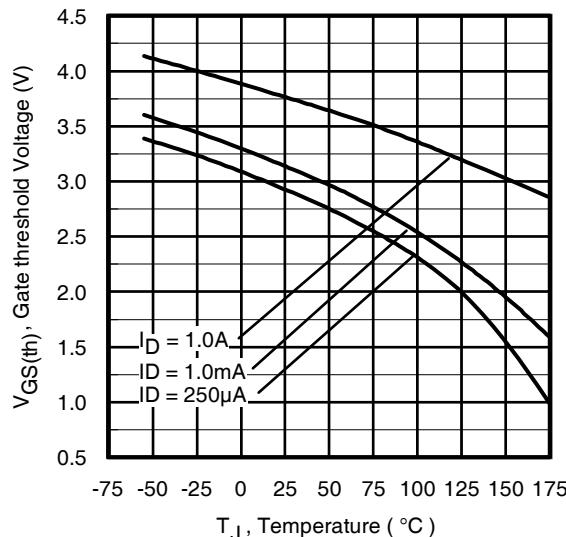


Fig 7. Typical Threshold Voltage vs. Junction Temperature

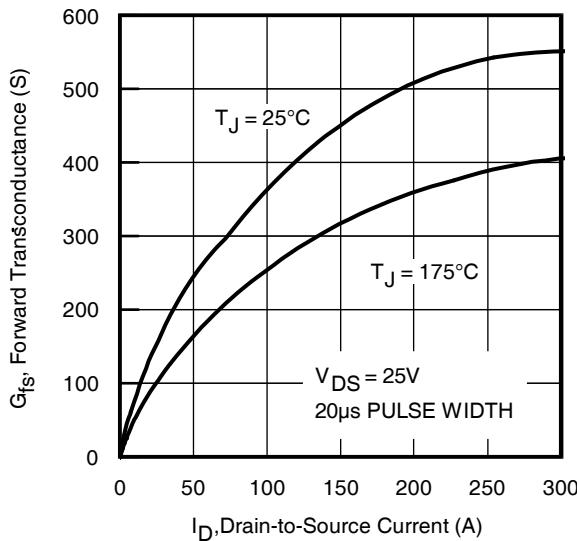


Fig 9. Typical Forward Transconductance vs. Drain Current

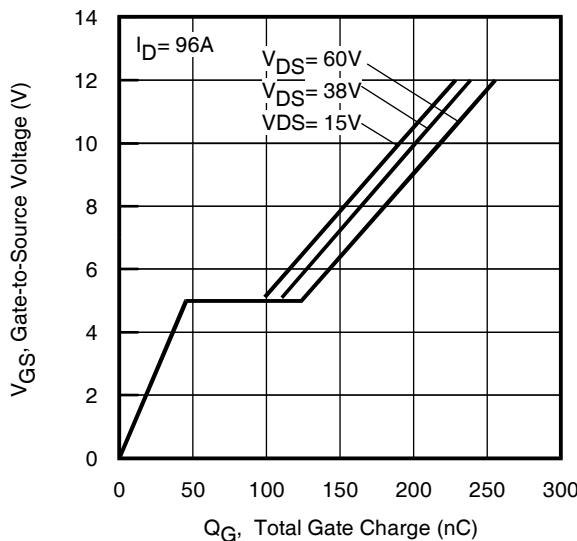


Fig 11 Typical Gate Charge vs. Gate-to-Source Voltage

AUIRF7759L2TR/TR1

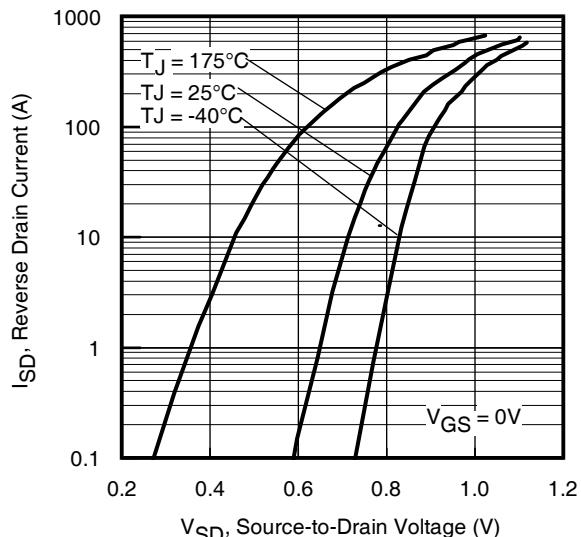


Fig 8. Typical Source-Drain Diode Forward Voltage

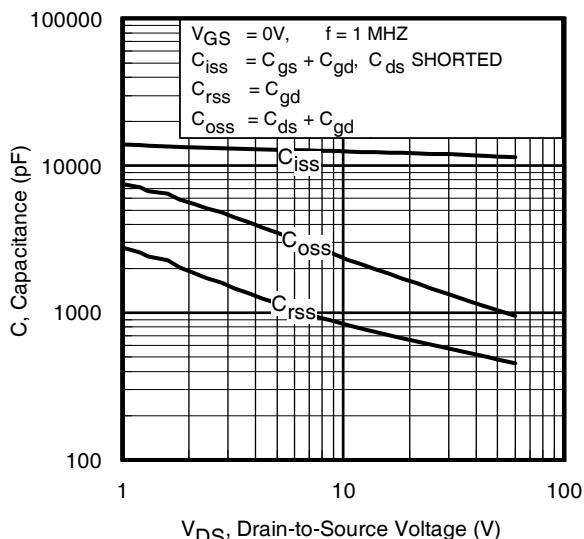


Fig 10. Typical Capacitance vs. Drain-to-Source Voltage

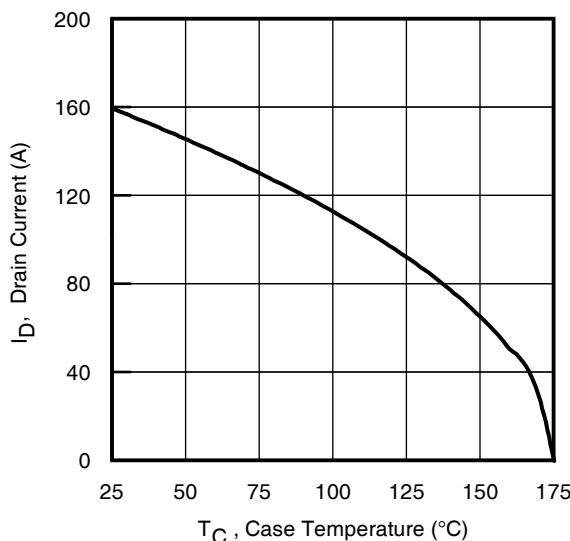


Fig 12. Maximum Drain Current vs. Case Temperature

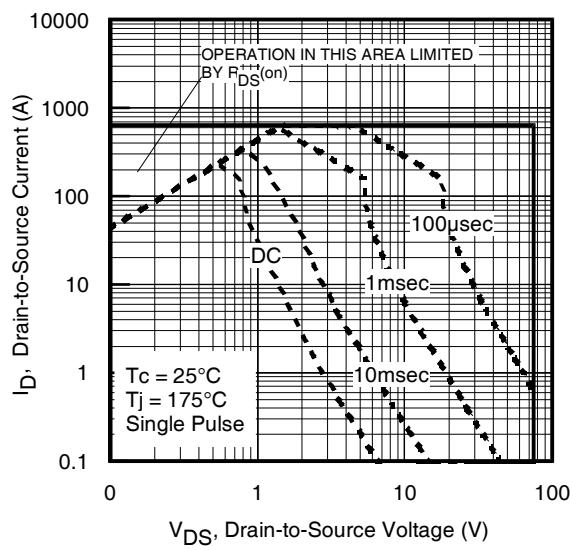


Fig 13. Maximum Safe Operating Area

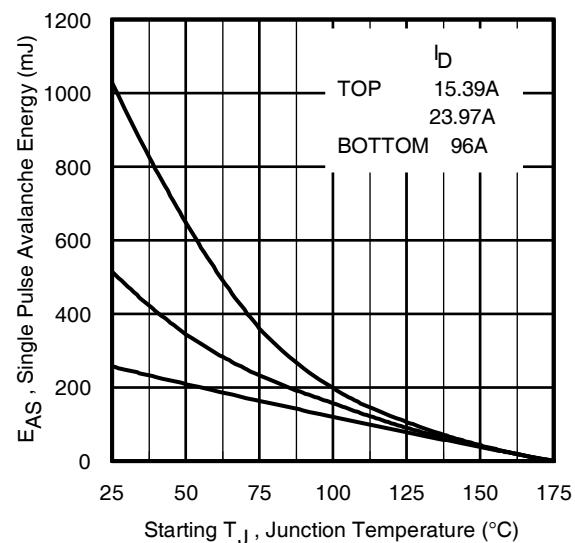


Fig 14. Maximum Avalanche Energy vs. Temperature

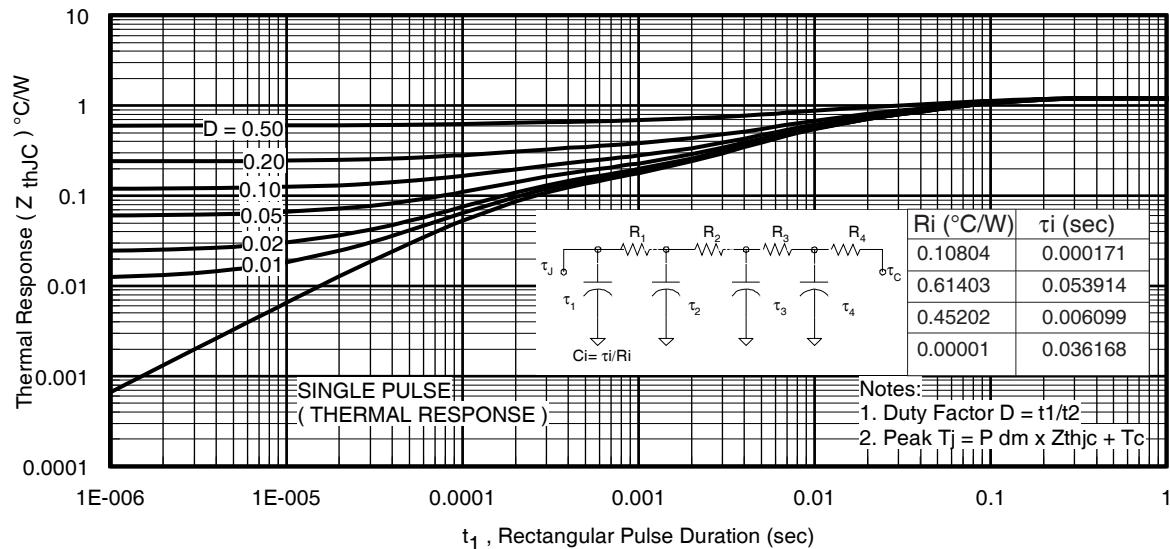


Fig 15. Maximum Effective Transient Thermal Impedance, Junction-to-Case

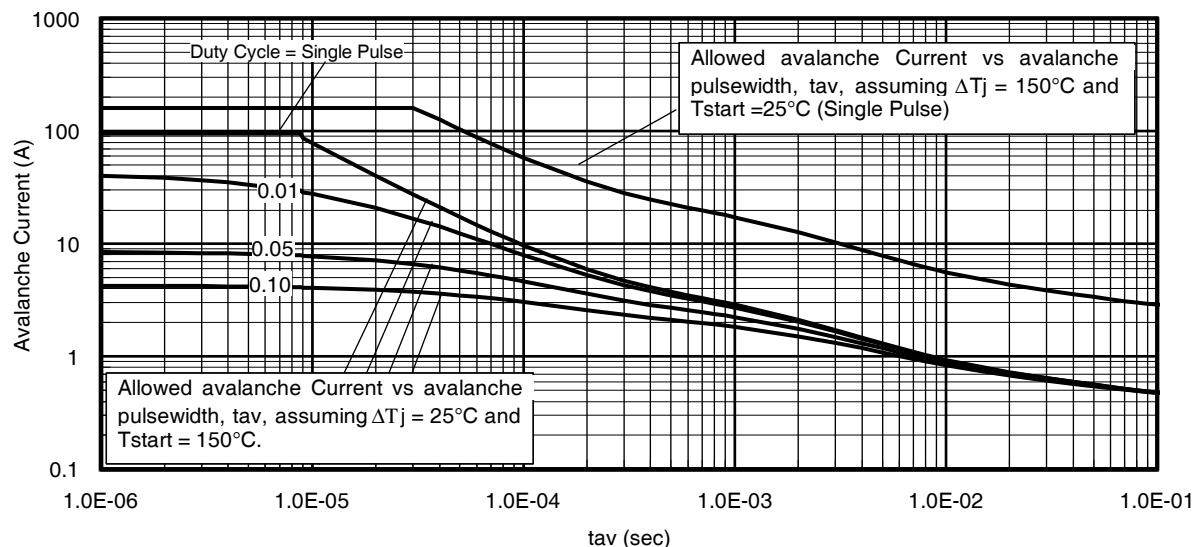


Fig 16. Typical Avalanche Current vs. Pulsewidth

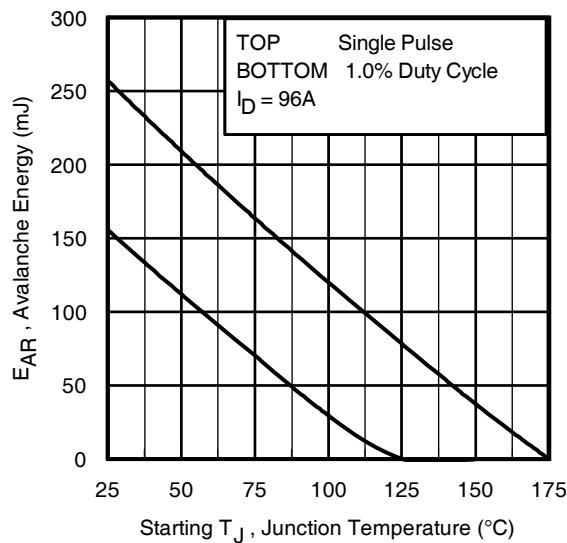


Fig 17. Maximum Avalanche Energy vs. Temperature

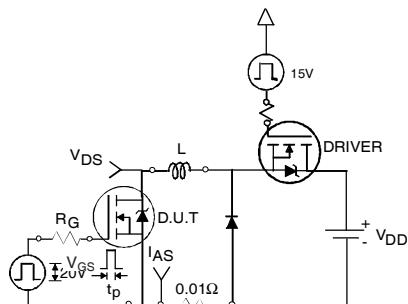


Fig 18a. Unclamped Inductive Test Circuit

$$P_D(\text{ave}) = 1/2 (1.3 \cdot BV \cdot I_{av}) = \Delta T / Z_{thJC}$$

$$I_{av} = 2\Delta T / [1.3 \cdot BV \cdot Z_{th}]$$

$$E_{AS(AR)} = P_D(\text{ave}) \cdot t_{av}$$

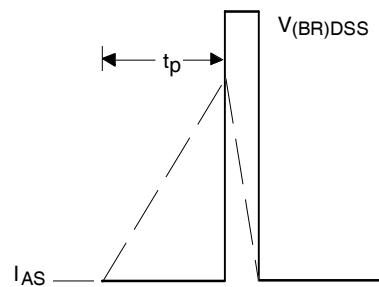


Fig 18b. Unclamped Inductive Waveforms

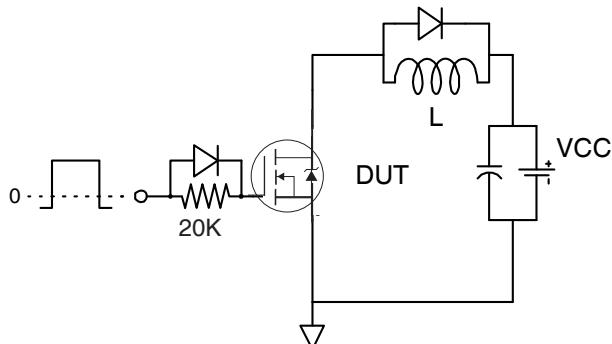


Fig 19a. Gate Charge Test Circuit

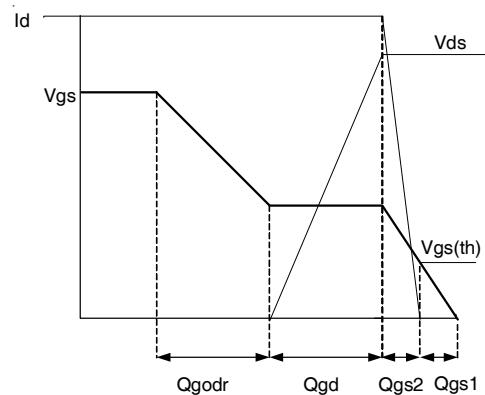


Fig 19b. Gate Charge Waveform

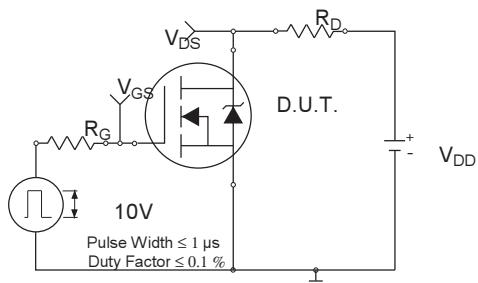


Fig 20a. Switching Time Test Circuit

Fig 20b. Switching Time Waveforms

Notes on Repetitive Avalanche Curves , Figures 14, 17:
(For further info, see AN-1005 at www.irf.com)

1. Avalanche failures assumption:
Purely a thermal phenomenon and failure occurs at a temperature far in excess of T_{jmax} . This is validated for every part type.
2. Safe operation in Avalanche is allowed as long as T_{jmax} is not exceeded.
3. Equation below based on circuit and waveforms shown in Figures 18a, 18b.
4. $P_D(\text{ave})$ = Average power dissipation per single avalanche pulse.
5. BV = Rated breakdown voltage (1.3 factor accounts for voltage increase during avalanche).
6. I_{av} = Allowable avalanche current.
7. ΔT = Allowable rise in junction temperature, not to exceed T_{jmax} (assumed as 25°C in Figure 15, 16).
 t_{av} = Average time in avalanche.
 D = Duty cycle in avalanche = t_{av} / f
 $Z_{thJC}(D, t_{av})$ = Transient thermal resistance, see figure 11

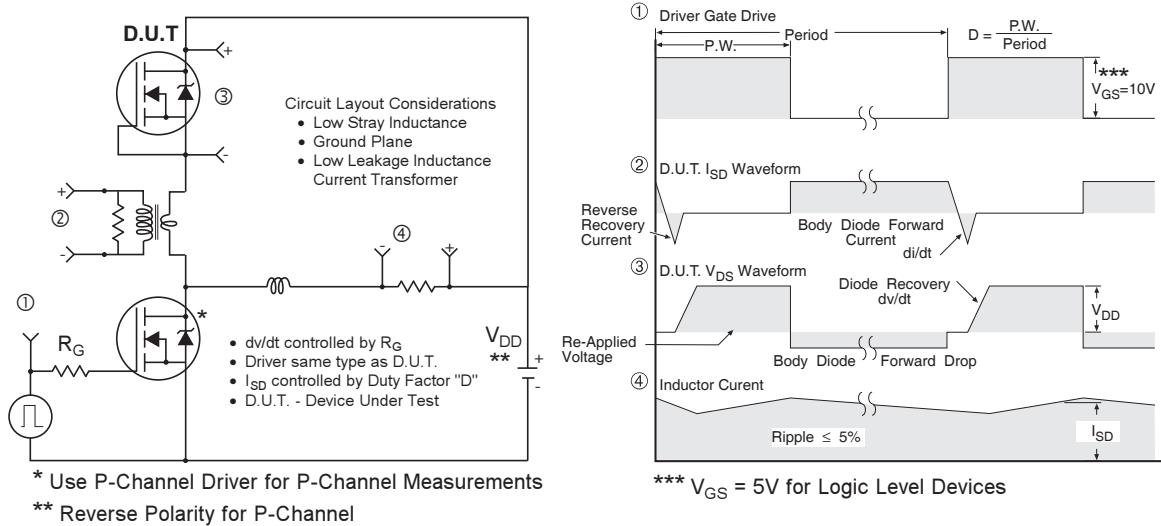
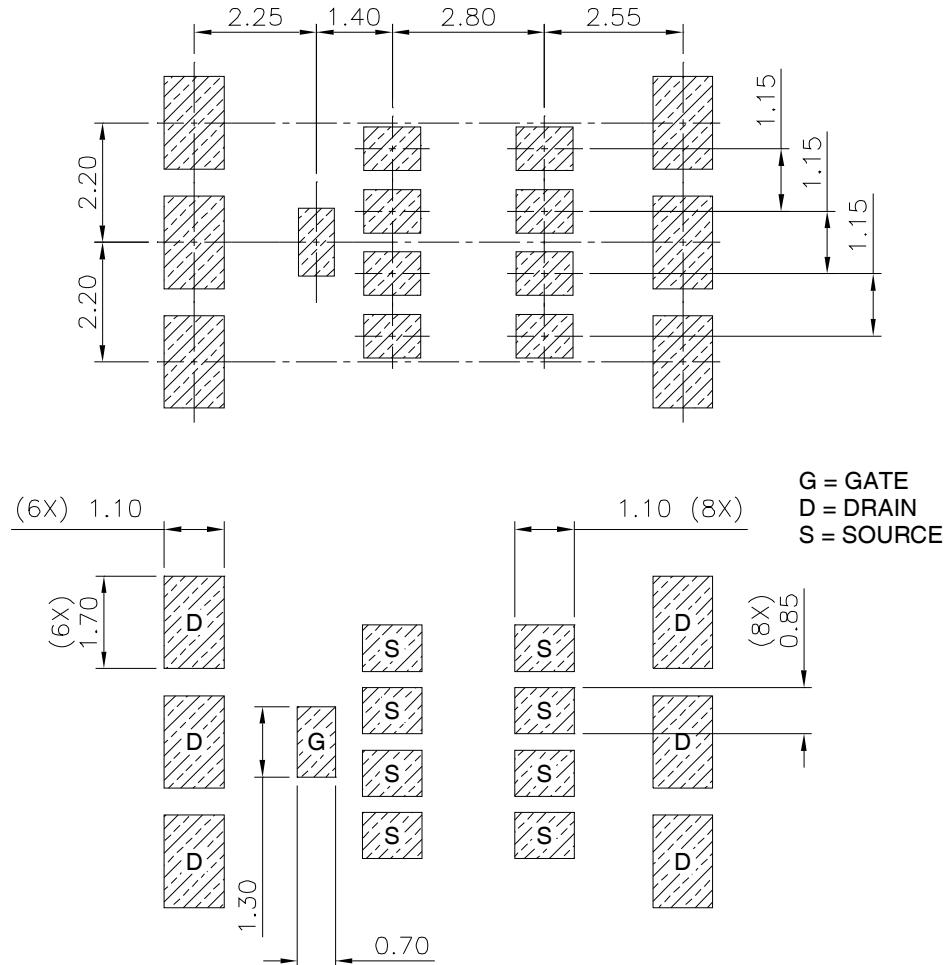


Fig 21. Diode Reverse Recovery Test Circuit for HEXFET® Power MOSFETs

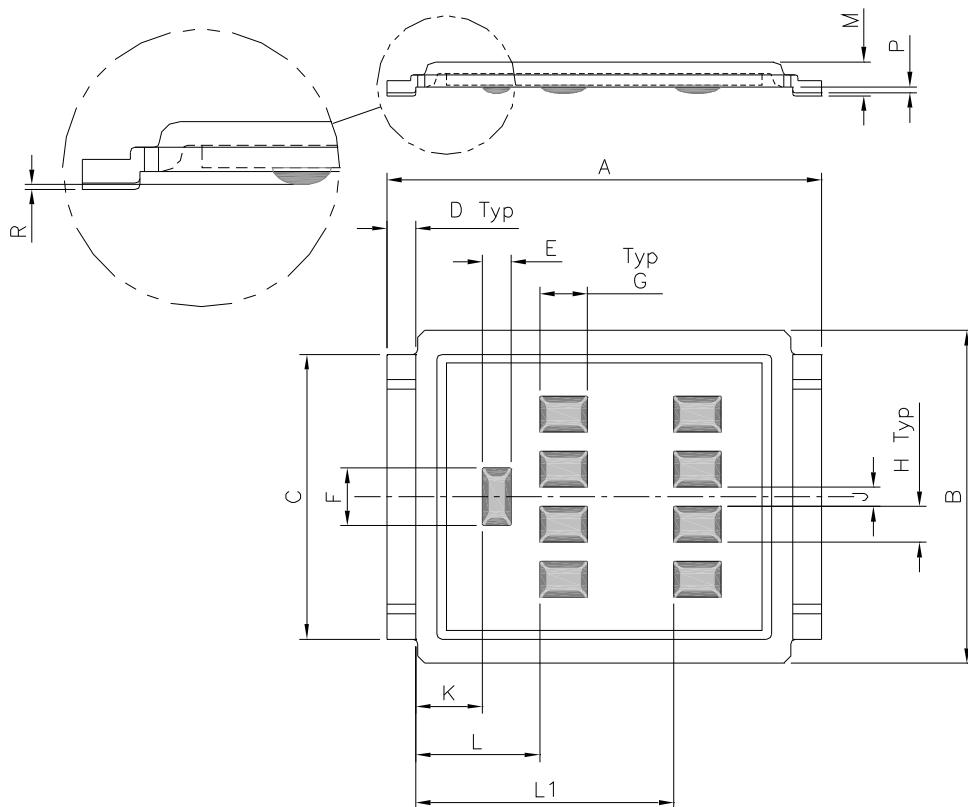
Automotive DirectFET® Board Footprint, L8 (Large Size Can).

Please see AN-1035 for DirectFET® assembly details and stencil and substrate design recommendations



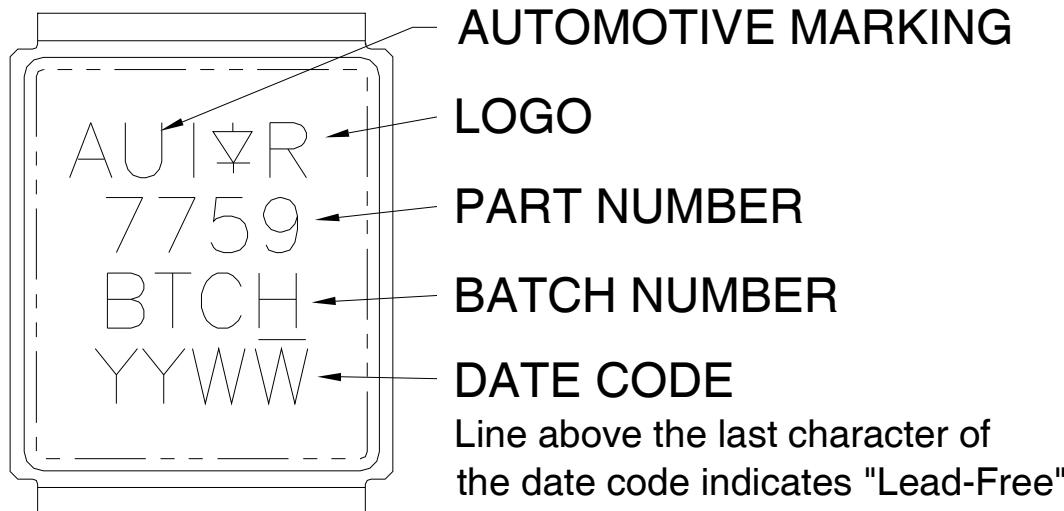
Note: For the most current drawing please refer to IR website at <http://www.irf.com/package>

Automotive DirectFET® Outline Dimension, L8 Outline (LargeSize Can).
Please see AN-1035 for DirectFET® assembly details and stencil and substrate design recommendations

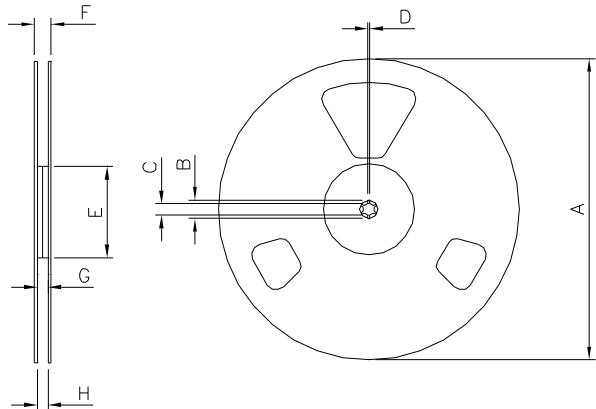


CODE	DIMENSIONS		IMPERIAL	
	Metric		MIN	MAX
A	9.05	9.15	0.356	0.360
B	6.85	7.10	0.270	0.280
C	5.90	6.00	0.232	0.236
D	0.55	0.65	0.022	0.026
E	0.58	0.62	0.023	0.024
F	1.18	1.22	0.046	0.048
G	0.98	1.02	0.039	0.040
H	0.73	0.77	0.029	0.030
J	0.38	0.42	0.015	0.017
K	1.35	1.45	0.053	0.057
L	2.55	2.65	0.100	0.104
L1	5.35	5.45	0.211	0.215
M	0.68	0.74	0.027	0.029
P	0.09	0.17	0.003	0.007
R	0.02	0.08	0.001	0.003

Automotive DirectFET® Part Marking

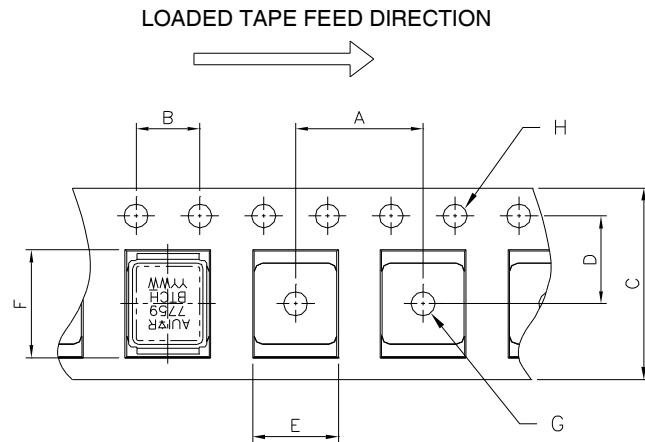


Automotive DirectFET® Tape & Reel Dimension (Showing component orientation).



NOTE: Controlling dimensions in mm
 Std reel quantity is 4000 parts. (ordered as AUIRF7759L2TR). For 1000 parts on 7" reel, order AUIRF7759L2TR1

REEL DIMENSIONS								
CODE	STANDARD OPTION (QTY 4000)		TR1 OPTION (QTY 1000)		METRIC		IMPERIAL	
	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX
A	330.00	N.C	12.992	N.C	177.80	N.C	7.000	N.C
B	20.20	N.C	0.795	N.C	20.20	N.C	0.795	N.C
C	12.80	13.20	0.504	0.520	12.98	13.50	0.331	0.50
D	1.50	N.C	0.059	N.C	1.50	2.50	0.059	N.C
E	99.00	100.00	3.900	3.940	62.48	N.C	2.460	N.C
F	N.C	22.40	N.C	0.880	N.C	N.C	N.C	0.53
G	16.40	18.40	0.650	0.720	N.C	N.C	N.C	N.C
H	15.90	19.40	0.630	0.760	16.00	N.C	0.630	N.C



NOTE: CONTROLLING DIMENSIONS IN MM

DIMENSIONS			
	METRIC	IMPERIAL	
CODE	MIN	MAX	MIN
A	11.90	12.10	4.69
B	3.90	4.10	0.154
C	15.90	16.30	0.623
D	7.40	7.60	0.291
E	7.20	7.40	0.283
F	9.90	10.10	0.390
G	1.50	N.C	0.059
H	1.50	1.60	0.059
	0.063		

Note: For the most current drawing please refer to IR website at <http://www.irf.com/package>

Notes:

- ① Click on this section to link to the appropriate technical paper.
- ② Click on this section to link to the DirectFET® Website.
- ③ Surface mounted on 1 in. square Cu board, steady state.
- ④ T_C measured with thermocouple mounted to top (Drain) of part.
- ⑤ Repetitive rating; pulse width limited by max. junction temperature.
- ⑥ Starting $T_J = 25^\circ\text{C}$, $L = 0.056\text{mH}$, $R_G = 25\Omega$, $I_{AS} = 96\text{A}$.
- ⑦ Pulse width $\leq 400\mu\text{s}$; duty cycle $\leq 2\%$.
- ⑧ Used double sided cooling, mounting pad with large heatsink.
- ⑨ Mounted on minimum footprint full size board with metalized back and with small clip heatsink.
- ⑩ R_θ is measured at T_J of approximately 90°C .

Ordering Information

Base part number	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIRF7759L2	DirectFET2 Large Can	Tape and Reel	4000	AUIRF7759L2TR
		Tape and Reel	1000	AUIRF7759L2TR1

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IR warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with IR's standard warranty. Testing and other quality control techniques are used to the extent IR deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

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