SEMICONDUCTOR

TOSHIBA

TECHNICAL DATA

TOSHIBA PHOTOCOUPLER

6N137

GaAℓAs IRDE & PHOTO IC

(6N137)

DEGITAL LOGIC ISOLATION.

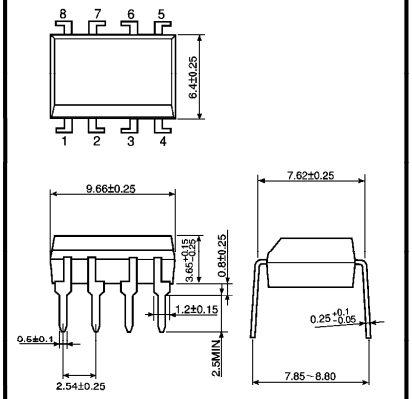
TELE-COMMUNICATION.

ANALOG DATA EQUIPMENT CONTROL

The TOSHIBA 6N137 consist of a high emitting diode and a one chip photo IC. This unit is 8-lead DIP package.

- LSTTL / TTL Compatible : 5V Supply
- Ultra High Speed : 10 MBd
- Guaranteed Performance Over Temperature : 0 °C to 70 °C
- High Isolation Voltage : 2500Vrms Min.
- UL Recognized : UL1577, File No. E67349

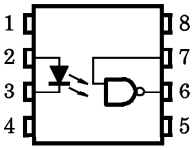
Unit in mm



JEDEC	—
EIAJ	—
TOSHIBA	11-10C4

Weight : 0.54g

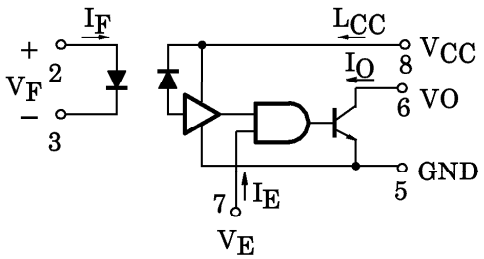
PIN CONFIGURATIONS (TOP VIEW)



1. N.C.
2. ANODE
3. CATHODE
4. N.C.
5. GND
6. OUTPUT (OPEN COLLECTOR)
7. ENABLE
8. V_{CC}

RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	MIN.	MAX.	UNIT
Input Current, Low Level Each Channel	I_{FL}	0	250	μA
Input Current, High Level Each Channel	I_{FH}	7	20	mA
High Level Enable Voltage	V_{EH}	2.0	V_{CC}	V
Low Level Enable Voltage (Output High)	V_{EL}	0	0.8	V
Supply Voltage, Output	V_{CC}	4.5	5.5	V
Fan Out (TTL LOAD)	N	—	8	—
Operating Temperature	Ta	0	70	°C



TRUTH TABLE

INPUT	ENABLE	OUTPUT
H	H	L
L	H	H
H	L	H
L	L	H

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MAXIMUM RATINGS

	CHARACTERISTIC		SYMBOL	RATING	UNIT
LED	Forward Current		I_F	20	mA
	Pulse Forward Current	(Note 1)	I_{FP}	40	mA
	Reverse Voltage		V_R	5	V
DETECTOR	Output Current		I_O	50	mA
	Output Voltage		V_O	7	V
	Supply Voltage (1 minute Maximum)		V_{CC}	7	V
	Enable Input Voltage (Not to exceed V_{CC} by more than 500mV)		V_{EH}	5.5	V
	Output Collector Power Dissipation		P_O	85	mW
Operating Temperature Range			T_{opr}	0~70	°C
Storage Temperature Range			T_{stg}	−55~125	°C
Lead Solder Temperature (10s)		(Note 2)	T_{sol}	260	°C

Note 1 : 50% duty cycle, 1ms Pulse width.
Note 2 : Soldering portion of lead : up to 2mm from the body of the device.

PRECAUTION

Please be careful of the followings.

A ceramic capacitor (0.1μF) should be connected from pin 8 to pin 5 to stabilize the operation of the high gain linear amplifier. Failure to provide the bypassing may impair the switching property. The total lead length between capacitor and coupler should not exceed 1cm.

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ELECTRICAL CHARACTERISTICS

OVER RECOMMENDED TEMPERATURE (Ta = 0~70°C Unless otherwise noted)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	※※TYP.	MAX.	UNIT
High Level Output Current	I_{OH}	V_{CC}=5.5V, V_O=5.5V I_F=250μA, V_E=2.0V	—	1	250	μA
Low Level Output Voltage	V_{OL}	V_{CC}=5.5V, I_F=5mA V_{EH}=2.0V I_{OL} (Sinking) =13mA	—	0.4	0.6	V
High Level Enable Current	I_{EH}	V_{CC}=5.5V, V_E=2.0V	—	−1.0	—	mA
Low Level Enable Current	I_{EL}	V_{CC}=5.5V, V_E=0.5V	—	−1.6	−2.0	mA
High Level Supply Current	I_{CCH}	V_{CC}=5.5V, I_F=0, V_E=0.5V	—	7	15	mA
Low Level Supply Current	I_{CCL}	V_{CC}=5.5V, I_F=10mA V_E=0.5V	—	12	18	mA
Resistance (Input-Output) (Note)	R_{I-O}	V_{I-O}=500V, Ta=25°C R.H.≦60%	—	10^{12}	—	Ω
Capacitance (Input-Output) (Note)	C_{I-O}	f=1MHz, Ta=25°C	—	0.6	—	pF
Input Forward Voltage	V_F	I_F=10mA, Ta=25°C	—	1.65	1.75	V
Input Reverse Breakdown Voltage	BV_R	I_R=10μA, Ta=25°C	5	—	—	V
Input Capacitance	C_{IN}	V_F=0, f=1MHz	—	45	—	pF
Current Transfer Ratio	CTR	I_F=5.0mA, R_L=100Ω	—	1000	—	%

※※ All typical values are at V_{CC}=5V, Ta=25°C

Note : Pins 1, 2, 3 and 4 shorted together and Pins 5, 6, 7 and 8 shorted together.

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SWITCHING CHARACTERISTICS (Ta = 25°C, V_{CC} = 5V)

CHARACTERISTIC	SYMBOL	TEST CIRCUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Propagation Delay Time to High Output Level	t_{pLH}	1	R_L=350Ω, C_L=15pF I_F=7.5mA	—	60	75	ns
Propagation Delay Time to Low Output Level	t_{pHL}	1	R_L=350Ω, C_L=15pF I_F=7.5mA	—	60	75	ns
Output Rise-Fall Time (10-90%)	t_r, t_f	—	R_L=350Ω, C_L=15pF I_F=7.5mA	—	30	—	ns
Propagation Delay Time of Enable from V_{EH} to V_{EL}	t_{ELH}	2	R_L=350Ω, C_L=15pF I_F=7.5mA V_{EH}=3.0V V_{EL}=0.5V	—	25	—	ns
Propagation Delay Time of Enable from V_{EL} to V_{EH}	t_{EHL}	2	R_L=350Ω, C_L=15pF I_F=7.5mA V_{EH}=3.0V V_{EL}=0.5V	—	25	—	ns
Common Mode Transient Immunity at Logic High Output Level	CM_H	3	V_{CM}=10V R_L=350Ω $V_{O\,(min.)}$=2V I_F=0mA	—	200	—	V / μs
Common Mode Transient Immunity at Logic Low Output Level	CM_L	3	V_{CM}=10V R_L=350Ω $V_{O\,(max.)}$=0.8V I_F=5mA	—	−500	—	V / μs

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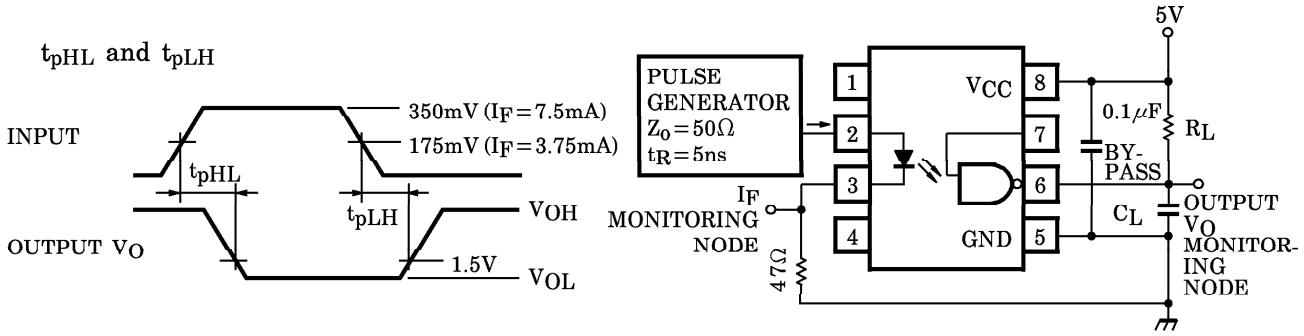
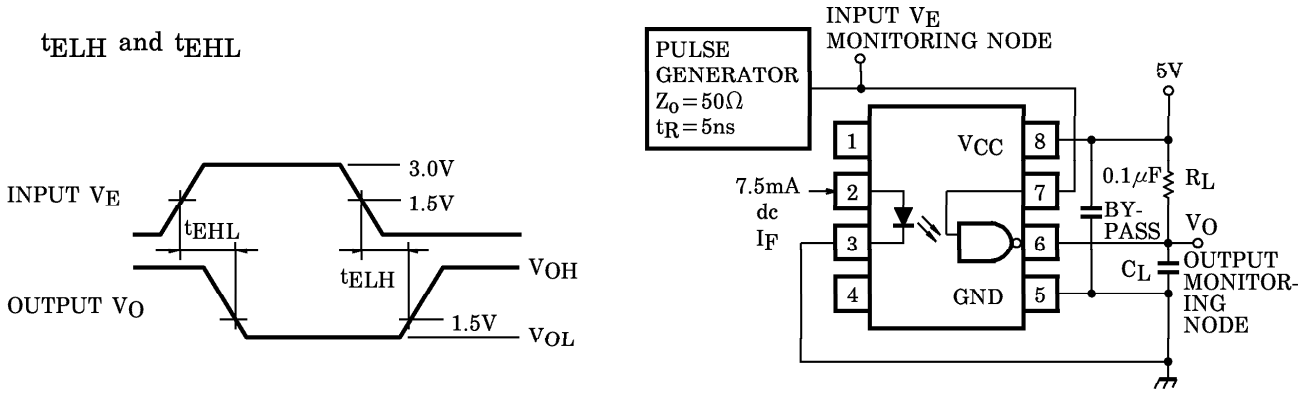
TEST CIRCUIT 1.



· C_L is approximately 15pF which includes probe and stray wiring capacitance.

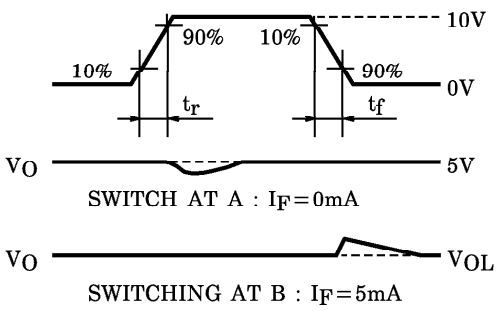
TEST CIRCUIT 2.

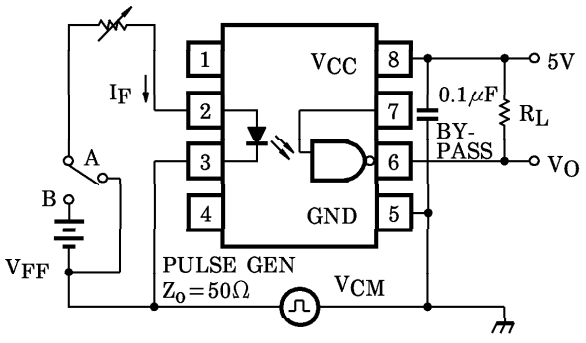


· C_L is approximately 15pF which includes prove and stray wiring capacitance.

TEST CIRCUIT 3.

Transient Immunity and Typical Waveforms





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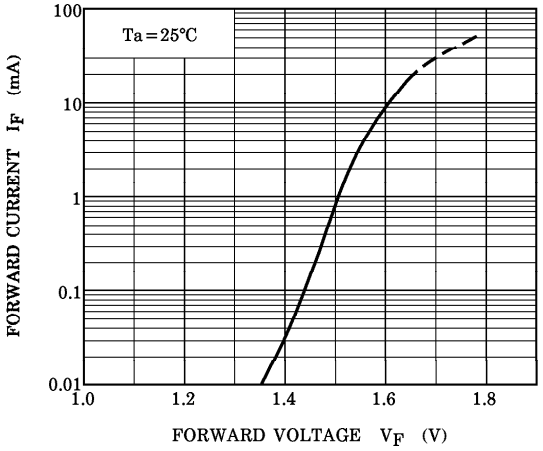
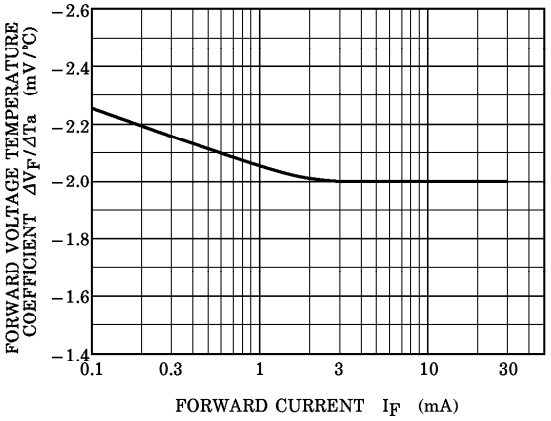
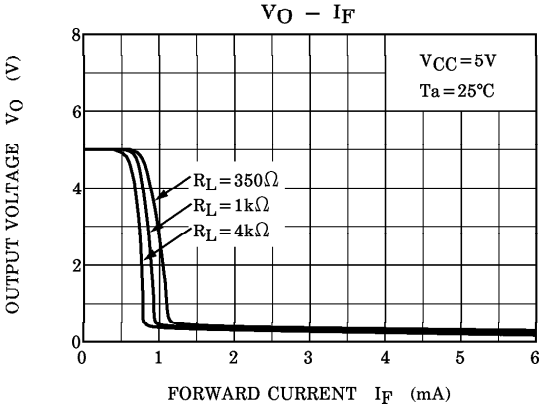
$I_F - V_F$



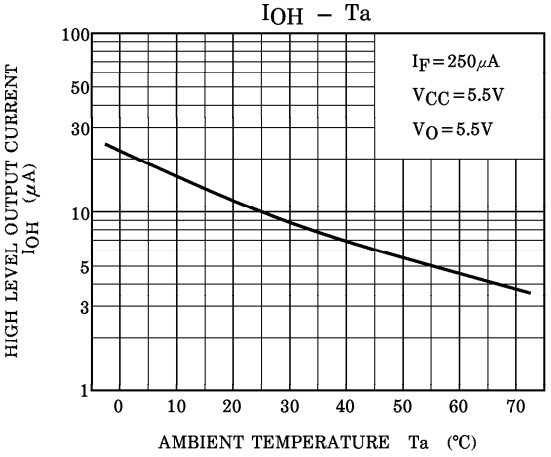
$\Delta T_F / \Delta Ta - I_F$



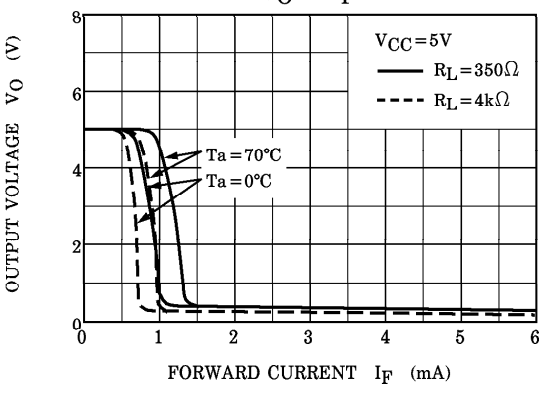
$V_O - I_F$



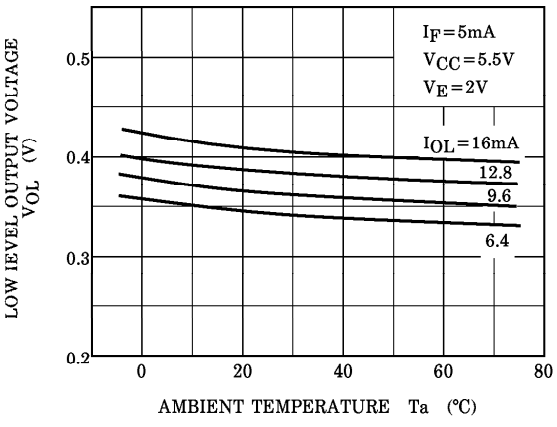
$I_{OH} - Ta$



$V_O - I_F$



$V_{OL} - Ta$



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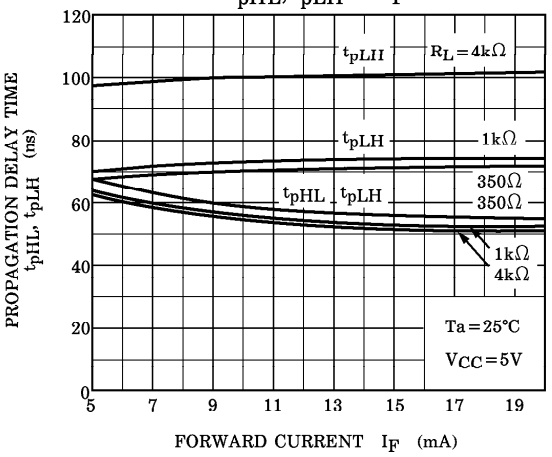
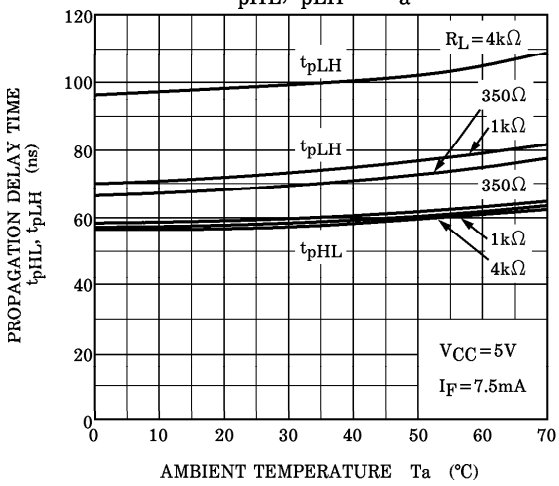
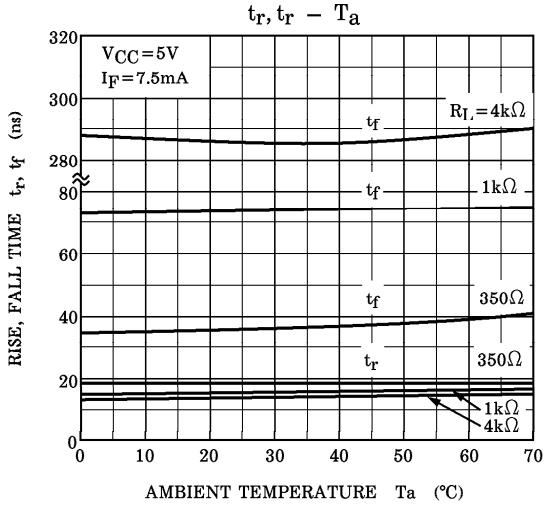
$t_{pHL}, t_{pLH} - I_F$



$t_{pHL}, t_{pLH} - T_a$



$t_r, t_r - T_a$



$t_{EHL}, t_{ELH} - T_a$