

SANYO Semiconductors DATA SHEET

PCP1201—NPN Epitaxial Planar Silicon Transistor High-Voltage Switching Applications

Applications

• DC / DC converter, relay drivers, lamp drivers, motor drivers, inverter.

Features

- · Adoption of FBET, MBIT process.
- · High current capacitance.
- · Low collector-to-emitter saturation voltage.
- · High-speed switching.
- · High allowable power dissipation.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		150	V
Collector-to-Emitter Voltage	VCES		150	V
Collector-to-Emitter Voltage	VCEO		120	V
Emitter-to-Base Voltage	VEBO		7	V
Collector Current	IC		2.5	Α
Collector Current (Pulse)	ICP		4	Α
Base Current	IB		500	mA
Collector Dissipation	PC	When mounted on ceramic substrate (450mm ² X0.8mm)	1.3	W
		Tc=25°C	3.5	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Marking : QH

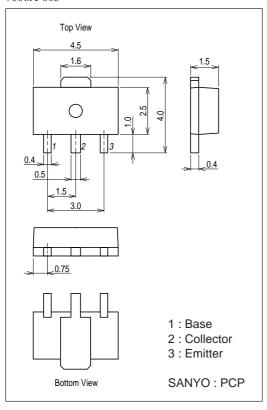
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Electrical Characteristics at Ta=25°C

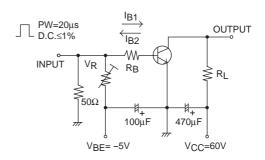
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector Cutoff Current	ICBO	V _{CB} =100V, I _E =0A			1	μΑ
Emitter Cutoff Current	I _{EBO}	V _{EB} =5V, I _C =0A			1	μΑ
DC Current Gain	hFE	V _{CE} =5V, I _C =100mA	200		560	
Gain-Bandwidth Product	fŢ	VCE=10V, IC=100mA		130		MHz
Output Capacitance	Cob	V _{CB} =10V, f=1MHz		13		pF
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)	I _C =1A, I _B =100mA		100	150	mV
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	I _C =1A, I _B =100mA		0.85	1.2	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =10μA, I _E =0A	150			V
Collector-to-Emitter Breakdown Voltage	V(BR)CES	I _C =100μA, R _{BE} =0Ω	150			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	IC=1mA, RBE=∞	120			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =10μA, I _C =0A	7			V
Turn-ON Time	ton	See specified Test Circuit.		50		ns
Storage Time	t _{stg}	See specified Test Circuit.		1250		ns
Fall Time	tf	See specified Test Circuit.		60		ns

Package Dimensions

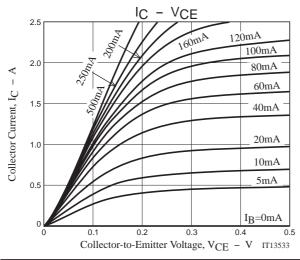
unit : mm (typ) 7008A-003

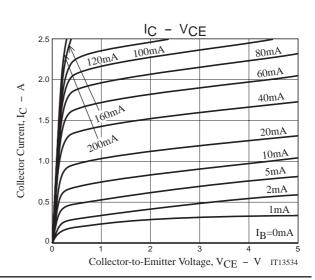


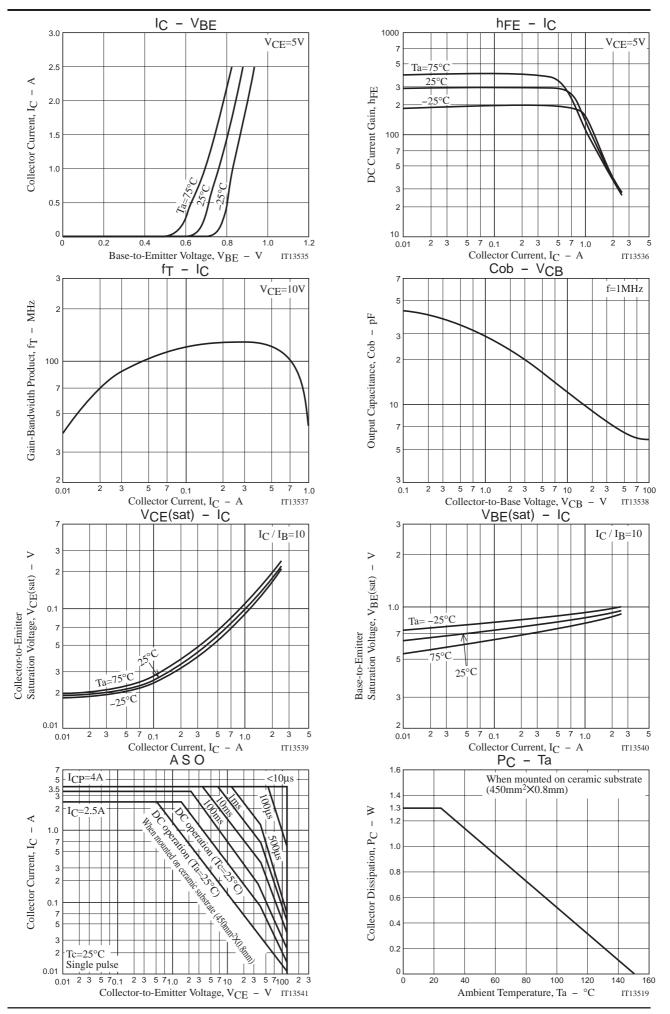
Switching Time Test Circuit

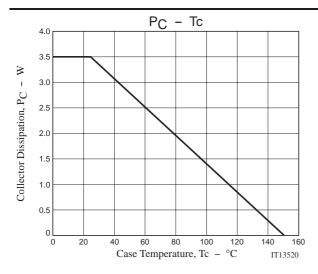


$$I_{C}=10I_{B1}=-10I_{B2}=0.5A$$









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