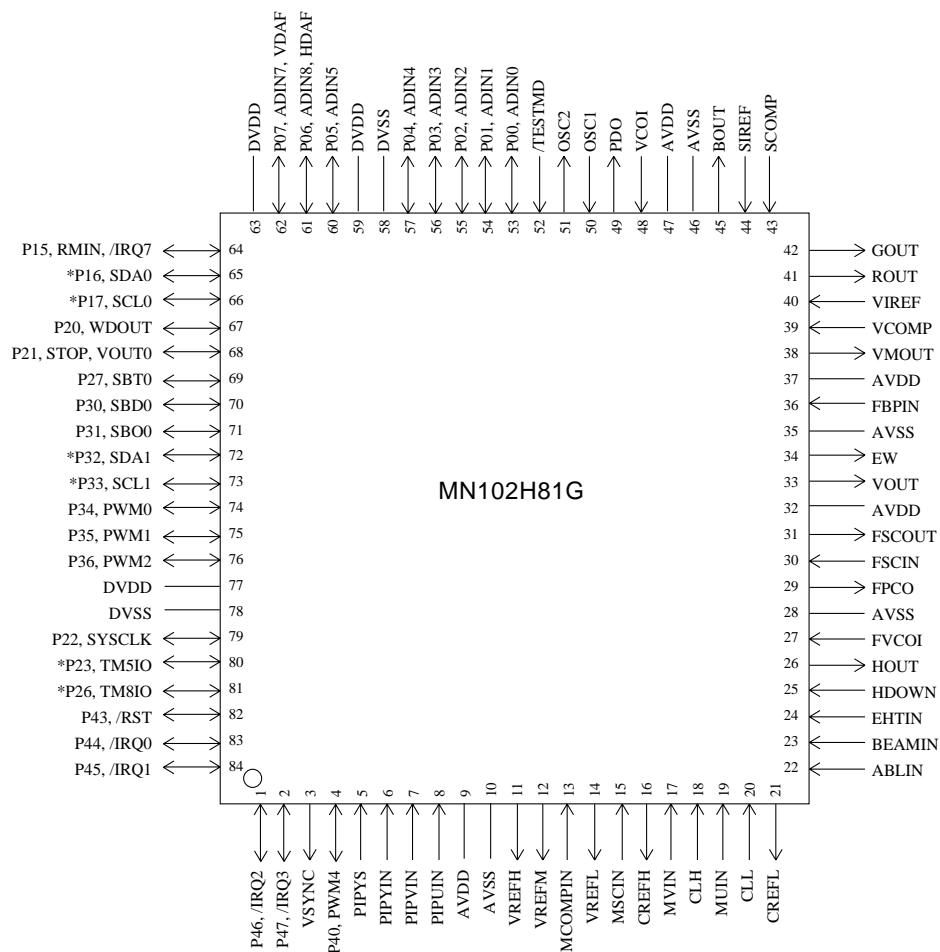


# **MN102H81G**

<b>Type</b>		MN102H81G	
<b>ROM (×16-bit)</b>		128 K	
<b>RAM (×16-bit)</b>		4 K	
<b>Package</b>		QFP084-P-1818E *Lead-free	
<b>Minimum Instruction Execution Time</b>		83 ns (at 3.135 V to 3.465 V, 12 MHz)	
<b>Interrupts</b>		External (5 lines) Internal (28 lines) : Timer × 11, A/D × 1, Undefined command × 1, RESET × 1, OSD × 2, Serial × 1, I <sup>2</sup> C × 1, Caption × 2, Remote control × 1, Address coincidence × 4, HSYNC × 1, VSYNC × 1	
<b>Timer Counter</b>		8-bit timer × 10 Watchdog timer: 17-bit × 1	
<b>Serial Interface</b>		I <sup>2</sup> C × 1: for multimaster mode, bus line (output) has 2 systems Sync serial / UART × 1	
<b>Caption</b>		• Built-in sync separator × 1	
<b>I/O Pins</b>	<b>I/O</b>	30	• Common use
<b>A/D Inputs</b>		10-bit × 8-ch. (with S/H)	
<b>PWM</b>		8-bit × 6-ch.	
<b>Special Ports</b>		Remote control reception	
<b>CRTC</b>		3-layer display (graphics, characters, splits)	
<b>Notes</b>		Remote control input discriminant circuit built-in, build-in NTSC video signal processing circuit, built-in 3-line comb filter	

## Pin Assignment



QFP084-P-1818E \*Lead-free

\*5 V dielectric Nch open drain output pin

## Support Tool

In-circuit Emulator

PX-ICE102H81-QFP084-P-1818E

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