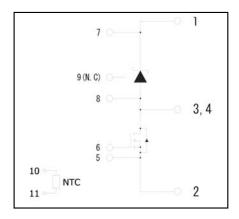
#### Application

- · Converter
- · Photovoltaics, wind power generation.
- · Induction heating equipment.

#### Features

- 1) Low surge, low switching loss.
- 2) High-speed switching possible.
- 3) Reduced temperature dependence.

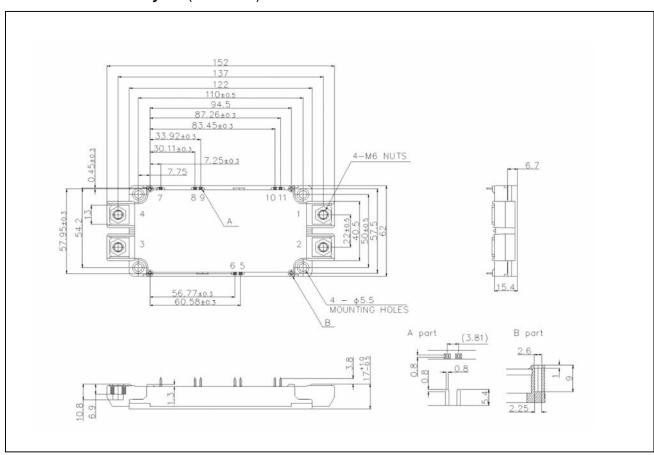
#### ●Circuit diagram



#### Construction

This product is a chopper module consisting of SiC-DMOSFET and SiC-SBD from ROHM.

#### ● Dimensions & Pin layout (Unit : mm)

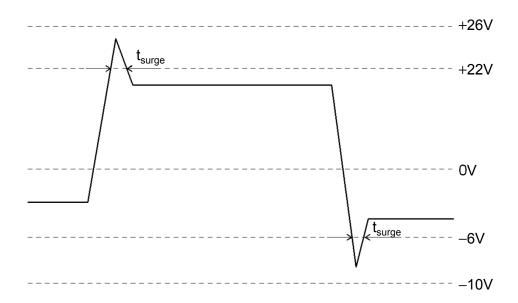


# ● Absolute maximum ratings (T<sub>j</sub> = 25°C)

Parameter	Symbol	Conditions	Limit	Unit		
Drain-source voltage	$V_{DSS}$	G-S short	1200			
Repetitive reverse voltage	$V_{DSS}$	Clamp diode	1200	]		
Gate-source voltage(+)		D-S short	22	V		
Gate-source voltage(-)	$V_{GSS}$	D-3 \$1101t	-6	<u>]</u>		
G - S Voltage (t <sub>surge</sub> <300nsec)	$V_{GSS\_surge}$	D-S short	-10 to 26			
Drain current *1	$I_D$	DC (T <sub>c</sub> =60°C)	204			
	$I_{DRM}$	Pulse (T <sub>c</sub> =60°C) 1ms *2	360	]		
	I <sub>DRM</sub>	Pulse (T <sub>c</sub> =60°C) 10us *2 *3	540			
Source current *1	Is	DC (T <sub>c</sub> =60°C ) V <sub>GS</sub> =18V	204			
	I <sub>SRM</sub>	Pulse (Tc=60°C) 1ms V <sub>GS</sub> =18V * <sup>2</sup>	360	Α		
	I <sub>SRM</sub>	Pulse (Tc=60°C) 10us V <sub>GS</sub> =18V * <sup>2</sup> * <sup>3</sup>	540	1		
Forward current (clamp diode) *1	I <sub>F</sub>	DC (T <sub>c</sub> =60°C)	204	1		
	I <sub>FRM</sub>	Pulse (Tc=60°C) 1ms *2	360	1		
	I <sub>FRM</sub>	Pulse (Tc=60°C) 10us *2 *3	540	1		
Total power dissipation *3	Ptot	T <sub>c</sub> =25°C	1360	W		
Max Junction Temperature	T <sub>jmax</sub>		175			
Operating junction temperature	$T_jop$		-40 to150	°C		
Storage temperature	T <sub>stg</sub>		-40 to125			
Isolation voltage	Visol	Terminals to baseplate, f=60Hz AC 1min.	2500	Vrms		
Mounting torque		Main Terminals : M6 screw	4.5	NI me		
	_	Mounting to heat shink : M5 screw	3.5	N·m		

<sup>(\*1)</sup> Case temperature (T<sub>c</sub>) is defined on the surface of base plate just under the chips.

## Example of acceptable V<sub>GS</sub> waveform



<sup>(\*2)</sup> Repetition rate should be kept within the range where temperature rise if die should not exceed  $T_{j \text{ max.}}$ 

<sup>(\*3)</sup> Please use an appropriate external gate resistor not to exceed maximum ratings of Drain - Source Voltage.

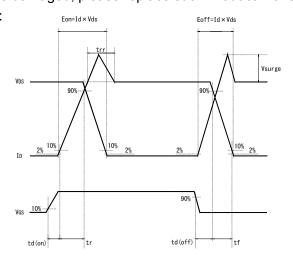
<sup>(\*4)</sup> T<sub>i</sub> is less than 175°C

# ●Electrical characteristics (T<sub>i</sub>=25°C)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
On-state static Drain-Source Voltage	$V_{DS(on)}$	I <sub>D</sub> =180A, V <sub>GS</sub> =18V	T <sub>j</sub> =25°C	-	2.2	3.2	V
			T <sub>j</sub> =125°C	-	3.1	-	
			T <sub>j</sub> =150°C	-	3.5	5.0	
Drain cutoff current	I <sub>DSS</sub>	V <sub>DS</sub> =1200V, V <sub>GS</sub> =0V		-	-	10	μΑ
Forward Voltage	$V_{F}$	I <sub>F</sub> =180A	T <sub>j</sub> =25°C	-	1.6	2.2	V
			T <sub>j</sub> =125°C		2.0	-	
			T <sub>j</sub> =150°C	-	2.2	3.3	
Reverse current	I <sub>RRM</sub>	Clamp diode		-	-	3.2	mA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS}$ =10V, $I_{D}$ =35.2mA		1.6	-	4	V
Gate-Source Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =22V, $V_{DS}$ =0V		-	-	0.5	μΑ
		$V_{GS}$ = -6V, $V_{DS}$ =0V		-0.5	-	-	
	t <sub>d(on)</sub>	$V_{GS(on)}$ =18V, $V_{GS(off)}$ =0	-	49	-	ns	
Switching Characteristics	t <sub>r</sub>	V <sub>DS</sub> =600V	-	36	-		
	t <sub>rr</sub>	I <sub>D</sub> =180A	-	20	-		
	$t_{d(off)}$	$R_{G(on)}$ =1.0 $\Omega$ , $R_{G(off)}$ =0.2 $\Omega$		-	139		-
	t <sub>f</sub>	inductive load	-	32	-		
Input Capacitance	Ciss	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, 200kHz		-	20	-	nF
Gate Registance	$R_{Gint}$	T <sub>j</sub> =25°C		-	1.2	-	Ω
NTC Rated Resistance	R25				5.0		$k\Omega$
NTC B Value	B50/25				3370		K
Stray Inductance	Ls				13.0	-	nΗ
Creepage Distance	-	Terminal to heat sink			14.5	-	mm
		Terminal to terminal			15.0	-	mm
Clearance Distance	1	Terminal to heat sink			12.0	-	mm
		Terminal to terminal			9.0	-	mm
Junction-to-Case Thermal Resistance	R <sub>th</sub> (j-c)	DMOSFET (1/2 module) *5		-	-	0.11	K/W
		SBD (1/2 module) *5		-	-	0.14	
Case-to -heat sink	R <sub>th</sub> (c-f)	Case to heat sink, per 1 module, Thermal grease appied *6		ı	0.035	-	
Thermal Resistance	· \tn(\cup 1)						

<sup>(\*5)</sup> Measurement of T<sub>c</sub> is to be done at the point just beneath the chip.

#### Waveform for switching test

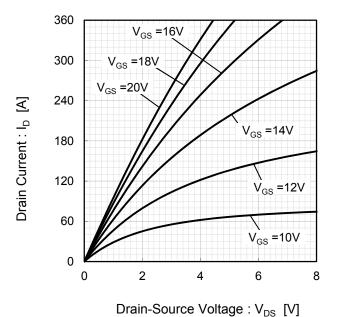


<sup>(\*6)</sup> Typical value is measured by using thermally conductive grease of  $\lambda$ =0.9W/(m·K).

<sup>(\*7)</sup> If the Product is used beyond absolute maximum ratings defined in the Specifications, as its internal structure may be damaged, please replace such Product with a new one.

#### ● Electrical characteristic curves (Typical)

Fig.1 Typical Output Characteristics [ $T_i$ =25°C] Fig.2 Drain-Source Voltage vs. Drain Current



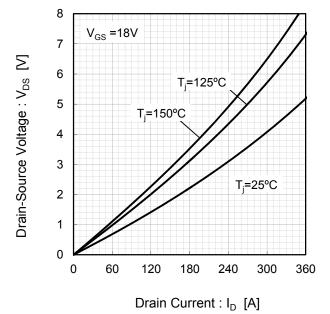


Fig.3 Drain-Source Voltage vs. Gate-Source Voltage [T<sub>i</sub>=25°C] 5 T<sub>i</sub>=25°C Drain-Source Voltage: V<sub>DS</sub> [V] 4 3 I<sub>D</sub>=180A 2 I<sub>D</sub>=120A I<sub>D</sub>=90A 1 I<sub>D</sub>=60A 0 12 14 16 18 20 22 24 Gate-Source Voltage : V<sub>GS</sub> [V]

vs. Junction Temperature 40 Static Drain - Source On-State Resistance 35 V<sub>GS</sub>=14V V<sub>GS</sub>=12V 30 25  $: R_{DS(on)} \ [m\Omega]$ 20 √<sub>GS</sub>=16V V<sub>GS</sub>=18V 15  $V_{GS}$ =20V10 5 I<sub>D</sub> =180A 0 100 50 150 200 250

Fig.4 Static Drain - Source On-State Resistance

Junction Temperature :  $T_j$  [°C]

#### • Electrical characteristic curves (Typical)

Fig.5 Forward characteristic of Diode

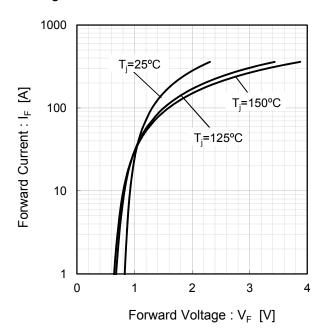


Fig.6 Forward characteristic of Diode

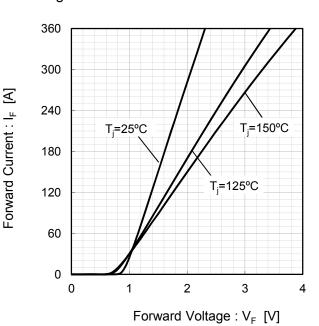


Fig.7 Drain Current vs. Gate-Source Voltage

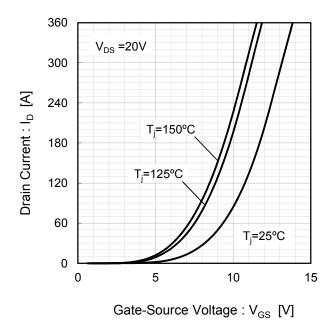
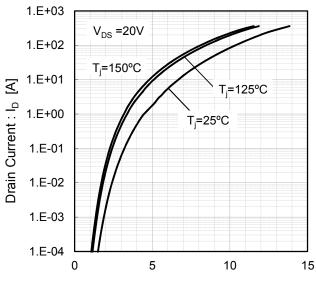


Fig.8 Drain Current vs. Gate-Source Voltage



#### ●Electrical characteristic curves (Typical)

Fig.9 Switching Characteristics [T<sub>i</sub>=25°C]

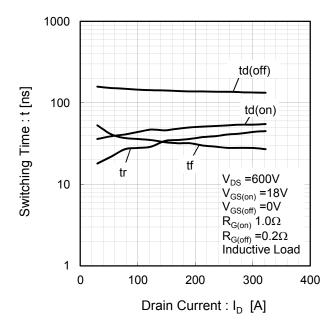


Fig.10 Switching Characteristics [T<sub>i</sub>=125°C]

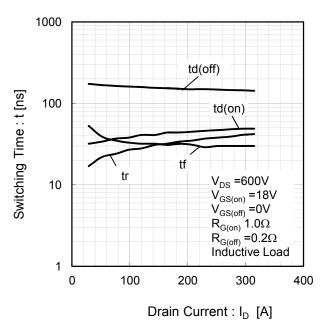


Fig.11 Switching Characteristics [T<sub>i</sub>=150°C]

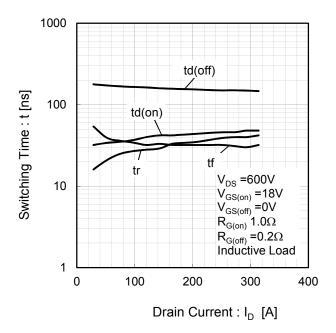
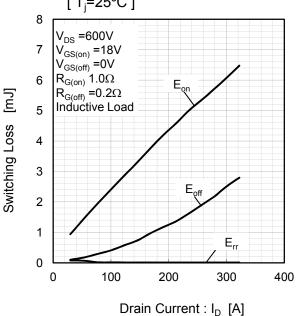


Fig.12 Switching Loss vs. Drain Current [ T<sub>i</sub>=25°C ]



#### ●Electrical characteristic curves (Typical)

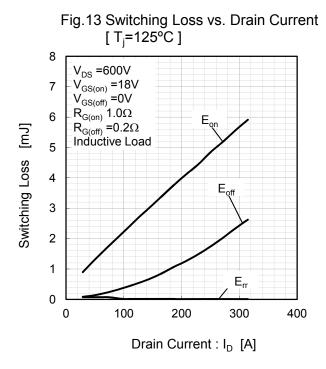
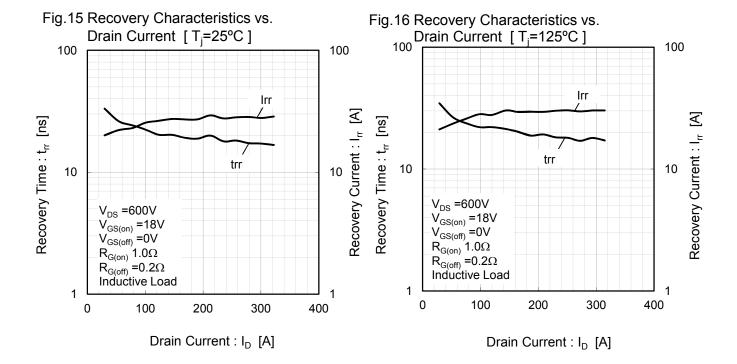


Fig.14 Switching Loss vs. Drain Current [T<sub>i</sub>=150°C] 8 V<sub>DS</sub> =600V  $V_{GS(on)} = 18V$   $V_{GS(off)} = 0V$   $R_{G(on)} = 1.0\Omega$ 7 6  $R_{G(off)} = 0.2\Omega$ <u>m</u> Inductive Load 5 Switching Loss 4 3 2 1  $\mathsf{E}_{\mathsf{rr}}$ 0 0 100 200 300 400 Drain Current: I<sub>D</sub> [A]



#### ● Electrical characteristic curves (Typical)

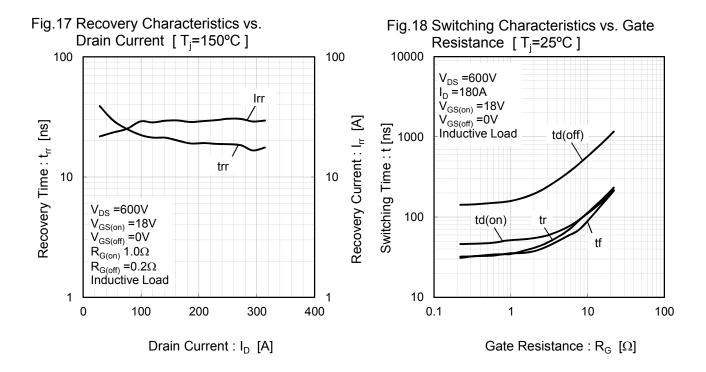


Fig. 19 Switching Characteristics vs. Gate Resistance [ $T_j$ =125°C]  $V_{DS} = 600V$   $I_D = 180A$   $V_{GS(on)} = 18V$   $V_{GS(off)} = 0V$ Inductive Load 100 0.1 1 10 0.1 1 10 0.1 1 10 0.1 1 10 100 100 100 100 100 100 100 100 100

Fig.20 Switching Characteristics vs. Gate Resistance [ $T_j$ =150°C]  $V_{DS} = 600V$   $V_{DS} = 180A$   $V_{GS(on)} = 18V$   $V_{GS(off)} = 0V$ Inductive Load tt 0.1 1 10 100Gate Resistance :  $R_G$  [ $\Omega$ ]

#### • Electrical characteristic curves (Typical)

Fig.21 Switching Loss vs. Gate Resistance [T<sub>i</sub>=25°C] 14 V<sub>DS</sub> =600V I<sub>D</sub> =180A 12  $V_{GS(on)} = 18V$   $V_{GS(off)} = 0V$ Inductive Load 10 Switching Loss [mJ] 8 6 4  $\mathsf{E}_{\mathsf{off}}$ 2  $\mathsf{E}_{\mathsf{rr}_{\!_{\!1}}}$ 0 0.1 1 10 100 Gate Resistance :  $R_G$  [ $\Omega$ ]

Fig.22 Switching Loss vs. Gate Resistance [T<sub>j</sub>=125°C] 14  $V_{\rm DS}$  =600V  $I_{\rm D}$  =180A  $V_{\rm GS(on)}$  =18V  $V_{\rm GS(off)}$  =0V Inductive Load 12 10 8 6  $\mathsf{E}_{\mathsf{off}}$ 4 2  $\mathsf{E}_{\mathsf{rr}}$ 0 100 0.1 1 10 Gate Resistance :  $R_G$  [ $\Omega$ ]

Switching Loss [mJ]

Fig.23 Switching Loss vs. Gate Resistance [ $T_i=150^{\circ}C$ ]

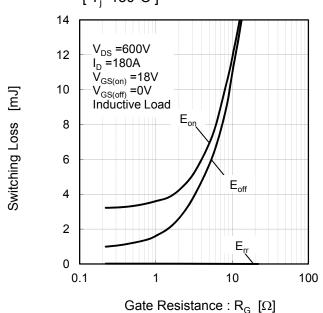
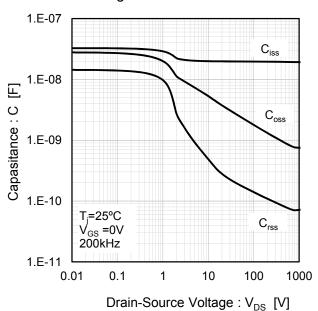
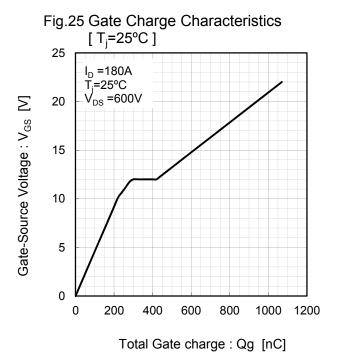
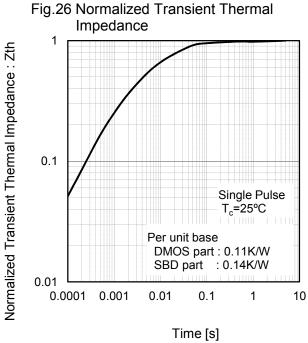


Fig.24 Typical Capacitance vs. Drain-Source Voltage



## ● Electrical characteristic curves (Typical)





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# BSM180C12P2E202 - Web Page

Part Number	BSM180C12P2E202
Package	E
Unit Quantity	4
Minimum Package Quantity	4
Packing Type	Corrugated Cardboard
Constitution Materials List	inquiry
RoHS	Yes