NEXT ASSEMBLY	REVISIONS			
	LTR		DATE	APPROVED
	Α	INITIAL RELEASE	11/01/11	FP
'			-	-

## SPECIFICATIONS:

NOMINAL FREQUENCY: 50.000 MHz FREQUENCY STABILITY: ±50 PPM (NOTE 1) **OPERATING TEMPERATURE RANGE:** -40 TO +85°C STORAGE TEMPERATURE RANGE: -55 TO +125°C 3.3VDC ± 10% INPUT VOLTAGE: INPUT CURRENT: 20mA MAX SYMMETRY: 45% To 55% RISE AND FALL TIME: 20 ns MAX

LOAD: 15 pF START-UP TIME: 10 ms MAX

OUTPUT VOLTAGE: LOGIC "0" LEVEL = 0.33VDC MAX

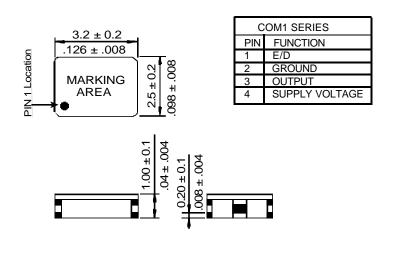
LOGIC "1" LEVEL = 2.97VDC MIN

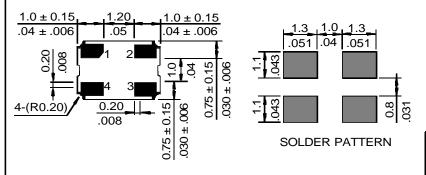
ENABLE / DISABLE: PIN 1: HIGH OR OPEN; PIN 3: OSCILLATION

PIN 1: LOW; PIN 3 HIGH IMPEDANCE

PACKAGING: TAPE AND REEL

NOTE 1: INCLUSIVE OF 25°C TOLERANCE, OPERATING TEMPERATURE RANGE, VOLTAGE OR LOAD CHANGES, AGING, SHOCK AND VIBRATION.





## **ROHS COMPLIANT / LEAD FREE**

## RALTRON 10400 NW 33rd STREET MIAMI, FL 33172 ALL DIMENSIONS ARE IN mm/inch UNLESS OTHERWISE SPECIFIED DRAWN BY: RG CHECKED BY: GC

DATE:11/01/11

SHEET 1 OF

APPROVED BY: FP

SCALE:

CAGE CODE:

**ØVUEØ** 

DRAWING TITLE:

## SURFACE MOUNT MICROPROCESSOR CRYSTAL

DRAWING NUMBER:

COM1305-50.000-EXT-T-TR