TOSHIBA CMOS Integrated Circuit Silicon Monolithic

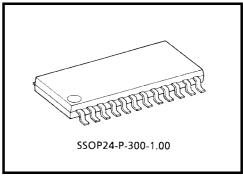
TB6548F

3-Phase Full-Wave PWM Sensorless Controller for Brushless DC Motors

TB6548F is a 3-phase full-wave sensorless controller for brushless DC motors. It is capable of controlling voltage by PWM signal input. It is capable of PWM type sensorless driving when used conjunction with TA84005F

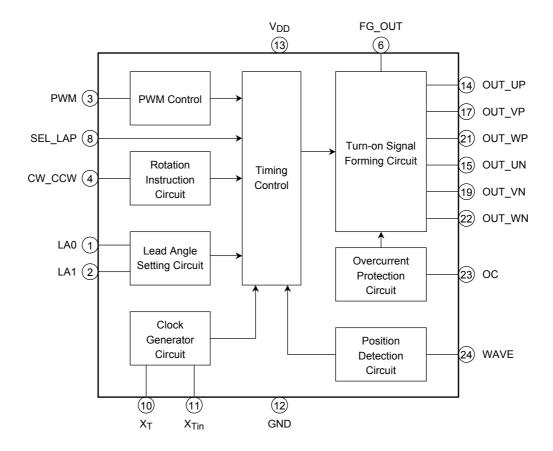
Features

- 3-phase full-wave sensorless drive
- PWM control (PWM signal is supplied from external sources.)
- Turn-on signal output current: 20 mA
- Built-in protection against overcurrent
- Forward/reverse modes
- Built-in lead angle control function (0, 7.5, 15 and 30 degrees)
- Built-in lap turn-on function

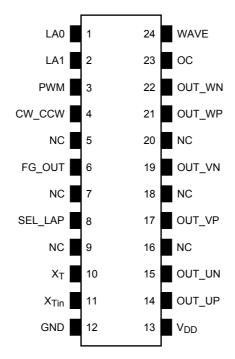


Weight: 0.32 g (typ.)

Block Diagram



Pin Assignment



Pin Description

Pin No.	Symbol	I/O	Description			
1	LA0	ı	 Lead angle setting signal input pin LA0 = Low, LA1 = Low: Lead angle 0 degree LA0 = High, LA1 = Low: Lead angle 7.5 degree 			
2	LA1	I	 LA0 = Low, LA1 = High: Lead angle 15 degree LA0 = High, LA1 = High: Lead angle 30 degree Built-in pull-down resistor 			
3	PWM	I	PWM signal input pin Inputs Low-active PWM signal Built-in pull-up resistor Disables input of duty-100% (Low) signal High for 250 ns or longer is required.			
4	cw_ccw	I	 Rotation direction signal input pin High: Reverse (U → W → V) Low, Open: Forward (U → V → W) Built-in pull-down resistor 			
5	NC	_	Not connected			
6	FG_OUT	0	Number of ratation detection signal output pin • Equiralent to U-phase signal (except PWM)			
7	NC	_	Not connected			
8	SEL_LAP	I	Lap turn-on select pin Low: Lap turn-on High: 120 degrees turn-on Built-in pull-up resistor			
9	NC	_	Not connected			
10	X _T	_	Resonator connecting pin			
11	X _{Tin}	_	Selects starting commutation frequency. Starting commutation frequency f _{st} = Resonator frequency f _{xt} /(6 × 2 ¹⁷)			
12	GND	_	Connected to GND.			
13	V _{DD}		Connected to 5-V power supply.			
14	OUT_UP	0	U-phase upper turn-on signal output pin U-phase winding wire positive ON/OFF switching pin ON: Low, OFF: High			
15	OUT_UN	0	U-phase lower turn-on signal output pin U-phase winding wire negative ON/OFF switching pin ON: High, OFF: Low			
16	NC	_	Not connected			
17	OUT_VP	0	V-phase upper turn-on signal output pin V-phase winding wire positive ON/OFF switching pin ON: Low, OFF: High			
18	NC	_	Not connected			
19	OUT_VN	0	V-phase lower turn-on signal output pin V-phase winding wire negative ON/OFF switching pin ON: High, OFF: Low			
20	NC		Not connected			

Pin No.	Symbol	I/O	Description			
21	OUT_WP	0	W-phase upper turn-on signal output pin W-phase winding wire positive ON/OFF switching pin ON: Low, OFF: High			
22	OUT_WN	0	W-phase lower turn-on signal output pin W-phase winding wire negative ON/OFF switching pin ON: High, OFF: Low			
23	ос	I	Overcurrent signal input pin High on this pin can put constraints on the turn-on signal which is performing PWM control. Built-in pull-up resistor			
24	WAVE	I	Positional signal input pin Inputs majority logic synthesis signal of three-phase pin voltage. Built-in pull-up resistor			

Functional Description

1. Sensorless Drive

On receipt of PWM signal start instruction turn-in signal for forcible commutation (commutation irrespective of the motor's rotor position) is output and the motor starts to rotate. The motor's rotation causes induced voltage on winding wire pin for each phase.

When signals indicating positive or negative for pin voltage (including induced voltage) for each phase are input on respective positional signal input pin, the turn-on signal for forcible commutation is automatically switched to turn-on signal for positional signal (induced voltage).

Thereafter turn-on signal is formed according to the induced voltage contained in the pin voltage so as to drive the brushless DC motor.

2. Starting commutation frequency (resonator pin and counter bit select pin)

The forcible commutation frequency at the time of start is determined by the resonator's frequency and the number of counter bit (within the IC).

Starting commutation frequency f_{st} = Resonator frequency $f_{xt}/(6 \times 2^{(bit + 3)})$ bit = 14

The forcible commutation frequency at the time of start can be adjusted using inertia of the motor and load.

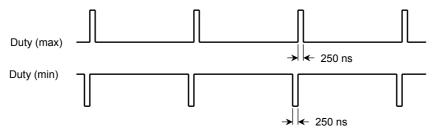
- The forcible commutation frequency should be set higher as the number of magnetic poles increases.
- The forcible commutation frequency should be set lower as the inertia of the load increases.

3. PWM Control

PWM signal can be reflected in turn-on signal by supplying PWM signal from external sources.

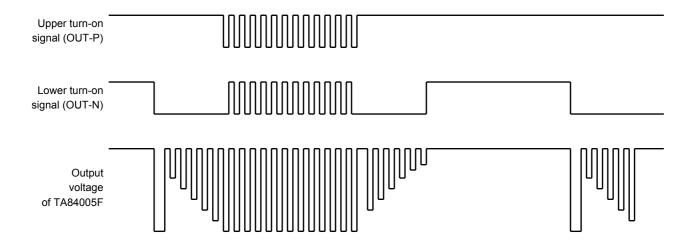
The frequency of the PWM signal should be set adequately high with regard to the electrical frequency of the motor and in accordance to the switching characteristics of the drive circuit.

Because positional detection is performed in synchronization with the falling edges of PWM signal, positional detection cannot be performed with 0% duty or 100% duty.



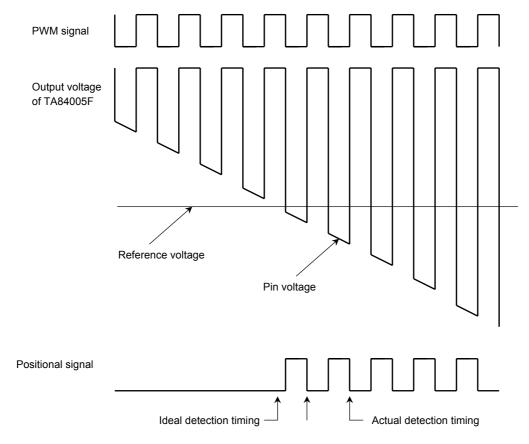
The voltage applied to the motor is duty 100% because of the storage time of the drive circuit even if the duty is 99%.

4. PWM Control



5. Positional Variation

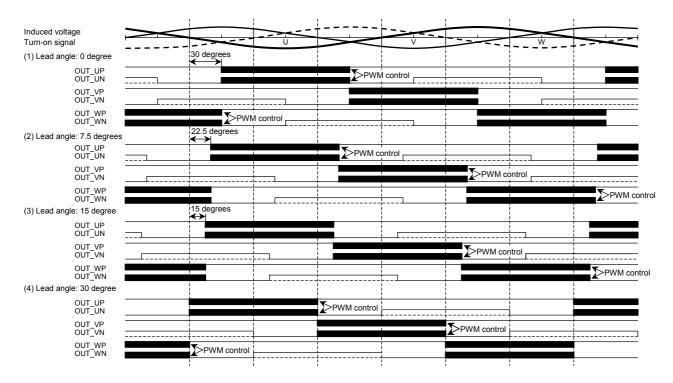
Since positional detection is performed in synchronization with PWM signal, positional variation occurs in connection with the frequency of PWM signal. Be especially careful when the IC is used for high-speed motors.



Variation is calculated by detecting at two consecutive rising edges of PWM signal. $1/f_p < Detection\ time\ variation < 2/f_p$ fp: PWM frequency

6. Lead Angle Control

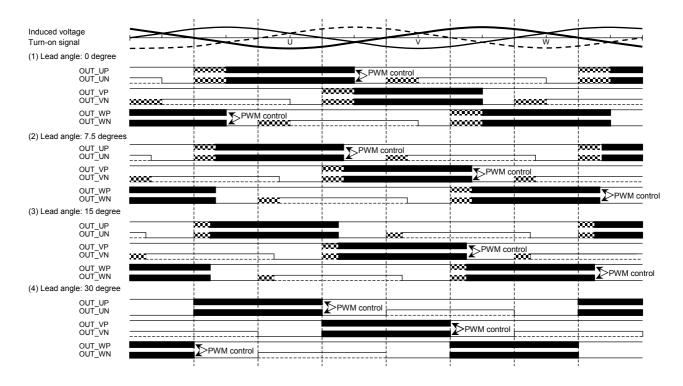
The lead angle is 0 degree during the starting forcible commutation and when normal commutation is started, automatically changes to the lead angle which has been set using LA0 and LA1. However, if both LA0 and LA1 are set for High, the lead angle is 30 degrees in the starting forcible commutation as well as in normal commutation.



7. Lap Turn-on Control

When SEL_LAP = High, the turn-on degree is 120 degrees. When SEL_LAP = Low, Lap Turn-on Mode starts.

In Lap Turn on Mode, the time between zero cross point and the 120 degrees turn on timing becomes longer (shaded area in the below chart) so as to create some overlap when switching turn on signals. The lap time differs depending ong the lead angle setting.

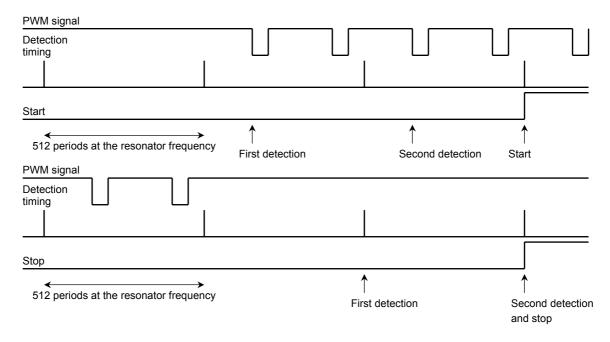


8. Start/Stop Control

Start/Stop is controlled using PWM signal input pin.

A stop is acknowledged when PWM signal duty is 0, and a start is acknowledged when ON-signal of a frequency 4 times higher than the resonator frequency or even higher is input continuously.

Timing chart



Note: Take sufficient care for noise on PWM signal input pin.

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V_{DD}	5.5	٧
Input voltage	V _{in}	-0.3 to $V_{DD} + 0.3$	٧
Turn-on signal output current	lout	20	mA
Power dissipation	PD	590	mW
Operating temperature	T _{opr}	-30 to 85	°C
Storage temperature	T _{stg}	-55 to 150	°C

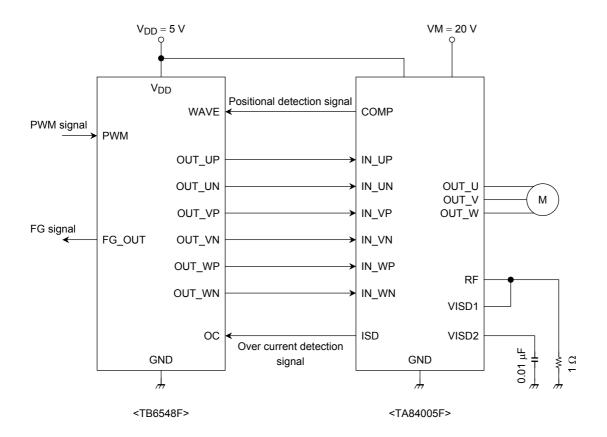
Recommended Operating Conditions ($Ta = -30 \text{ to } 85^{\circ}\text{C}$)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Power supply voltage	V_{DD}	_	4.5	5.0	5.5	V
Input voltage	V _{in}	_	-0.3	_	V _{DD} + 0.3	V
PWM frequency	f _{PWM}	_	_	16	_	kHz
Oscillation frequency	f _{osc}	-	1.0	_	10	MHz

Electrical Characteristics (Ta = 25°C, V_{DD} = 5 V)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Static power supply current	I_{DD}	_	PWM = H, X _{Tin} = H	_	0.1	0.3	mA
Dynamic power supply current	I _{DD (opr)}	_	PWM = 50% Duty, X _{Tin} = 4 MHz	_	1	3	mA
	I _{IN-1} (H)	_	V _{IN} = 5 V, PWM, OC, WAVE_U, SEL_LAP	_	0	1	
Input current	I _{IN-1} (L)	_	V _{IN} = 0 V, PWM, OC, WAVE_U, SEL_LAP	-75	-50	_	μΑ
	I _{IN-2} (H)	_	V _{IN} = 5 V, CW_CCW, LA0, LA1	_	50	75	
	I _{IN-2} (L)	_	V _{IN} = 0 V, CW_CCW, LA0, LA1	-1	0	_	
	V _{IN} (H)	_	PWM, OC, SEL_LAP, CW_CCW WAVE_U, LA0, LA1	3.5	_	5	
Input voltage	V _{IN} (L)	_	PWM, OC, SEL_LAP, CW_CCW WAVE_U, LA0, LA1	GND	_	1.5	V
Input hysteresis voltage	V _H	_	PWM, OC, SEL_LAP, CW_CCW WAVE_U, LA0, LA1	_	0.6	_	V
	V _{O-1} (H)	_	I _{OH} = -1 mA OUT_UP, OUT_VP, OUT_WP	4.3	_	V _{DD})
	V _{O-1} (L)	_	I _{OL} = 20 mA OUT_UP, OUT_VP, OUT_WP	GND	_	0.5	
0	V _{O-2} (H)	_	I _{OH} = -20 mA OUT_UN, OUT_VN, OUT_WN	4.0	_	V _{DD}	
Output voltage	V _{O-2} (L)		I _{OL} = 1 mA OUT_UN, OUT_VN, OUT_WN	GND	_	0.5	V
	V _{O-3} (H)	_	I _{OH} = -0.5 mA FG_OUT	4.0	_	V _{DD}	
	V _{O-3} (L)	_	I _{OL} = 0.5 mA FG_OUT	GND	_	0.5	
	I _L (H)	_	V _{DD} = 5.5 V, V _{OUT} = 0 V OUT_UP, OUT_VP, OUT_WP OUT_UN, OUT_VN, OUT_WN FG_OUT	_	0	10	
Output leak current	l <u>լ</u> (L)	_	V _{DD} = 5.5 V, V _{OUT} = 5.5 V OUT_UP, OUT_VP, OUT_WP OUT_UN, OUT_VN, OUT_WN FG_OUT	_	0	10	μΑ
	t _{pLH}			_	0.5	1	μS
Output delay time	t _{pHL}	 -	PWM-Output	_	0.5	1	

Application Circuit Example



Note 1: Take enough care in designing output V_{DD} line and GND line to avoid short circuit between outputs, V_{DD} fault or GND fault which may cause the IC to break down.

Note 2: The above application circuit and values mentioned are just an example for reference. Since the values may vary depending on the motor to be used, appropriate values must be determined through experiments before using the device.

Package Dimensions

2	

Weight: 0.32 g (typ.)

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Handbook" etc..

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