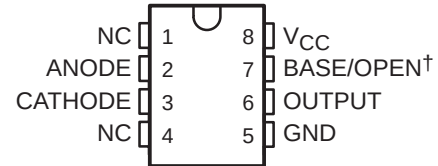


6N135, 6N136, HCPL4502 OPTOCOUPLEDERS/OPTOISOLATORS

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- Compatible with TTL Inputs
- High-Speed Switching . . . 1 Mbit/s Typ
- Bandwidth . . . 2 MHz Typ
- High Common-Mode Transient Immunity . . . 1000 V/ μ s Typ
- High-Voltage Electrical Insulation . . . 3000 Vdc Min
- Open-Collector Output
- UL Recognized . . . File Number 65085

6N135, 6N136, OR HCPL4502 PACKAGE
(TOP VIEW)



† Terminal 7 is BASE on the 6N135 and 6N136 and OPEN on the HCPL4502

NC – No internal connection

description

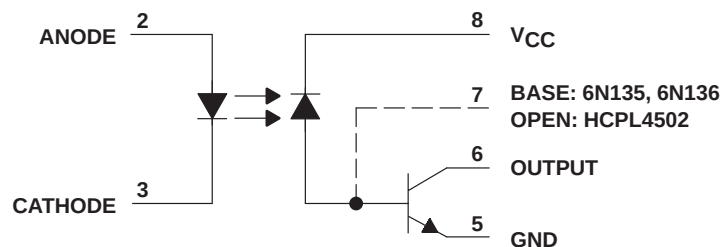
These high-speed optocouplers are designed for use in analog or digital interface applications that require high-voltage isolation between the input and output. Applications include line receivers that require high common-mode transient immunity, and analog or logic circuits that require input-to-output electrical isolation.

The 6N135, 6N136, and HCPL4502 optocouplers each consists of a light-emitting diode and an integrated photon detector composed of a photodiode and an open-collector output transistor. Separate connections are provided for the photodiode bias and the transistor-collector output. This feature, which reduces the transistor base-to-collector capacitance, results in speeds up to one hundred times that of a conventional phototransistor optocoupler.

The 6N135 is designed for TTL/CMOS, TTL/LSTTL, and wide-band analog applications.

The 6N136 and HCPL4502 are designed for high-speed TTL/TTL applications. The HCPL4502 has no base connection.

schematic



6N135, 6N136, HCPL4502 OPTOCOUPERS/OPTOISOLATORS

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absolute maximum ratings at 25°C free-air temperature (unless otherwise noted)^{†‡}

Supply and output voltage range, V_{CC} and V_O	–0.5 V to 15 V
Reverse input voltage	5 V
Emitter-base reverse voltage	5 V
Peak input forward current (pulse duration = 1 ms, 50% duty cycle, see Note 1)	50 mA
Peak transient input forward current (pulse duration 1 μ s, 300 Hz)	1 A
Average forward input current(see Note 2)	25 mA
Peak output current	16 mA
Average output current	8 mA
Base current	5 mA
Input power dissipation at (or below) 70°C free-air temperature (see Note 3)	45 mW
Output power dissipation at (or below) 70°C free-air temperature (see Note 4)	100 mW
Storage temperature range, T_{stg}	–55°C to 125°C
Operating free-air temperature range, T_A	–55°C to 100°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C

[†] Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

[‡] JEDEC registered data for 6N135 and 6N136

- NOTES:
1. Derate linearly above 70°C free-air temperature at the rate of 1.67 mA/°C.
 2. Derate linearly above 70°C free-air temperature at the rate of 0.83 mA/°C.
 3. Derate linearly above 70°C free-air temperature at the rate of 1.50 mW/°C.
 4. Derate linearly above 70°C free-air temperature at the rate of 3.33 mW/°C.



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6N135, 6N136, HCPL4502 OPTOCOUPERS/OPTOISOLATORS

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electrical characteristics over operating free-air temperature range of 0°C to 70°C (unless otherwise noted)

PARAMETER		TEST CONDITIONS		6N135			6N136, HCPL4502			UNIT
				MIN	TYP†	MAX	MIN	TYP†	MAX	
V _F [‡]	Input forward voltage	I _F = 16 mA, T _A = 25°C		1.6		1.7	1.6		1.7	V
∞V _F	Temperature coefficient of forward voltage	I _F = 16 mA		−1.8			−1.8			mV/°C
V _{BR} [‡]	Input breakdown voltage	I _R = 10 μA, T _A = 25°C		5			5			V
V _{OL}	Low-level output voltage	V _{CC} = 4.5 V, I _F = 16 mA, I _B = 0	I _{OL} = 1.1 mA	0.1		0.4				V
			I _{OL} = 2.4 mA			0.1		0.4		
I _{OH} [‡]	High-level output current	I _F = 0, I _B = 0, T _A = 25°C	V _{CC} = V _O = 5.5 V	3		500	3		500	nA
			V _{CC} = V _O = 15 V	0.01		1	0.01		1	μA
I _{OH}	High-level output current	V _{CC} = 15 V, I _F = 0,	V _O = 15 V, I _B = 0			50			50	μA
I _{CCH} [‡]	Supply current, high-level output	V _{CC} = 15 V, I _F = 0, T _A = 25°C	I _O = 0, I _B = 0,	0.02		1	0.02		1	μA
I _{CCH}	Supply current, high-level output	V _{CC} = 15 V, I _F = 0,	I _O = 0, I _B = 0			2			2	μA
I _{CCL}	Supply current, low-level output	V _{CC} = 15 V, I _F = 16 mA,	I _O = 0, I _B = 0	40			40			μA
h _{FE}	Transistor forward current transfer ratio	V _O = 5 V,	I _O = 3 mA	100			100 (6N136 only)			
CTR [‡]	Current transfer ratio	V _{CC} = 4.5 V, I _F = 16 mA, T _A = 25°C,	V _O = 0.4 V, I _B = 0, See Note 5	7%		18%	19%		24%	
CTR	Current transfer ratio	V _{CC} = 4.5 V, I _F = 16 mA, See Note 5	V _O = 0.5 V, I _B = 0,	5%			15%			
r _{IO}	Input-output resistance	V _{IO} = 500 V, See Note 6	T _A = 25°C,	10 ¹²			10 ¹²			Ω
I _{IO} [‡]	Input-output insulation leakage current	V _{IO} = 3000 V, T _A = 25°C, See Note 6	t = 5 s, RH = 45%,			1			1	μA
C _i	Input capacitance	V _F = 0,	f = 1 MHz	60			60			pF
C _{io}	Input-output capacitance	f = 1 MHz,	See Note 6	0.6			0.6			pF

[†] All typical values are at $T_A = 25^\circ\text{C}$.

[‡] JEDEC registered data for 6N135 and 6N136

NOTES: 5. Current transfer ratio is defined as the ratio of output collector current I_O to the forward LED input current I_F times 100%.
6. These parameters are measured with terminals 2 and 3 shorted together and terminals 5, 6, 7, and 8 shorted together.

6N135, 6N136, HCPL4502 OPTOCOUPPLERS/OPTOISOLATORS

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operating characteristics, $V_{CC} = 5\text{ V}$, $I_F = 16\text{ mA}$, $T_A = 25^\circ\text{C}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	6N135			6N136, HCPL4502			UNIT
		MIN	TYP	MAX	MIN	TYP	MAX	
BW Bandwidth (–3 dB)	$R_L = 100\ \Omega$, See Note 7		2			2		MHz

NOTE 7: Bandwidth is the range of frequencies within which the ac output voltage is not more than 3 dB below the low-frequency value.

switching characteristics at $V_{CC} = 5\text{ V}$, $I_F = 16\text{ mA}$, $T_A = 25^\circ\text{C}$

PARAMETER		TEST CONDITIONS		6N135			6N136, HCPL4502			UNIT
				MIN	TYP	MAX	MIN	TYP	MAX	
$t_{PLH}^\dagger$	Propagation delay time, low-to-high-level output	$R_L = 4.1\text{ k}\Omega$, See Figure 1	See Note 8,		1	1.5				μs
		$R_L = 1.9\text{ k}\Omega$, See Figure 1	See Note 9,					0.6	0.8	
$t_{PHL}^\dagger$	Propagation delay time, high-to-low-level output	$R_L = 4.1\text{ k}\Omega$, See Figure 1	See Note 8,		0.7	1.5				μs
		$R_L = 1.9\text{ k}\Omega$, See Figure 1	See Note 9,					0.6	0.8	
$\frac{dV_{CM}}{dt} (H)$	Common-mode input transient immunity, high-level output	$\Delta V_{CM} = 10\text{ V}$, $R_L = 4.1\text{ k}\Omega$, See Figure 2	$I_F = 0$, See Notes 8 and 10,		1000					$\text{V}/\mu\text{s}$
		$\Delta V_{CM} = 10\text{ V}$, $R_L = 1.9\text{ k}\Omega$, See Figure 2	$I_F = 0$, See Notes 9 and 10,					1000		
$\frac{dV_{CM}}{dt} (L)$	Common-mode input transient immunity, low-level output	$\Delta V_{CM} = 10\text{ V}$, See Notes 9 and 10,	$R_L = 4.1\text{ k}\Omega$, See Figure 2		1000					$\text{V}/\mu\text{s}$
		$\Delta V_{CM} = 10\text{ V}$, See Notes 9 and 10,	$R_L = 1.9\text{ k}\Omega$, See Figure 2					1000		

† JEDEC registered data for 6N135 and 6N136

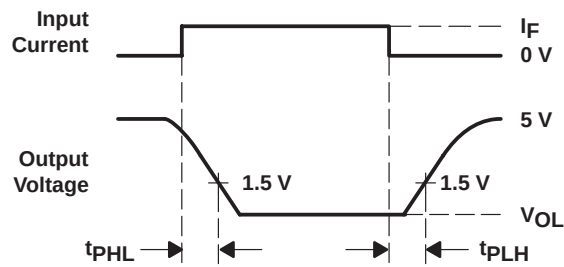
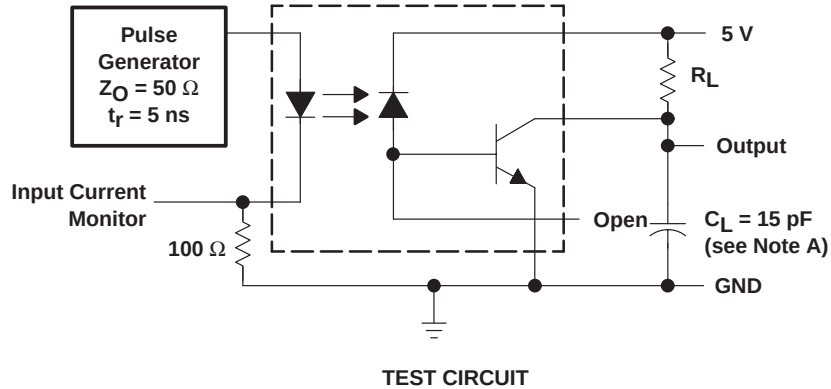
NOTES: 8. The 4.1-k Ω load represents one LSTTL unit load of 0.36 mA and a 6.1-k Ω pullup resistor.

9. The 1.9-k Ω load represents one TTL unit load of 1.6 mA and a 5.6-k Ω pullup resistor.

10. Common-mode transient immunity, high-level output, is the maximum rate of rise of the common-mode input voltage that does not cause the output voltage to drop below 2 V. Common-mode input transient immunity, low-level output, is the maximum rate of fall of the common-mode input voltage that does not cause the output voltage to rise above 0.8 V.



PARAMETER MEASUREMENT INFORMATION



NOTE A: C_L includes probe and stray capacitance.

Figure 1. Switching Test Circuit and Waveforms

PARAMETER MEASUREMENT INFORMATION

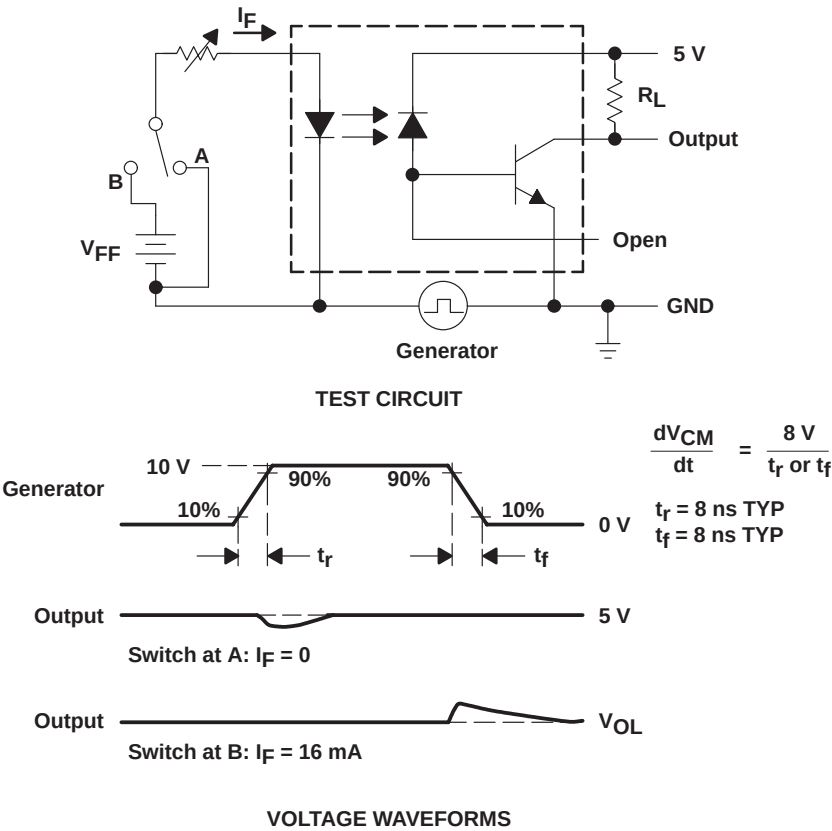


Figure 2. Transient Immunity Test Circuit and Waveforms

TYPICAL CHARACTERISTICS

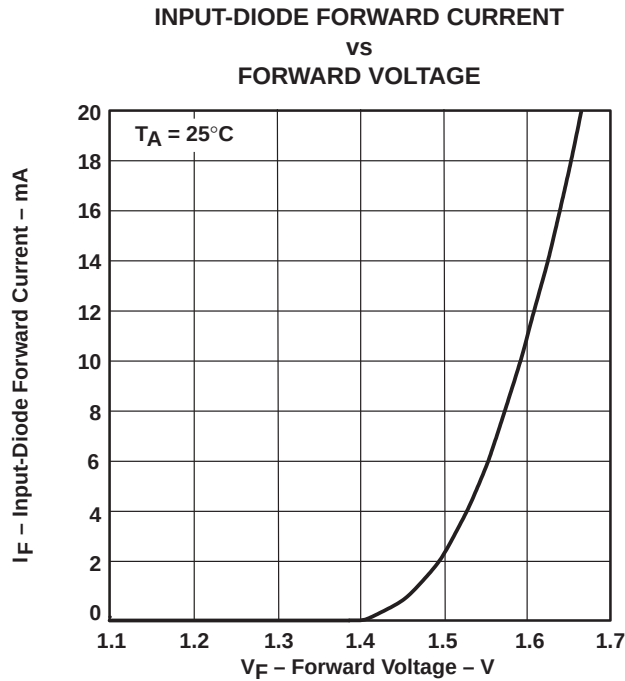


Figure 3

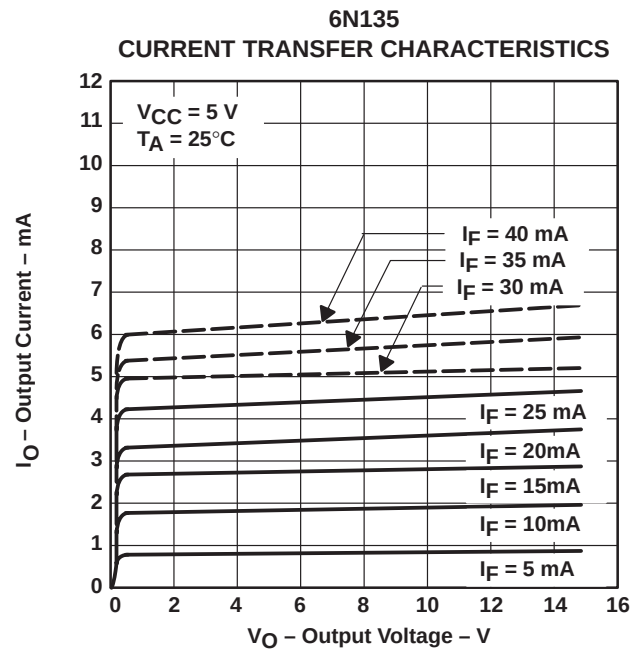


Figure 4

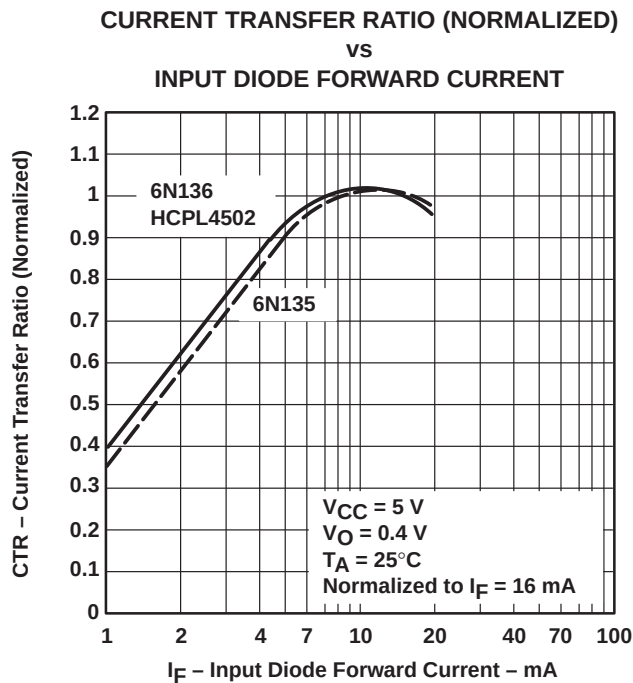


Figure 5

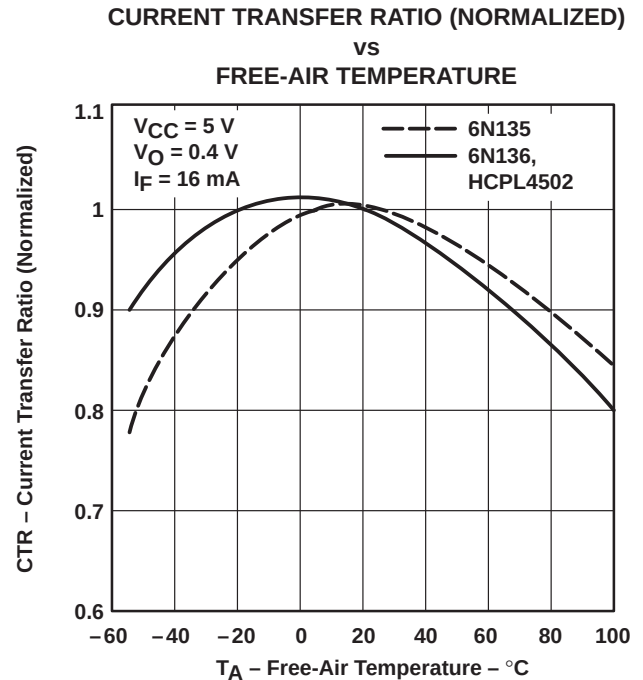


Figure 6

6N135, 6N136, HCPL4502 OPTOCOUPLEDERS/OPTOISOLATORS

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TYPICAL CHARACTERISTICS

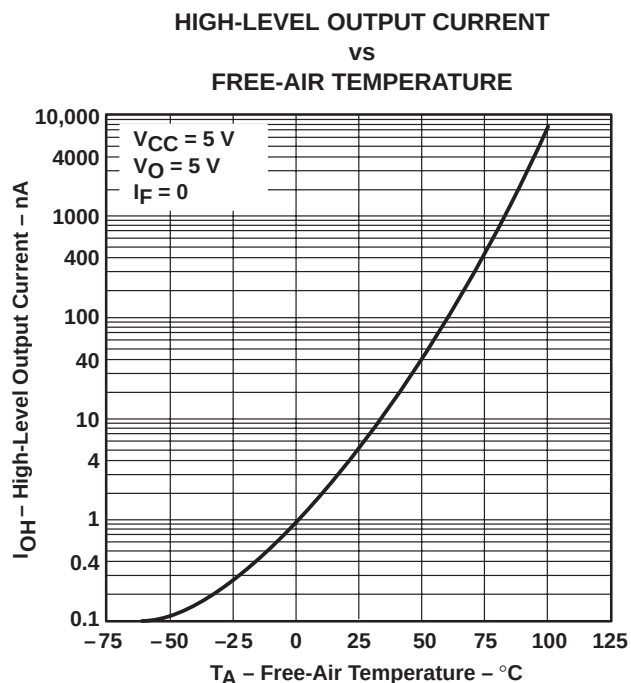


Figure 7

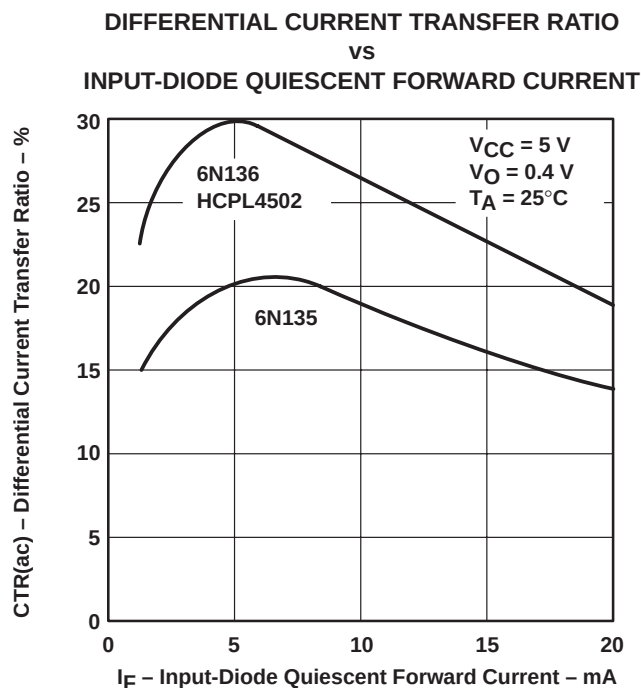


Figure 8

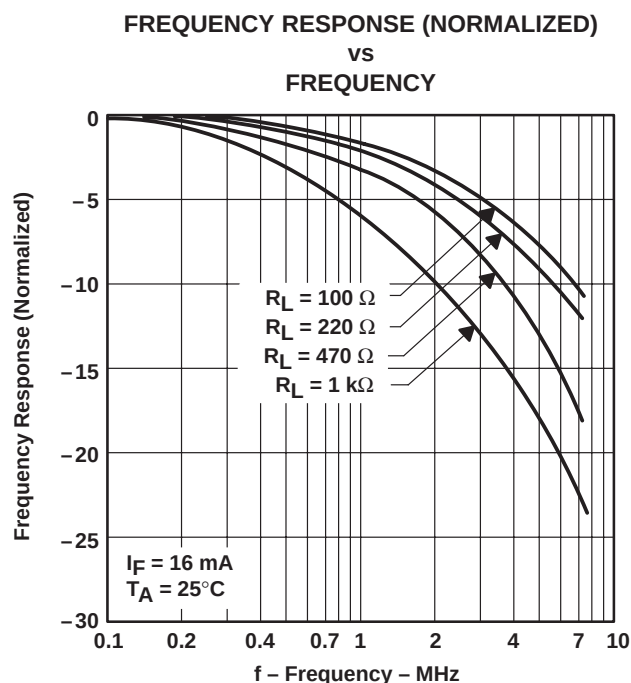


Figure 9

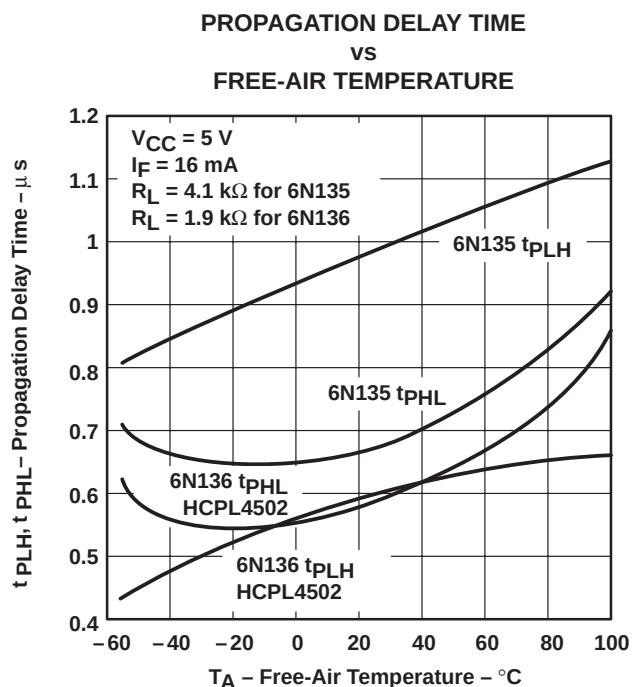
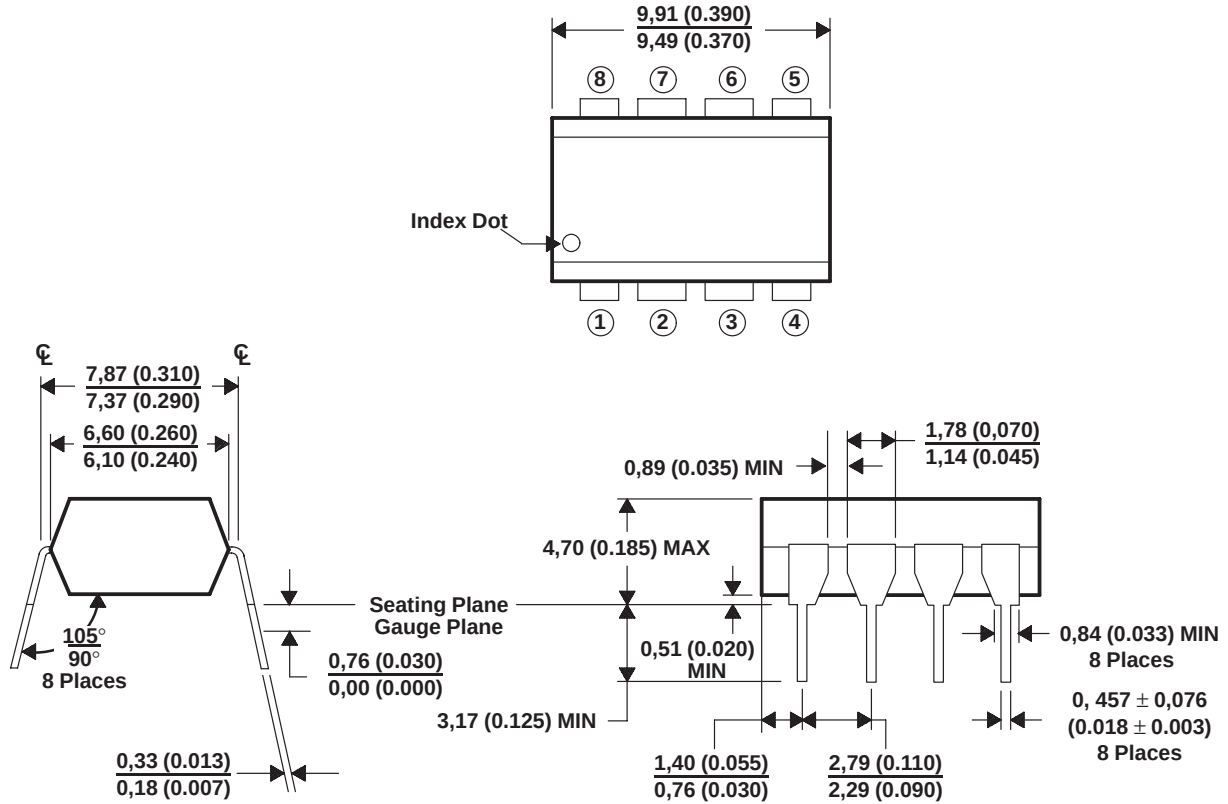


Figure 10

MECHANICAL INFORMATION



NOTES: A. JEDEC registered data. This data sheet contains all applicable registered data in effect at the time of publication.
B. All linear dimensions are given in millimeters and parenthetically given in inches.

Figure 11. Packaging Specifications

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
6N135	OBSOLETE	PDIP	N	8		TBD	Call TI	Call TI
6N136	OBSOLETE	PDIP	N	8		TBD	Call TI	Call TI
HCPL4502	OBSOLETE	PDIP	N	8		TBD	Call TI	Call TI

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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