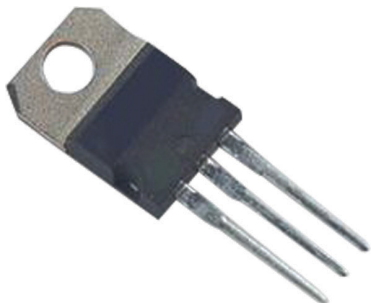


High Voltage Power Transistor



Description:

High Voltage NPN Silicon Power Transistors are designed for line operated audio output amplifier, and switching power supply drivers applications.

Features:

- Collector-Emitter sustaining voltage- 250V - 400V (Min.)
- 1A Rated collector current
- $f_T = 10\text{MHz}$ (Min.) at $I_C = 200\text{mA}$

Maximum Ratings

Characteristic	Symbol	TIP47	TIP50	Unit
Collector-Emitter Voltage	V _{CEO}	250	400	V
Collector-Base Voltage	V _{CBO}	350	500	
Emitter-Base Voltage	V _{EBO}	5		
Collector Current-Continuous -Peak	I _C	1 2		A
Base Current	I _B	0.6		
Total Power Dissipation at T _C = 25°C Derate above 25°C	P _D	40 0.32		W W/°C
Operation and Storage Junction Temperature Range	T _J , T _{STG}	-65 to +150		°C

Thermal Characteristics

Characteristic	Symbol	Max.	Unit
Thermal Resistance Junction to Case	$R_{\theta jc}$	3.125	$^\circ\text{C/W}$

High Voltage Power Transistor

Electrical Characteristics ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min.	Max.	Unit
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OFF Characteristics

Collector-Emitter Sustaining Voltage (1) $I_C = 30\text{mA}$, $I_B = 0$ TIP47 TIP50	$V_{CE(sus)}$	250 400	-	V
Collector Cut off Current $V_{CE} = 150\text{V}$, $I_B = 0$ $V_{CE} = 300\text{V}$, $I_B = 0$ TIP47 TIP50	I_{CEO}	-	1	mA
Collector Cut off Current $V_{CE} = 350\text{V}$, $V_{EB} = 0$ $V_{CE} = 500\text{V}$, $V_{EB} = 0$ TIP47 TIP50	I_{CES}	-	1	
Emitter Cut off Current $V_{EB} = 5\text{V}$, $I_C = 0$	I_{EBO}	-	1	

ON Characteristics (1)

DC Current Gain $I_C = 0.3\text{A}$, $V_{CE} = 10\text{V}$ $I_C = 1.0\text{A}$, $V_{CE} = 10\text{V}$	h_{FE}	30 10	150	-
Collector-Emitter Saturation Voltage $I_C = 1\text{A}$, $I_B = 200\text{mA}$	$V_{CE(sat)}$	-	1	V
Base-Emitter On Voltage $I_C = 1\text{A}$, $V_{CE} = 10\text{V}$	$V_{BE(on)}$	-	1.5	

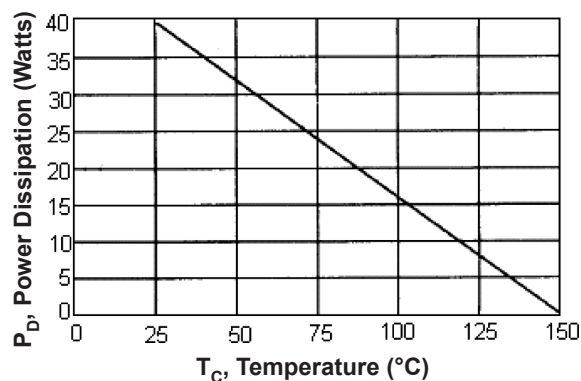
Dynamic Characteristics

Current Gain-Bandwidth Product (2) $I_C = 200\text{mA}$, $V_{CE} = 10\text{V}$, $f_{TEST} = 2\text{MHz}$	f_T	10	-	MHz
Small Signal Current Gain $I_C = 200\text{mA}$, $V_{CE} = 10\text{V}$, $f = 1\text{kHz}$	h_{FE}	25	-	-

(1) Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

(2) $f_T = h_{FE} \cdot f_{TEST}$

Figure - 1 Power Derating



High Voltage Power Transistor

Figure - 2 DC Current Gain

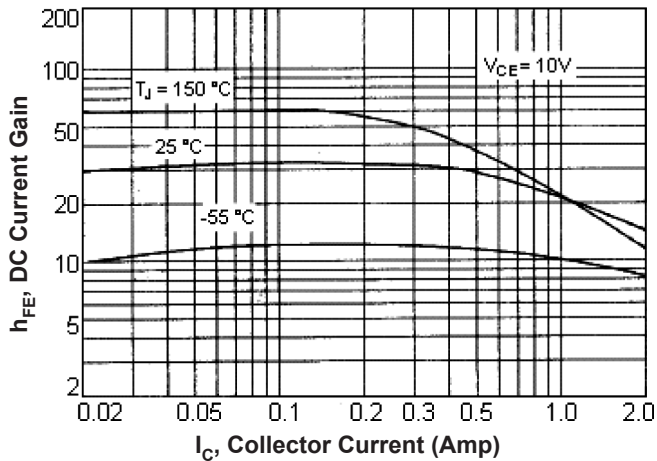


Figure - 3 Turn-On Time

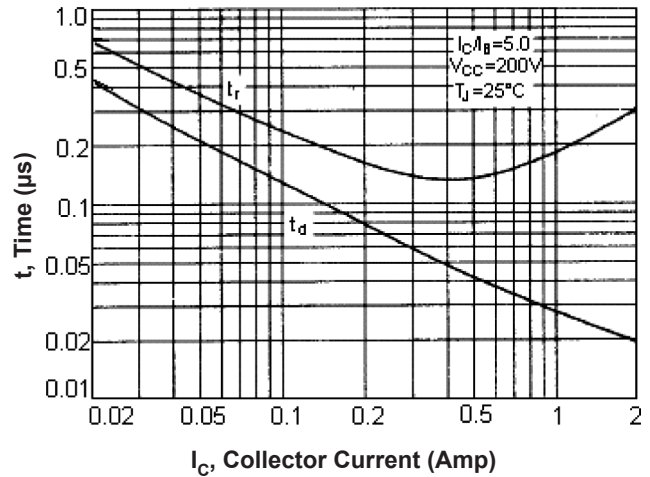


Figure - 4 "ON" Voltages

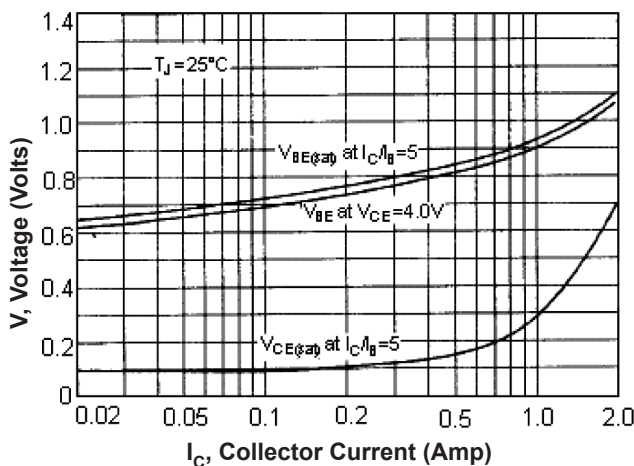


Figure - 5 Turn-Off Time

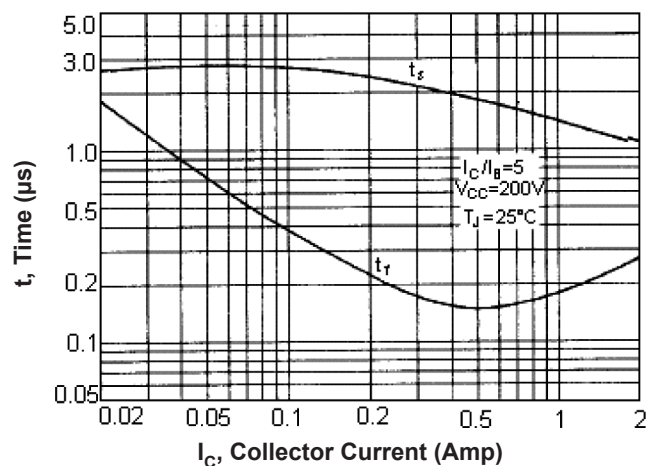
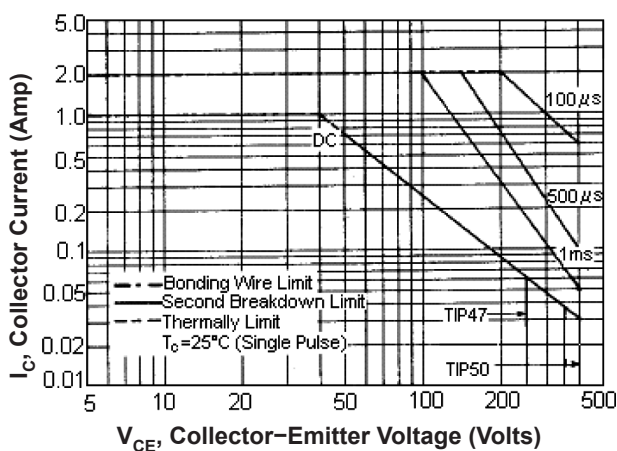


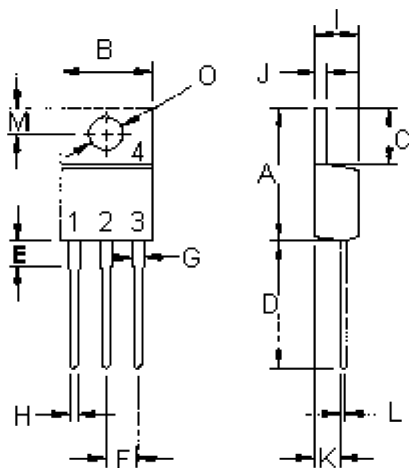
Figure - 6 Active Region Safe Operating Area



There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown safe operating area curves indicate I_C - V_{CE} limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure - 6 curve is based on $T_{J(PK)} = 150^\circ\text{C}$; T_C is variable depending on power level. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(PK)} \leq 150^\circ\text{C}$. At high case temperatures, thermal limitation will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

High Voltage Power Transistor



Pin Configuration:

1. Base
2. Collector
3. Emitter
4. Collector(Case)

Dimensions	Min.	Max.
A	14.68	15.31
B	9.78	10.42
C	5.01	6.52
D	13.06	14.62
E	3.57	4.07
F	2.42	3.66
G	1.12	1.36
H	0.72	0.96
I	4.22	4.98
J	1.14	1.38
K	2.2	2.97
L	0.33	0.55
M	2.48	2.98
O	3.7	3.9

Dimensions : Millimetres

Part Number Table

Description	Part Number
Transistor, NPN, TO-220	TIP47
	TIP50

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