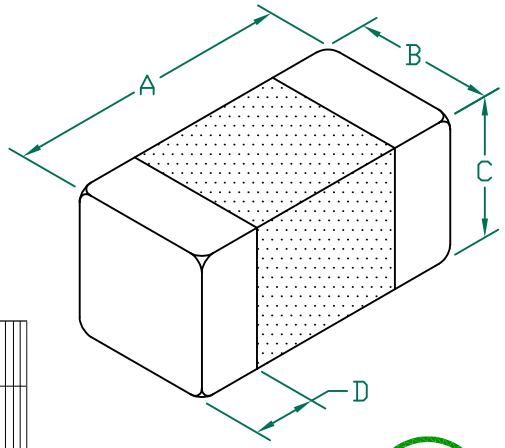
# HZ0603A182R-10

**UNCONTROLLED DOCUMENT**

## PHYSICAL DIMENSIONS:

| | | | |
|---|---|---|---|
| A | 1.60 [.063] | ± 0.15 | [.006] |
| B | 0.80 [.031] | ± 0.15 | [.006] |
| C | 0.80 [.031] | ± 0.15 | [.006] |
| D | 0.36 [.014] | ± 0.15 | [.006] |



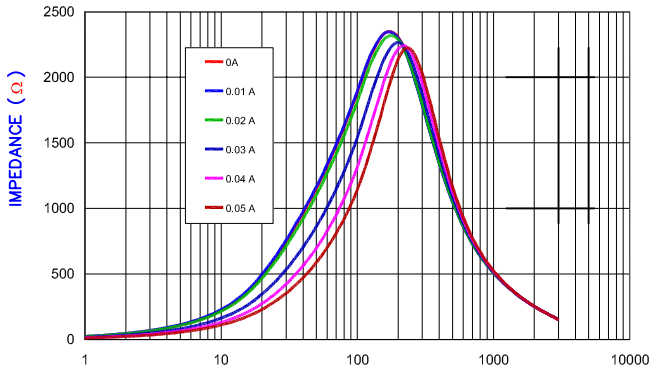

| ELECTRICAL CHARACTERISTICS: | | | |
|---|---|---|---|
| Z @ 100MHz ( Ω ) | | DCR ( Ω ) | Rated Current |
| Nominal | 1800 | | |
| Minimum | 1350 | | |
| Maximum | 2250 | 1.5 | 50 mA |

NOTES: UNLESS OTHERWISE SPECIFIED

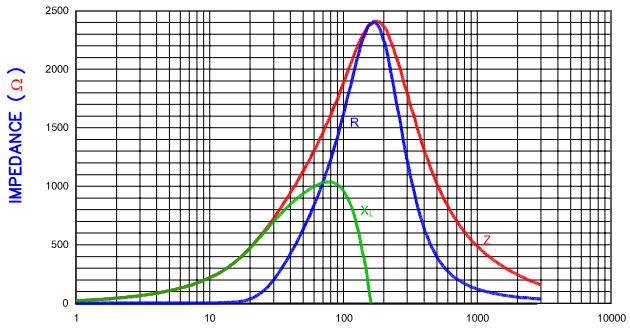
1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER CARRIER TAPE.
2. TERMINATION FINISH IS 100% TIN.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. OPERATEING TEMPERATURE TEMP: −40°C~+125°C (INCLUDING SELF−HEATING)

Z vs. FREQUENCY

IMPEDANCE UNDER DC BIAS

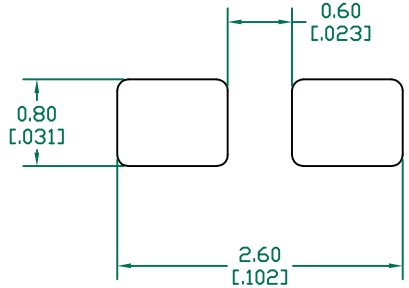



|Z| , R, AND X vs. FREQUENCY



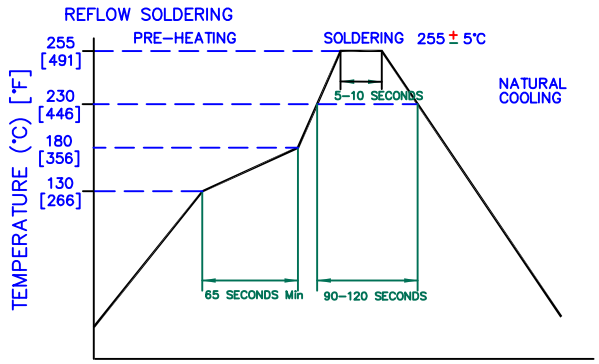

AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture. TEST REF. 5544

LAND PATTERNS FOR REFLOW SOLDERING




(For wave soldering, add 0.762 [.030] to this dimension)

RECOMMENDED SOLDERING CONDITIONS




RoHS 2002/95/EC

| REV | DESCRIPTION | DATE | INT |
|---|---|---|---|
| | DIMENSIONS ARE IN mm [INCHES]. | | |
| D | ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE | 08/05/13 | QU |
| C | UPDATE COMPANY LOGO | 02/22/08 | JRK |
| B | ADD DC BIAS CURVE | 07/25/06 | JRK |
| A | ORIGINAL DRAFT | 03/30/06 | JRK |



Laird

| PROJECT/PART NUMBER: | REV | PART TYPE: | DRAWN BY: |
|---|---|---|---|
| HZ0603A182R−10 | D | CO−FIRE | JRK |

| DATE: | SCALE: | SHEET: |
|---|---|---|
| 03/30/06 | – | 2 of 2 |

CAD # HZ0603A182R−10−D TOOL # –