

RF Power Field Effect Transistor

N-Channel Enhancement-Mode Lateral MOSFET

Designed for GSM 900 MHz frequency band, the high gain and broadband performance of this device make it ideal for large-signal, common-source amplifier applications in 26 volt base station equipment.

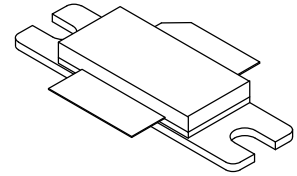
- Typical Performance for GSM Frequencies, 960 MHz, 26 Volts
Output Power @ P1db: 75 Watts
Power Gain @ P1db: 18.5 dB
Efficiency @ P1db: 55%
- Capable of Handling 5:1 VSWR, @ 26 Vdc, 921 MHz, 90 Watts CW Output Power

Features

- Internally Matched for Ease of Use
- High Gain, High Efficiency and High Linearity
- Integrated ESD Protection
- Designed for Maximum Gain and Insertion Phase Flatness
- Excellent Thermal Stability
- Characterized with Series Equivalent Large-Signal Impedance Parameters
- Available with Low Gold Plating Thickness on Leads. L Suffix Indicates 40μ" Nominal.
- RoHS Compliant
- In Tape and Reel. R3 Suffix = 250 Units per 56 mm, 13 inch Reel.

MRF9080LR3

**920-960 MHz, 75 W, 26 V
LATERAL N-CHANNEL
RF POWER MOSFET**



**CASE 465-06, STYLE 1
NI-780**

Table 1. Maximum Ratings

Rating	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	- 0.5, +65	Vdc
Gate-Source Voltage	V _{GS}	- 0.5, +15	Vdc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	250 1.43	W W/°C
Storage Temperature Range	T _{stg}	- 65 to +150	°C
Case Operating Temperature	T _C	150	°C
Operating Junction Temperature	T _J	200	°C

Table 2. Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Case	R _{θJC}	0.7	°C/W

Table 3. ESD Protection Characteristics

Test Conditions	Class
Human Body Model	1 (Minimum)
Machine Model	M1 (Minimum)

Table 4. Electrical Characteristics ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Off Characteristics					
Zero Gate Voltage Drain Leakage Current ($V_{DS} = 65\text{ Vdc}$, $V_{GS} = 0$)	I_{DSS}	—	—	10	μAdc
Zero Gate Voltage Drain Leakage Current ($V_{DS} = 26\text{ Vds}$, $V_{GS} = 0$)	I_{DSS}	—	—	1	μAdc
Gate-Source Leakage Current ($V_{GS} = 5\text{ Vdc}$, $V_{DS} = 0$)	I_{GSS}	—	—	1	μAdc
On Characteristics					
Gate Threshold Voltage ($V_{DS} = 10\text{ Vdc}$, $I_D = 300\text{ }\mu\text{Adc}$)	$V_{GS(th)}$	2.0	—	4.0	Vdc
Gate Quiescent Voltage ($V_{DS} = 26\text{ Vdc}$, $I_D = 700\text{ mAdc}$)	$V_{GS(Q)}$	—	3.7	—	Vdc
Drain-Source On-Voltage ($V_{GS} = 10\text{ Vdc}$, $I_D = 2\text{ Adc}$)	$V_{DS(on)}$	—	0.19	0.4	Vdc
Forward Transconductance ($V_{DS} = 10\text{ Vdc}$, $I_D = 6\text{ Adc}$)	g_{fs}	—	8.0	—	S
Dynamic Characteristics ⁽¹⁾					
Output Capacitance ($V_{DS} = 26\text{ Vdc}$, $V_{GS} = 0$, $f = 1\text{ MHz}$)	C_{oss}	—	73	—	pF
Reverse Transfer Capacitance ($V_{DS} = 26\text{ Vdc}$, $V_{GS} = 0$, $f = 1\text{ MHz}$)	C_{rss}	—	2.9	—	pF
Functional Tests (In Freescale Test Fixture, 50 ohm system)					
Power Output, 1 dB Compression Point ($V_{DD} = 26\text{ Vdc}$, $I_{DQ} = 600\text{ mA}$, $f = 960\text{ MHz}$)	P_{1dB}	68	75	—	W
Common-Source Amplifier Power Gain @ 70 W (Min) ($V_{DD} = 26\text{ Vdc}$, $I_{DQ} = 600\text{ mA}$, $f = 960\text{ MHz}$)	G_{ps}	17	18.5	20	dB
Drain Efficiency @ $P_{out} = 70\text{ W}$ ($V_{DD} = 26\text{ Vdc}$, $I_{DQ} = 600\text{ mA}$, $f = 960\text{ MHz}$)	η_1	47	52	—	%
Drain Efficiency @ P_{1dB} ($V_{DD} = 26\text{ Vdc}$, $I_{DQ} = 600\text{ mA}$, $f = 960\text{ MHz}$)	η_2	—	55	—	%
Input Return Loss ($V_{DD} = 26\text{ Vdc}$, $P_{out} = 70\text{ W}$, $I_{DQ} = 600\text{ mA}$, $f = 960\text{ MHz}$)	IRL	9.5	12.5	—	dB

1. Part is internally input matched.

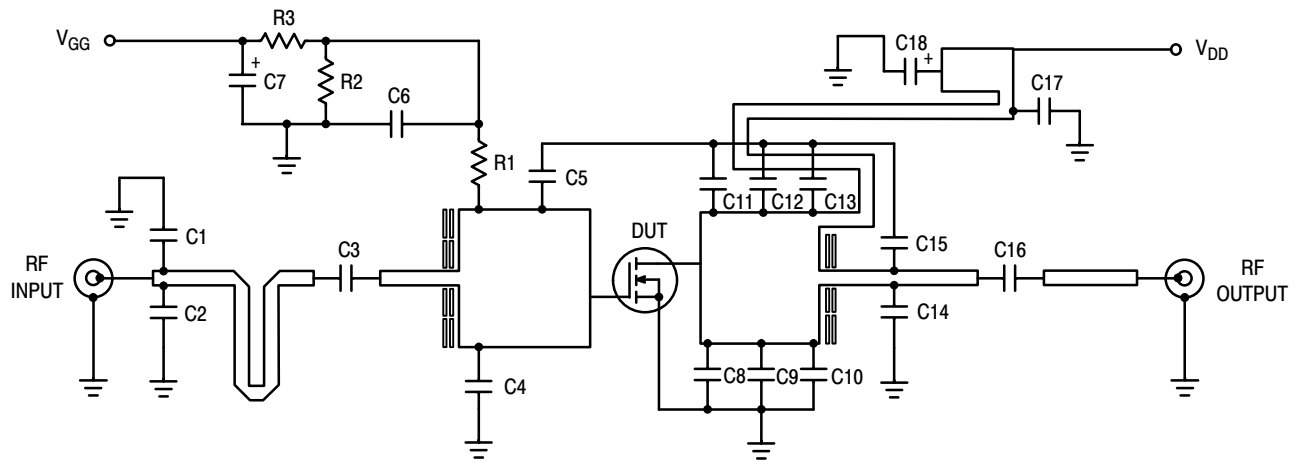
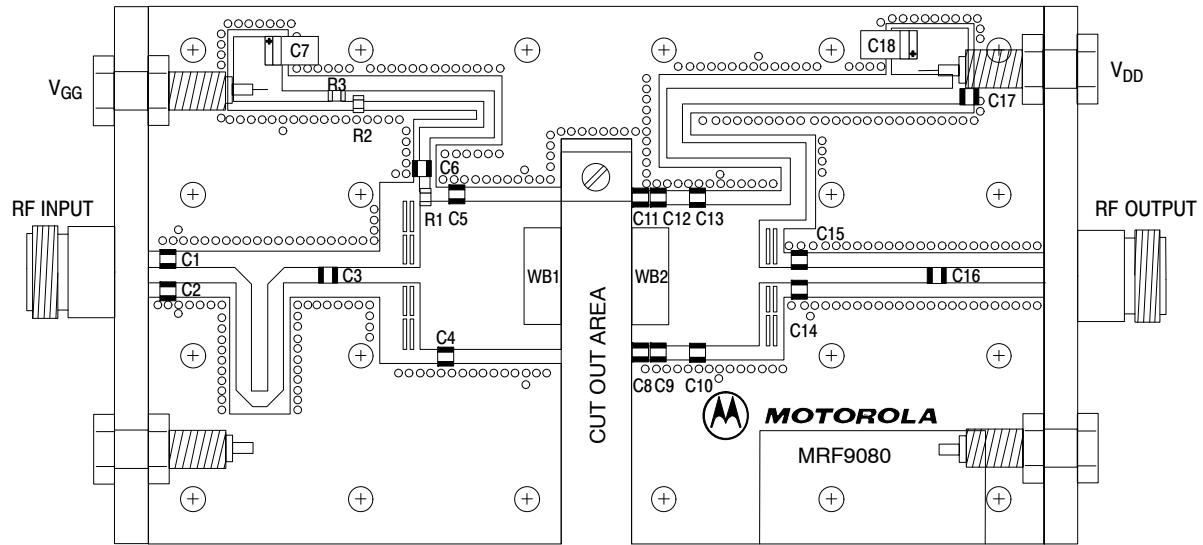


Figure 1. Broadband GSM 900 Test Circuit Schematic

Table 5. Broadband GSM 900 Test Circuit Component Designations and Values

Part	Description	Part Number	Manufacturer
C1	4.7 pF Chip Capacitor	ATC100B4R7BT500XT	ATC
C2	2.7 pF Chip Capacitor	ATC100B2R7BT500XT	ATC
C3	1.5 pF Chip Capacitor	ATC100B1R5BT500XT	ATC
C4, C5, C9, C10, C12, C13	5.6 pF Chip Capacitors	ATC100B5R6CT500XT	ATC
C6, C16, C17	22 pF Chip Capacitors	ATC100B220GT500XT	ATC
C7, C18	10 μ F, 35 V Tantalum Chip Capacitors	T491D106M035AT	Kemet
C8, C11	10 pF Chip Capacitors	ATC100B100JT500XT	ATC
C14	0.8 pF Chip Capacitor	ATC100B0R8BT500XT	ATC
C15	8.2 pF Chip Capacitor	ATC100B8R2GT500XT	ATC
R1, R2, R3	1.0 k Ω , 1/8 W Chip Resistors	CRCW08051001FKEA	Vishay
Raw PCB Material	30 mil Glass Teflon [®] , $\epsilon_r = 2.55$	TLX8-0300	Taconic



Freescall has begun the transition of marking Printed Circuit Boards (PCBs) with the Freescall Semiconductor signature/logo. PCBs may have either Motorola or Freescall markings during the transition period. These changes will have no impact on form, fit or function of the current product.

Figure 2. Broadband GSM 900 Test Circuit Component Layout

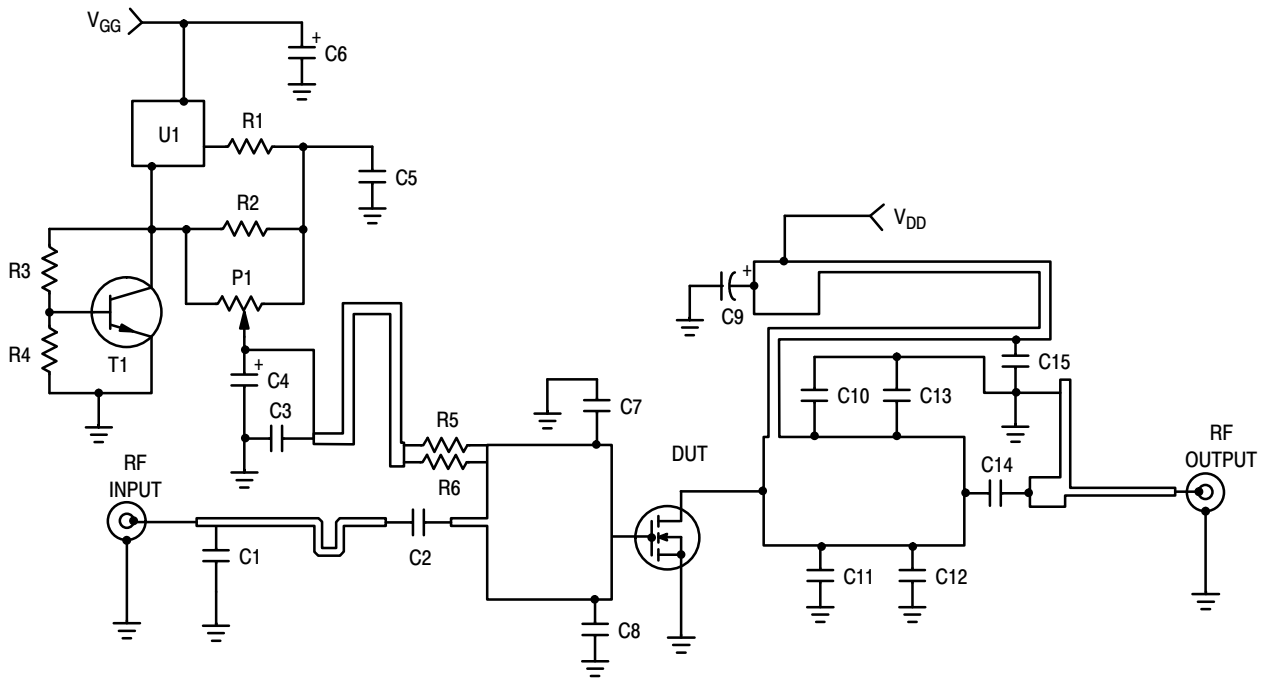
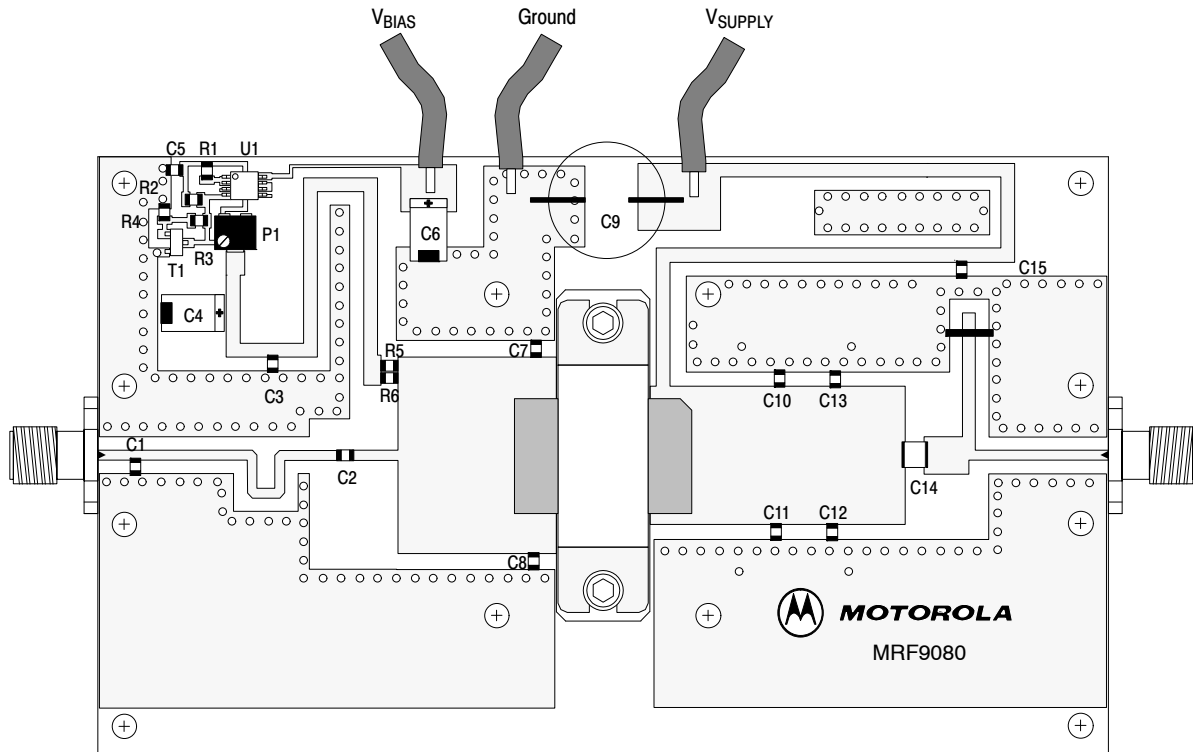


Figure 3. Broadband GSM 900 Optimized Demo Board Schematic

Table 6. Broadband GSM 900 Optimized Demo Board Component Designations and Values

Part	Description	Part Number	Manufacturer
C1	4.7 pF Chip Capacitor, ACCU-P	08051J4R7CBS	AVX
C2	3.9 pF Chip Capacitor, ACCU-P	08051J3R9CBS	AVX
C3, C15	22 pF Chip Capacitors, ACCU-P	08051J221CBS	AVX
C4, C6	22 μ F, 35 V Tantalum Chip Capacitors	T491X226K035AS	Kemet
C5	1 μ F Chip Capacitor, ACCU-P	08053105	AVX
C7, C8	5.6 pF Chip Capacitors, ACCU-P	08051J5R6CBS	AVX
C9	220 μ F, 63 V Electrolytic Capacitor	2222-136-68221	Vishay
C10, C11	3.3 pF Chip Capacitors, ACCU-P	08051J3R3CBS	AVX
C12, C13	2.2 pF Chip Capacitors, ACCU-P	08051J2R2CBS	AVX
C14	4.7 pF Chip Capacitor	ATC100B4R7JT500XT	ATC
P1	5.0 k Ω Potentiometer CMS Cermet Multi-turn	3224W	Bourns
R1	10 Ω , 1/8 W Chip Resistor	CRCW080510R0FKEA	Vishay
R2, R5, R6	1 k Ω , 1/8 W Chip Resistor	CRCW08051001FKEA	Vishay
R3	1.2 k Ω , 1/8 W Chip Resistor	CRCW08051201FKEA	Vishay
R4	2.2 k Ω , 1/8 W Chip Resistor	CRCW08052201FKEA	Vishay
T1	Bipolar NPN Transistor, SOT-23	BC847ALT1G	ON Semiconductor
U1	Voltage Regulator, Micro-8	LP2951ACDMR2G	ON Semiconductor
Substrate = Taconic RF35, Thickness 0.5 mm			



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Figure 4. Broadband GSM 900 Optimized Demo Board Component Layout

TYPICAL CHARACTERISTICS (IN FREESCALE BROADBAND GSM 900 OPTIMIZED DEMO BOARD)

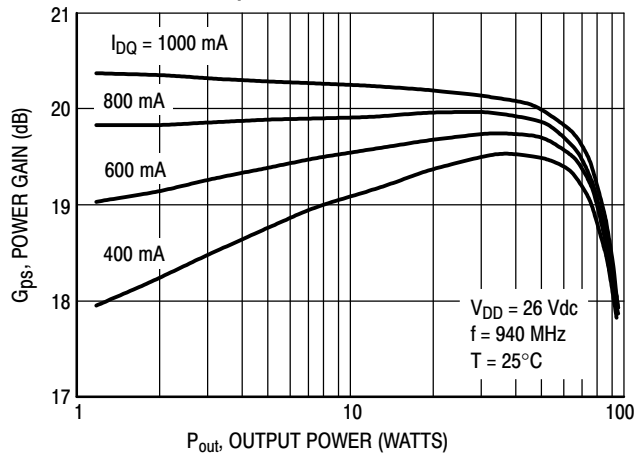


Figure 5. Power Gain versus Output Power

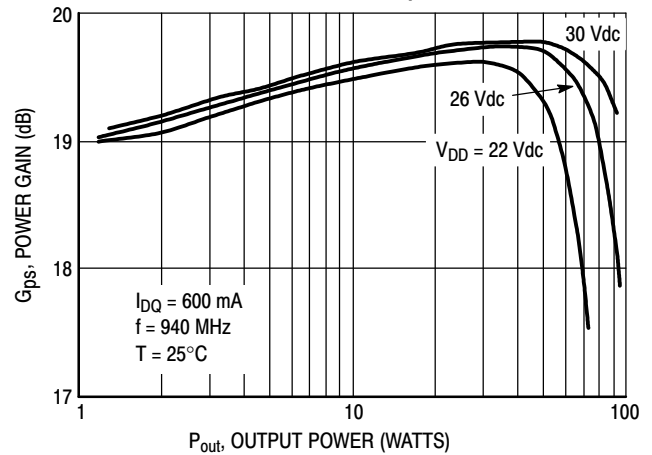


Figure 6. Power Gain versus Output Power

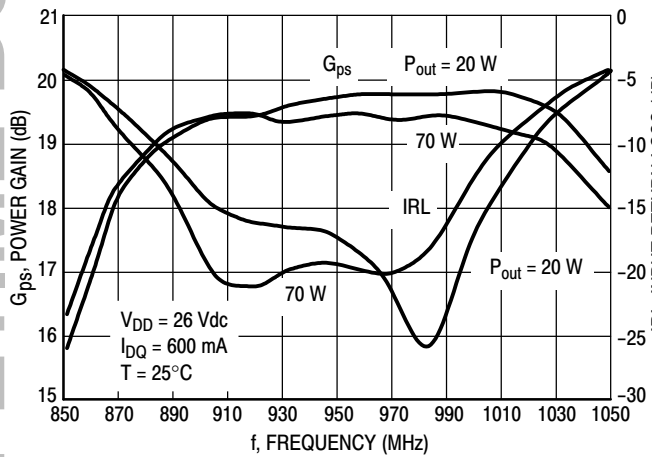


Figure 7. Power Gain and Input Return Loss versus Frequency

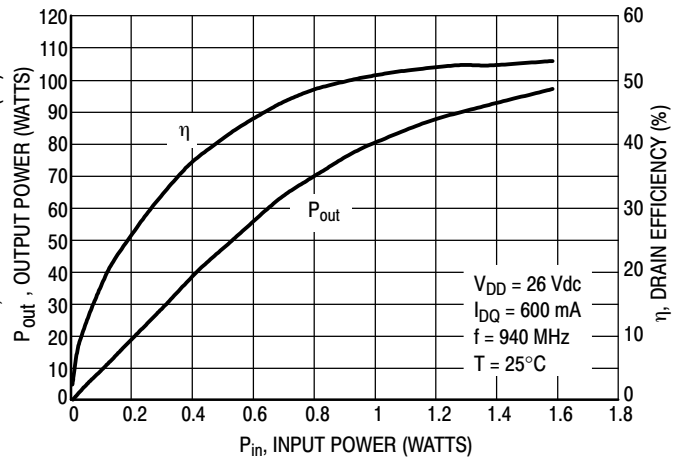


Figure 8. Output Power and Efficiency versus Input Power

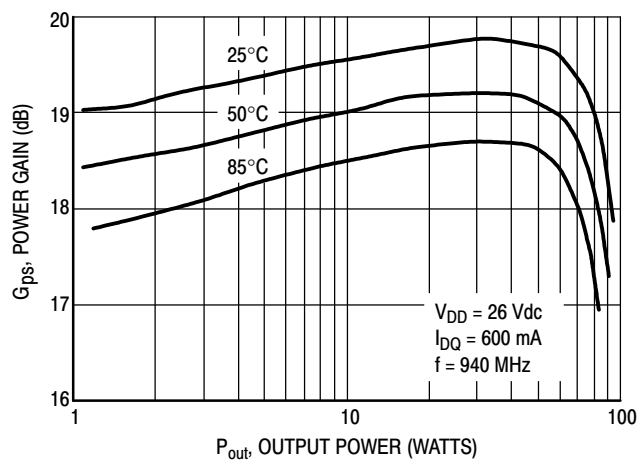


Figure 9. Power Gain versus Output Power

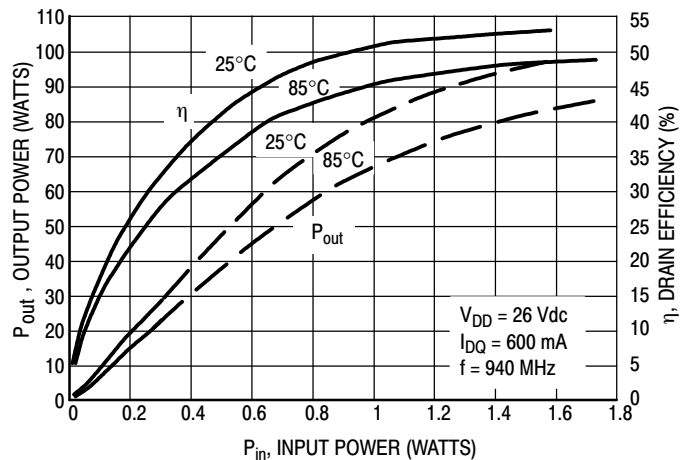
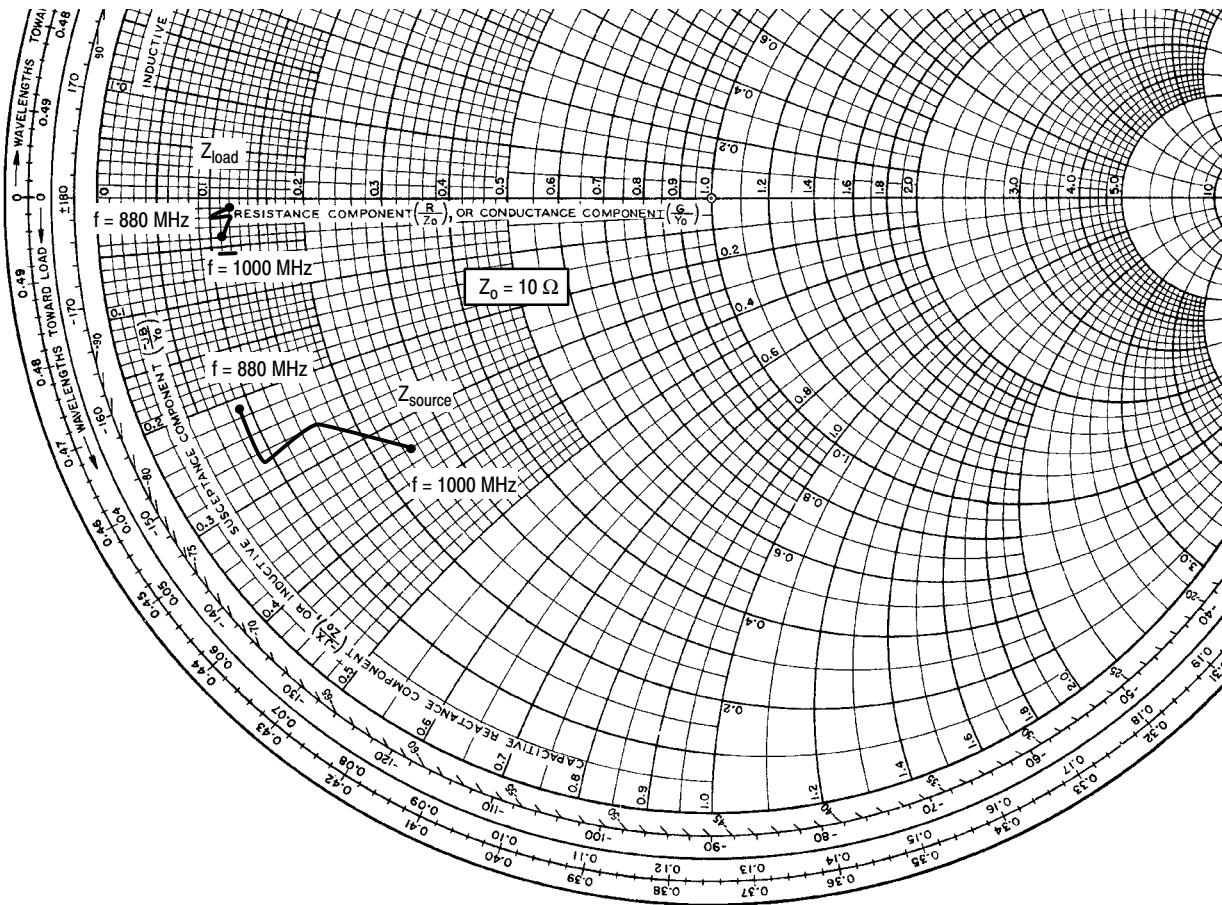


Figure 10. Output Power and Efficiency versus Input Power

MRF9080LR3



$V_{DD} = 26 \text{ V}$, $I_{DQ} = 600 \text{ mA}$, $P_{out} = 90 \text{ W CW}$

f MHz	Z_{source} Ω	Z_{load} Ω
880	$0.91 - j2.11$	$1.22 - j0.12$
920	$0.88 - j2.65$	$1.00 - j0.16$
960	$1.6 - j2.61$	$1.22 - j0.22$
1000	$2.45 - j3.38$	$1.14 - j0.41$

Z_{source} = Test circuit impedance as measured from gate to ground.

Z_{load} = Test circuit impedance as measured from drain to ground.

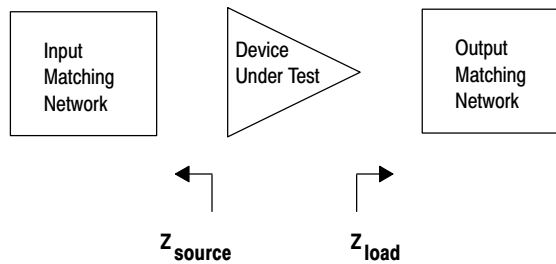


Figure 11. Series Equivalent Source and Load Impedance



- _____

- _____

PRODUCT DOCUMENTATION

Refer to the following documents to aid your design process.

Engineering Bulletins

- EB212: Using Data Sheet Impedances for RF LDMOS Devices

REVISION HISTORY

The following table summarizes revisions to this document.

Revision	Date	Description
8	Oct. 2008	• Data sheet revised to reflect part status change, p. 1, including use of applicable overlay. • Modified data sheet to reflect RF Test Reduction described in Product and Process Change Notification number, PCN12779, p. 1, 2 • Updated Part Numbers in Tables 5 and 6, Component Designations and Values, to RoHS compliant part numbers, p. 3, 5 • Added Product Documentation and Revision History, p. 10

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