



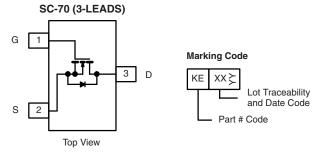
# N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)		
20	0.85 at V <sub>GS</sub> = 4.5 V	0.4	0.335		
	1.08 at V <sub>GS</sub> = 2.5 V	0.35	0.333		

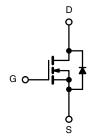
# **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 %  $R_g$  Tested
- Compliant to RoHS Directive 2002/95/EC









N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	<b>S</b> (T <sub>A</sub> = 25 °C, unle	ess otherwise no	ted)	
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	20	V	
Gate-Source Voltage	V <sub>GS</sub>	± 8	V	
	T <sub>C</sub> = 25 °C		0.4	
Continuous Prais Current /T 150 °C)	T <sub>C</sub> = 70 °C	_	0.32	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	0.37 <sup>b, c</sup>	
	T <sub>A</sub> = 70 °C		0.30 <sup>b, c</sup>	Α
Pulsed Drain Current	I <sub>DM</sub>	0.5		
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	1	0.18	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	0.14 <sup>b, c</sup>	
	T <sub>C</sub> = 25 °C		0.2	
Manipular Davida Dissipation	T <sub>C</sub> = 70 °C		0.14	10/
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	0.19	W
	T <sub>A</sub> = 70 °C		0.12 <sup>b, c</sup>	
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 5 s	R <sub>thJA</sub>	540	670	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	450	570		

#### Notes:

- a. Based on T<sub>C</sub> = 25 °C.
- b. Surface mounted on 1" x 1" FR4 board.
- d. Maximum under steady state conditions is 360 °C/W.

# Vishay Siliconix



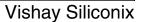
<b>SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static			•	•		•
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	1 - 250 uA		20		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = 250  \mu A$		- 2.8		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.4		1.0	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current		V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V			100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			5	μΑ
On-State Drain Current <sup>a</sup>		$V_{DS} \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	0.4			А
On-State Drain Guirent	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 2.5 \text{ V}$	0.12			
Drain-Source On-State Resistance <sup>a</sup>	В	$V_{GS} = 4.5 \text{ V}, I_D = 0.25$		0.65	0.85	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, I_D = 0.15$		0.85	1.08	Ω
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>			35		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		13		
Reverse Transfer Capacitance	C <sub>rss</sub>			4		
Total Gate Charge	Q <sub>g</sub> Q <sub>gs</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 0.4$		560	840	
Total Gate Charge				335	503	pC
Gate-Source Charge		$V_{DS} = 10 \text{ V}, V_{GS} = 2.5 \text{ V}, I_{D} = 0.35$		98		
Gate-Drain Charge	$Q_{gd}$			85		
Gate Resistance	$R_g$	f = 1 MHz	1.5	7	12	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			7	12	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 25 $\Omega$		10	15	ns
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong 0.4$ A, $V_{GEN}=4.5$ V, $R_g=1$ $\Omega$		8	13	
Fall Time	t <sub>f</sub>			7	12	
Drain-Source Body Diode Characteristic	s					
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	T <sub>C</sub> = 25 °C 0		0.18	A
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				0.4	
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = 0.05 A		0.7	1.2	V

#### Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

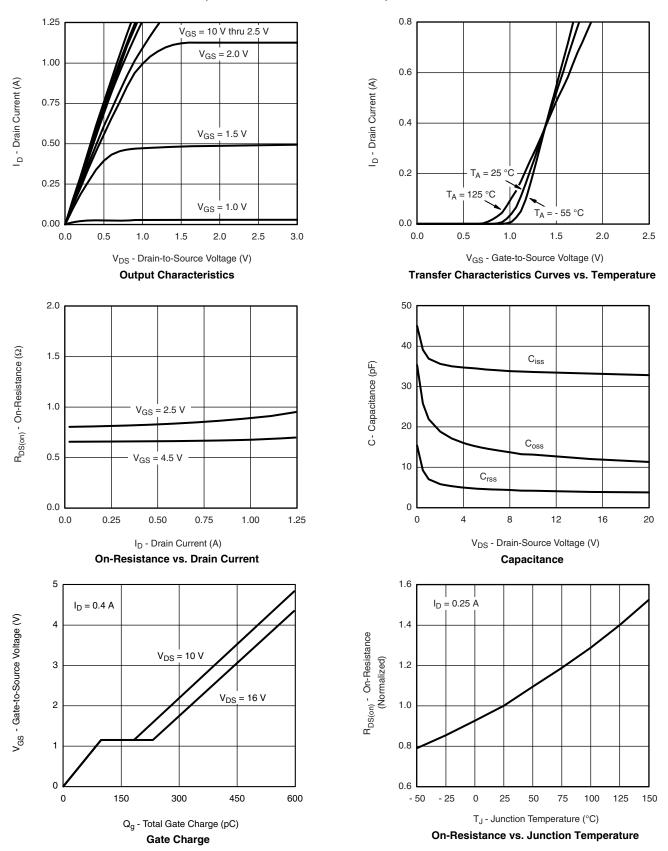
a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$ 

b. Guaranteed by design, not subject to production testing.



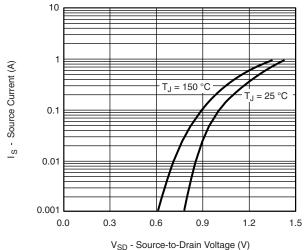


## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

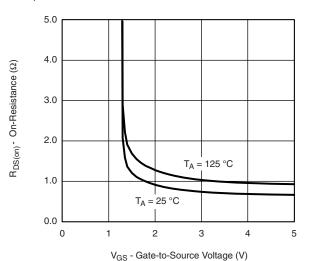


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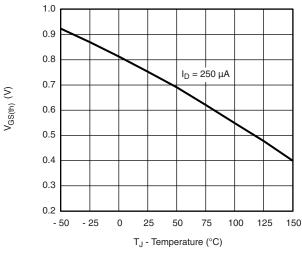
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



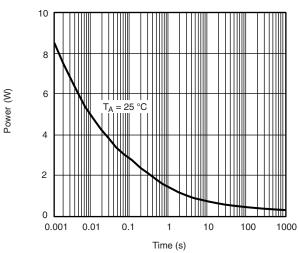
Forward Diode Voltage vs. Temperature



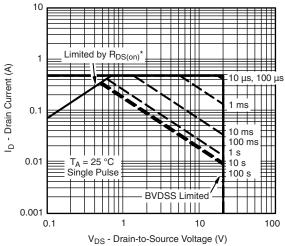
R<sub>DS(on)</sub> vs. V<sub>GS</sub> vs. Temperature



**Threshold Voltage** 



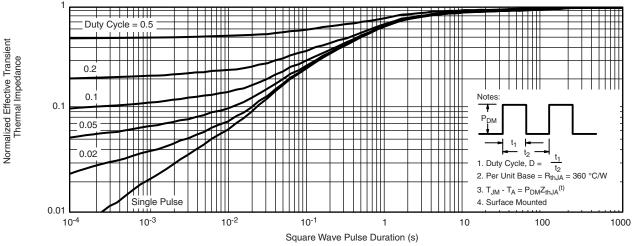
Single Pulse Power, Junction-to-Ambient



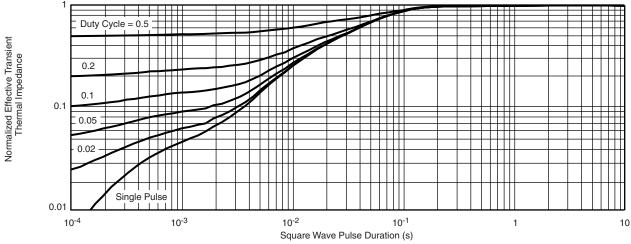
\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified Safe Operating Area



## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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