

RB550VA-30

General rectification

- 1) Small mold type (TUMD2)
- 2) Low V_F , Low I_R
- 3) High reliability

Silicon Epitaxial Planer

ROHM : TUMD2

dot(year week factory)+day

TUMD2

Trapping dimensions (Unit : mm)

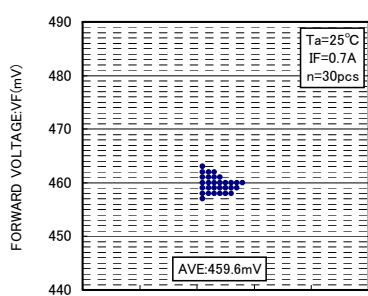
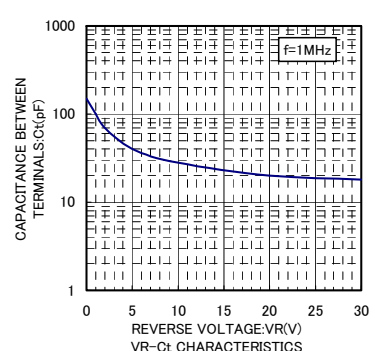
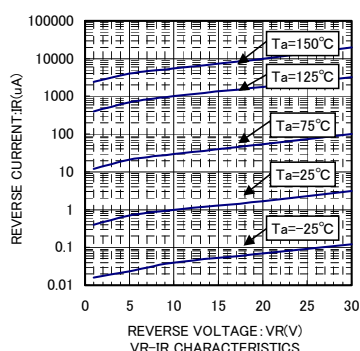
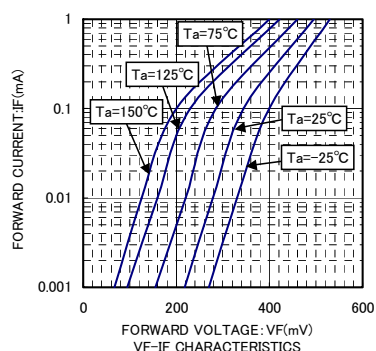
Technical drawing of a 4-hole trap door. The drawing shows a top view and a side view. The top view is a rectangle with a wavy bottom edge. It has four circular holes. Dimensions are given in millimeters. The top view dimensions are: 4.0 ± 0.1, 2.0 ± 0.05, 1.43 ± 0.05, 4.0 ± 0.1, 0.9 ± 0.08, 0.9 ± 0.08, 0.25 ± 0.05, 2.8 ± 0.05, 2.8 ± 0.05, 3.5 ± 0.05, 1.75 ± 0.1, 8.0 ± 0.2, 0.05, 1.55 ± 0.1, 0.05, 1.0 ± 0.2, 0.9 ± 0.08, 0.9 ± 0.08, 0.25 ± 0.05. The side view shows the profile of the trap door with a wavy top edge and a flat bottom edge. It has a central hole. Dimensions are given in millimeters. The side view dimensions are: 4.0 ± 0.1, 2.0 ± 0.05, 1.43 ± 0.05, 4.0 ± 0.1, 0.9 ± 0.08, 0.9 ± 0.08, 0.25 ± 0.05, 2.8 ± 0.05, 2.8 ± 0.05, 3.5 ± 0.05, 1.75 ± 0.1, 8.0 ± 0.2, 0.05, 1.55 ± 0.1, 0.05, 1.0 ± 0.2, 0.9 ± 0.08, 0.9 ± 0.08, 0.25 ± 0.05.

Parameter	Symbol	Limits	Unit
Reverse voltage (repetitive peak)	V_{RM}	30	V
Reverse voltage (DC)	V_R	30	V
Average rectified forward current	I_o	1	A
Forward current surge peak	I_{FSM}	3	A
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	-40 to +150	°C

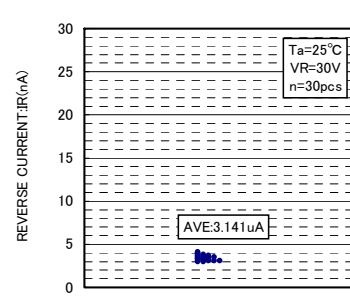
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V _{F1}	-	0.45	0.49	V	I _F =700mA
	V _{F2}	-	0.48	0.52	μA	I _F =1A
Reverse current	I _R	-	1	30	μA	V _R =10V

Diodes

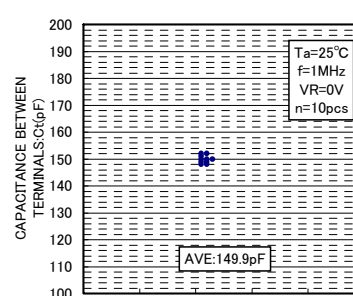
●Electrical characteristic curves



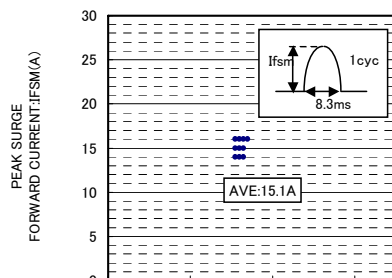
VF DISPERSION MAP



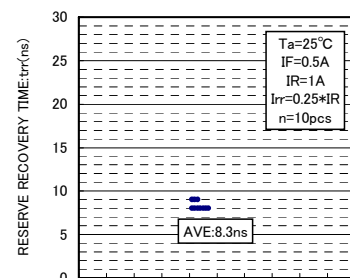
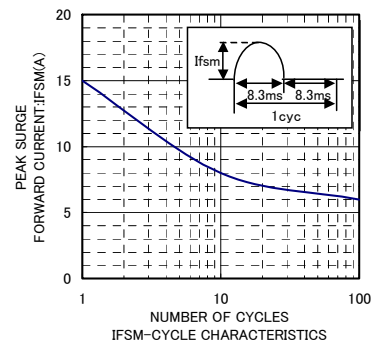
IR DISPERSION MAP



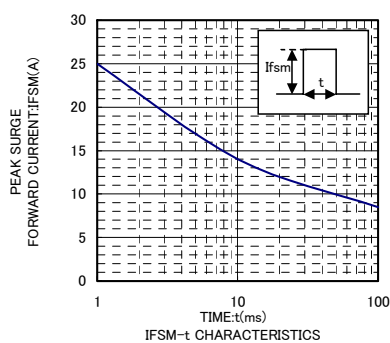
Ct DISPERSION MAP



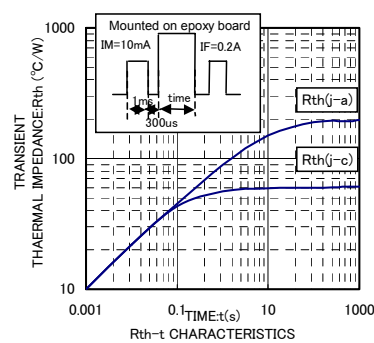
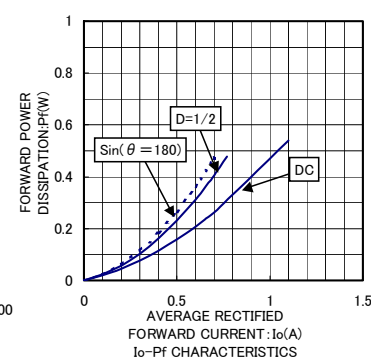
IFSM DISPERSION MAP

 t_{rr} DISPERSION MAP

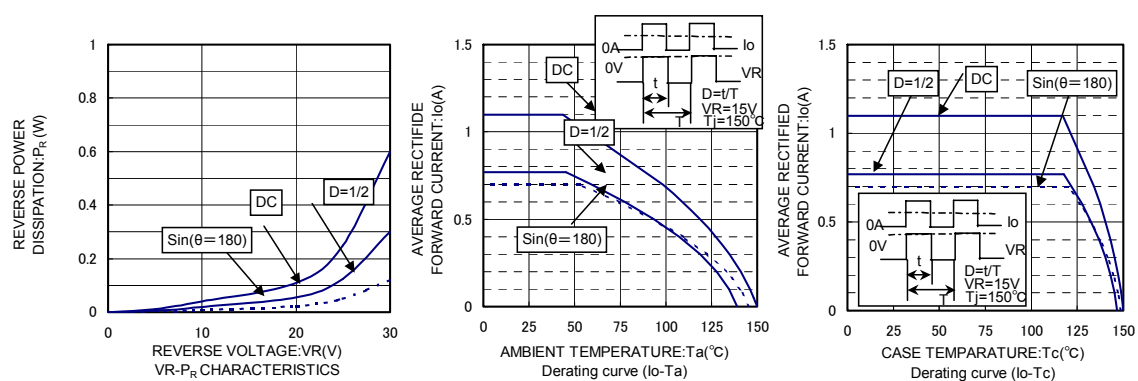
IFSM-CYCLE CHARACTERISTICS



IFSM-t CHARACTERISTICS

 R_{th} -t CHARACTERISTICS I_o - P_F CHARACTERISTICS

Diodes



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