

Alternator Voltage Regulator Darlington Driver

Description

The CS3341/3351/386/387 integral alternator regulator integrated circuit provides the voltage regulation for automotive, 3-phase alternators.

It drives an external power Darlington for control of the alternator field current. In the event of a charge fault, a lamp output pin is

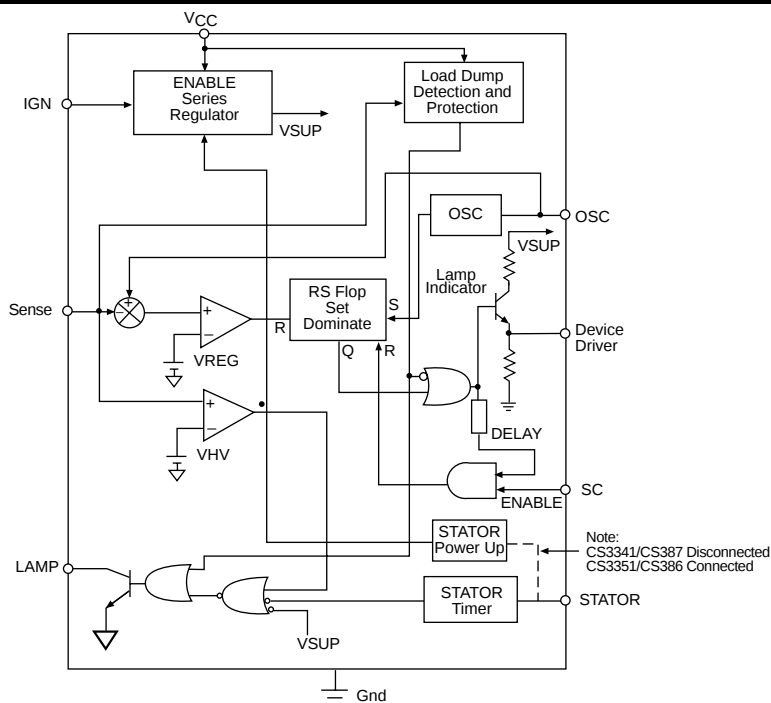
provided to drive an external darlington transistor capable of switching on a fault indicator lamp. An overvoltage or no STATOR signal condition activates the lamp output.

The CS3341 and CS3351 are available in SO14 packages. The CS386 and CS387 are available as Flip Chips.

Absolute Maximum Ratings

Storage Temperature Range	-55°C to +165°C
Junction Temperature Range	-40°C to +150°C
Continuous Supply	27V
ICC Load Dump	400 mA
Lead Temperature Soldering	
Reflow (SMD styles only)	60 sec. max above 183°C, 230°C peak

Block Diagram



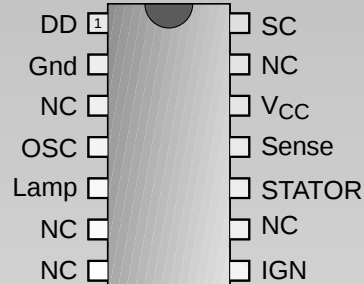
Features

- Drives NPN Darlington
- Short Circuit Protection
- 80V Load Dump
- Temperature Compensated Regulation Voltage
- Shorted Field Protection Duty Cycle, Self Clearing

Package Options

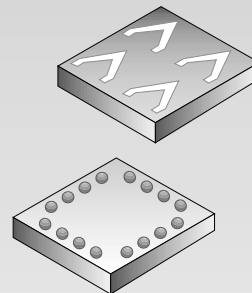
CS3341/51

14 Lead SO



CS386/387

Flip Chip



ON Semiconductor

**Electrical Characteristics: CS3341/51: $-40^{\circ}\text{C} < T_A < 125^{\circ}\text{C}$; $-40^{\circ}\text{C} < T_J < 150^{\circ}\text{C}$, $9\text{V} \leq V_{CC} \leq 17\text{V}$;
unless otherwise specified**

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
■ Supply					
Supply Current Enabled		–	12	25	mA
Supply Current Disabled		–		50	μA
■ Driver Stage					
Device Driver		–		–	–
Output High Current	$V_{DD} = 1.2\text{V}$	-10	-6	-4	mA
Output Low Voltage	$I_{OL} = 25\mu\text{A}$	–		0.35	V
Minimum ON Time		200			μs
Minimum Duty Cycle		–	6	10	%
Short Circuit Duty Cycle		1		5	%
Field Switch Turn On					
Rise Time		30		90	μs
Fall Time		30		90	μs
■ Stator					
Input High Voltage		10			V
Input Low Voltage		–		6	V
Stator Time Out	High to Low	6	100	600	ms
Stator Power-Up Input High	CS3351/386 only	10		–	V
Stator Power-Up Input Low	CS3351/386 only	–		6	V
■ Lamp					
Output High Current	$V_{LAMP} @ 3\text{V}$	–		50	μA
Output Low Voltage	$I_{LAMP} @ 30\text{mA}$	–		0.35	V
■ Ignition					
Input High Voltage	$I_{CC} > 1\text{mA}$	1.8		–	V
Input Low Voltage	$I_{CC} < 100\mu\text{A}$	–		0.5	V
■ Oscillator					
Oscillator Frequency	$C_{OSC} = 0.22\mu\text{F}$	65		325	Hz
Rise Time / Fall Time	$C_{OSC} = 0.22\mu\text{F}$		17	–	–
Oscillator High Threshold	$C_{OSC} = 0.22\mu\text{F}$	–		6	V
■ Battery Sense					
Input Current		-10		+10	μA
Regulation Voltage	@ 25°C , $R_1 = 100\text{k}\Omega$, $R_2 = 50\text{k}\Omega$	13.5		16.0	V
Proportional Control		0.050		0.400	V
High Voltage	$V_{\text{High Voltage @ Lamp On}}$	1.083		1.190	
Threshold Ratio	$V_{\text{Regulation @ 50% Duty Cycle}}$				
High Voltage Hysteresis		0.020		0.600	V

Package Pin Description

PACKAGE PIN #	PIN SYMBOL	FUNCTION
14L SO	Flip Chip	
1	1	Driver
2	2	Gnd
3, 6, 7, 9, 13	3	NC
4	4	OSC
5	5	Lamp
8	6	IGN
10	7	Stator
11	8	Sense
12	9	V _{CC}
14	10	SC

Output driver for external power switch-Darlington.

Ground.

No connection.

Timing capacitor for oscillator.

Base driver for lamp driver indicates no stator signal or over-voltage condition.

Switched ignition power up.

Stator signal input for stator timer (CS3351 also power up).

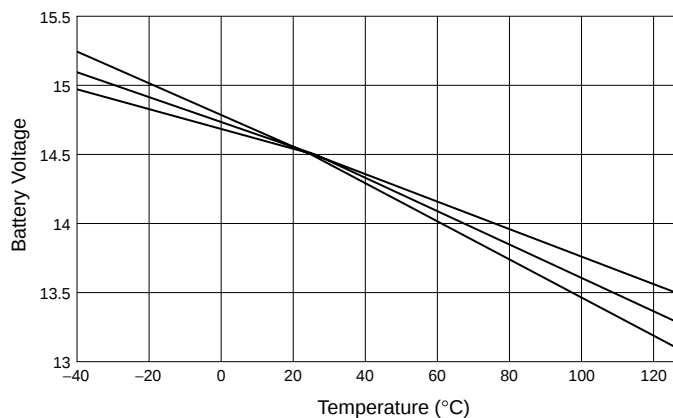
Battery sense voltage regulator comparator input and protection.

Supply for IC.

Short circuit sensing.

Typical Performance Characteristics

CS3341/3351 Battery Voltage vs Temperature (°C)
Over Process Variation



The CS3341 and CS3351 IC's are designed for use in an alternator charging system. The circuit is also available in flip-chip form as the CS387 and CS386.

In a standard alternator design (Figure 1), the rotor carries the field winding. An alternator rotor usually has several N and S poles. The magnetic field for the rotor is produced by forcing current through a field or rotor winding. The Stator windings are formed into a number of coils spaced around a cylindrical core. The number of coils equals the number of pairs of N and S poles on the rotor. The alternating current in the Stator windings is rectified by the diodes and applied to the regulator. By controlling the amount of field

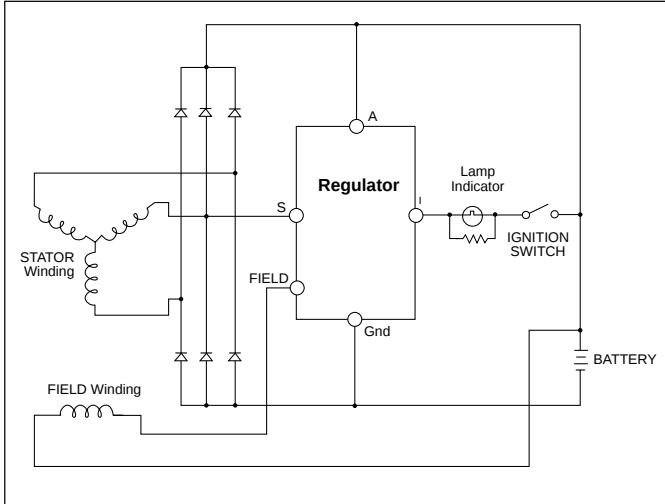


Figure 1. IAR System Block Diagram

current, the magnetic field strength is controlled and hence the output voltage of the alternator.

Referring to Figure 2, a typical application diagram, the oscillator frequency is set by an external capacitor connected between OSC and ground. The sawtooth waveform ramps between 1V and 3V and provides the timing for the system. For the circuit shown the oscillator frequency is approximately 140Hz. The alternator voltage is sensed at Terminal A via the resistor divider network R1/R2 on the Sense pin of the IC. The voltage at the sense pin determines the duty cycle for the regulator. The voltage is adjusted by potentiometer R2. A relatively low voltage on the sense pin causes a long duty cycle that increases the Field current. A high voltage results in a short duty cycle.

The Ignition Terminal (I) switches power to the IC through the V_{CC} pin. In the CS3351/CS386, the Stator pin senses the voltage from the stator. This will keep the device powered while the voltage is high, and it also senses a stopped engine condition and drives the Lamp pin high after the stator timeout expires. The Lamp pin also goes high when an overvoltage condition is detected on the sense pin. This causes the darlington lamp drive transistor to switch on and pull current through the lamp. If the system voltage continues to increase, the field and lamp output turn off as in an overvoltage or load dump condition.

The SC or Short Circuit pin monitors the field voltage. If the drive output and the SC voltage are simultaneously high for a predetermined period, a short circuit condition is assumed and the output is disabled. The regulator is forced to a minimum short circuit duty cycle.

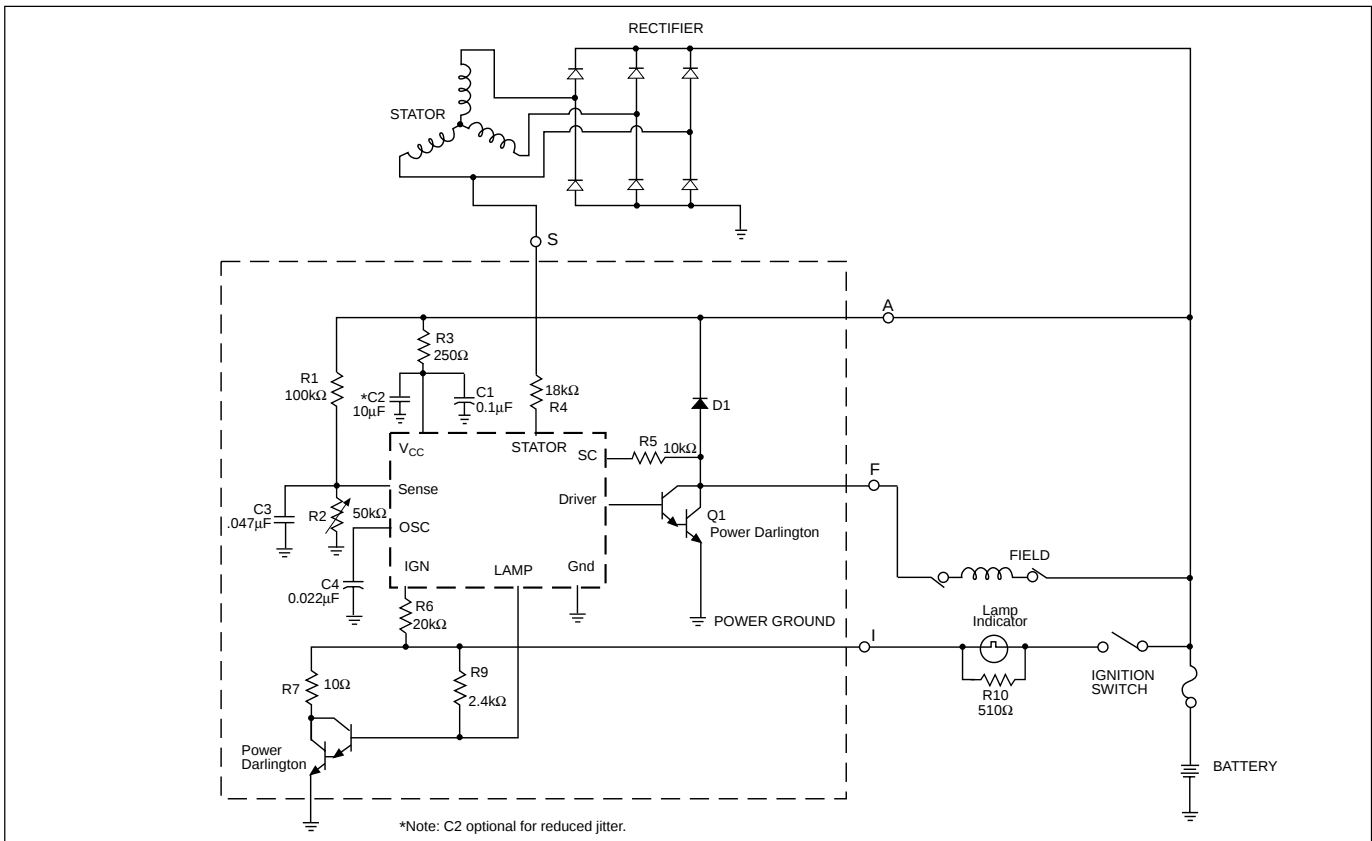
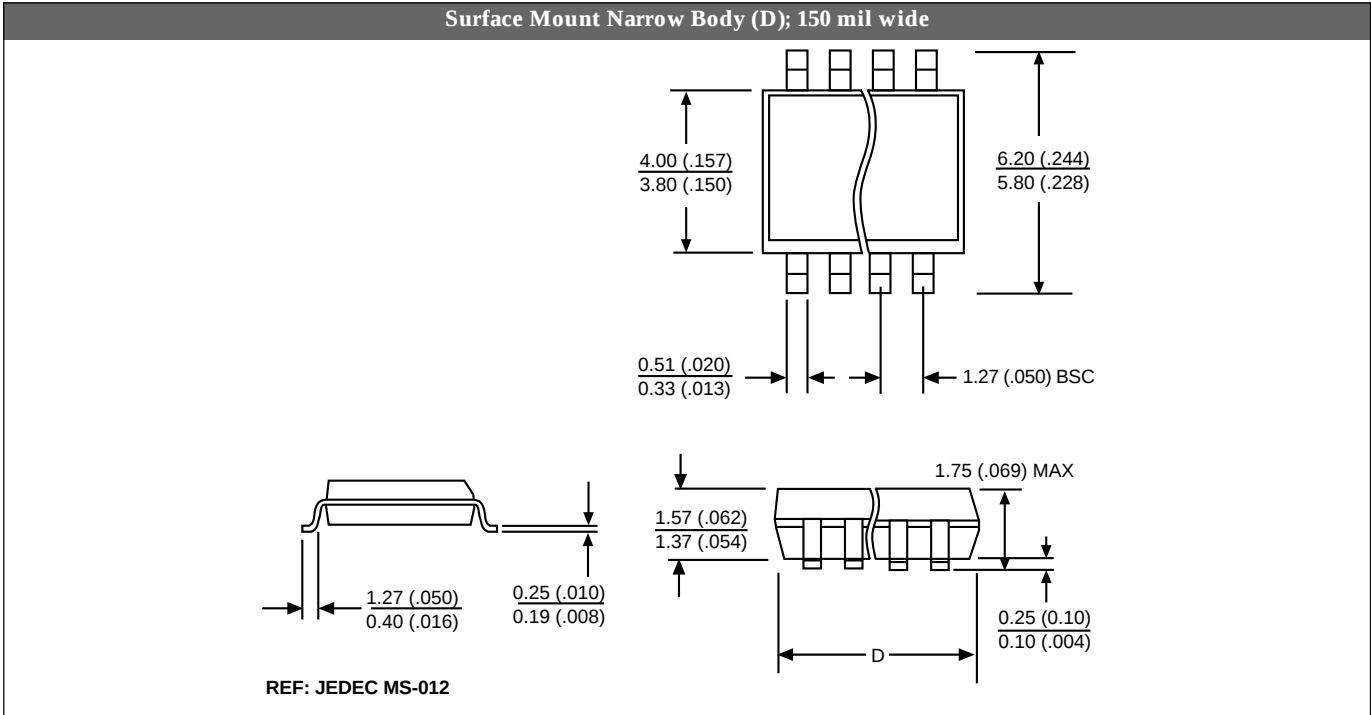


Figure 2. Typical Application Diagram

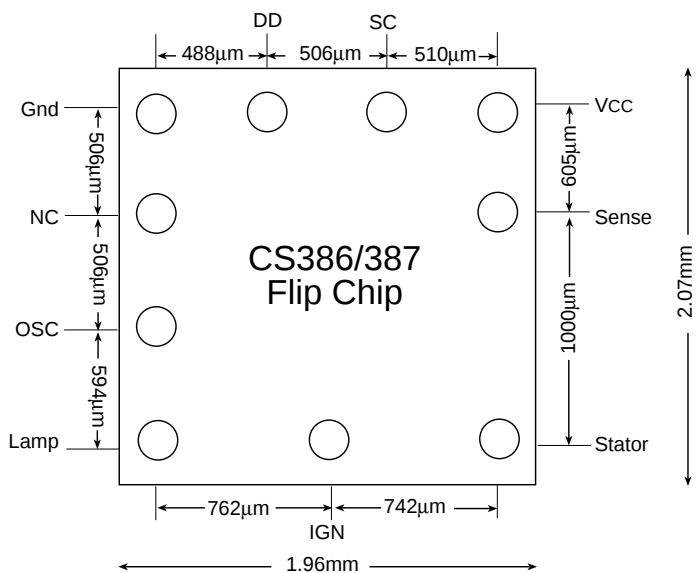
Package Specification

PACKAGE DIMENSIONS IN mm (INCHES)				
Lead Count	D			
	Metric		English	
	Max	Min	Max	Min
14L SO	8.75	8.55	.344	.337

PACKAGE THERMAL DATA			
Thermal Data		14L SO	
R _{ΘJC}	typ	30	°C / W
R _{ΘJA}	typ	125	°C / W



Flip Chip



Solder Bump Locations, Bump Side Up

Ordering Information

Part Number	Description
CS3341YD14	14L SO
CS3341YDR14	14L SO (<i>tape & reel</i>)
CS3351YD14	14L SO
CS3351YDR14	14L SO (<i>tape & reel</i>)
CS386H	Flip Chip
CS387H	Flip Chip

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