

# MC1489, MC1489A

## Quad Line EIA-232D Receivers

The MC1489 monolithic quad line receivers are designed to interface data terminal equipment with data communications equipment in conformance with the specifications of EIA Standard No. EIA-232D.

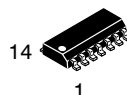
### Features

- Input Resistance – 3.0 k to 7.0 k $\Omega$
- Input Signal Range –  $\pm 30$  V
- Input Threshold Hysteresis Built In
- Response Control
  - a) Logic Threshold Shifting
  - b) Input Noise Filtering
- Pb-Free Packages are Available

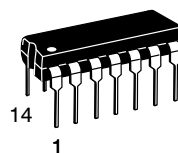


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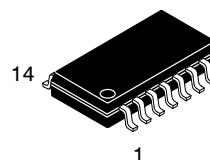
<http://onsemi.com>



**SOIC-14  
D SUFFIX  
CASE 751A**



**PDIP-14  
P SUFFIX  
CASE 646**



**SOEIAJ-14  
M SUFFIX  
CASE 965**

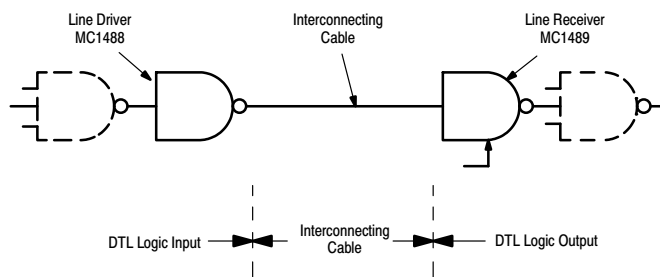
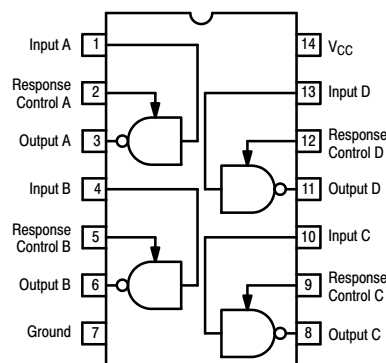


**Figure 1. Simplified Application**

### PIN CONNECTIONS



### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 8 of this data sheet.

### DEVICE MARKING INFORMATION

See general marking information in the device marking section on page 9 of this data sheet.

# MC1489, MC1489A

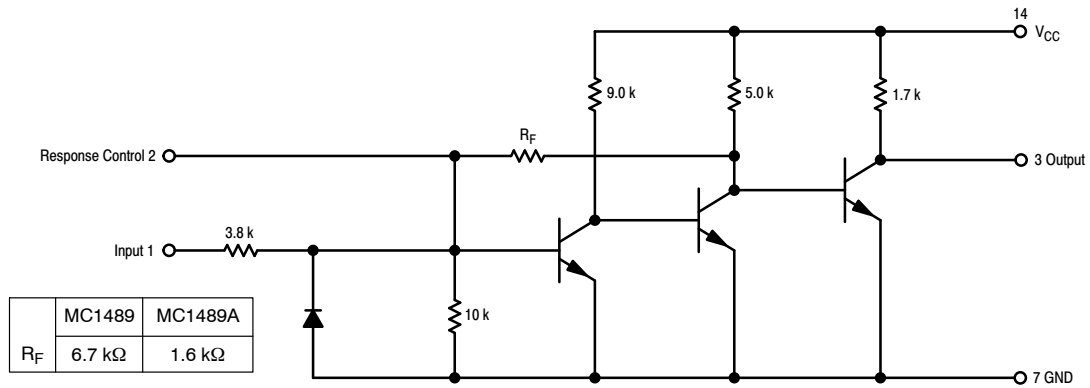


**Figure 2. Representative Schematic Diagram**  
(1/4 of Circuit Shown)

# MC1489, MC1489A

## MAXIMUM RATINGS (T<sub>A</sub> = + 25°C, unless otherwise noted)

| Rating                                                                                                                   | Symbol                              | Value         | Unit        |
|--------------------------------------------------------------------------------------------------------------------------|-------------------------------------|---------------|-------------|
| Power Supply Voltage                                                                                                     | V <sub>CC</sub>                     | 10            | Vdc         |
| Input Voltage Range                                                                                                      | V <sub>IR</sub>                     | ± 30          | Vdc         |
| Output Load Current                                                                                                      | I <sub>L</sub>                      | 20            | mA          |
| Power Dissipation (Package Limitation, SOIC-14 and Plastic Dual In-Line Package)<br>Derate above T <sub>A</sub> = + 25°C | P <sub>D</sub><br>1/θ <sub>JA</sub> | 1000<br>6.7   | mW<br>mW/°C |
| Operating Ambient Temperature Range                                                                                      | T <sub>A</sub>                      | 0 to + 75     | °C          |
| Storage Temperature Range                                                                                                | T <sub>stg</sub>                    | – 65 to + 175 | °C          |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

## ELECTRICAL CHARACTERISTICS (Response control pin is open.) (V<sub>CC</sub> = + 5.0 Vdc ± 10%, T<sub>A</sub> = 0 to + 75°C, unless otherwise noted)

| Characteristics                                                                                                                        | Symbol          | Min             | Typ        | Max          | Unit |
|----------------------------------------------------------------------------------------------------------------------------------------|-----------------|-----------------|------------|--------------|------|
| Positive Input Current<br>(V <sub>IH</sub> = + 25 Vdc)<br>(V <sub>IH</sub> = + 3.0 Vdc)                                                | I <sub>IH</sub> | 3.6<br>0.43     | –<br>–     | 8.3<br>–     | mA   |
| Negative Input Current<br>(V <sub>IH</sub> = – 25 Vdc)<br>(V <sub>IH</sub> = – 3.0 Vdc)                                                | I <sub>IL</sub> | – 3.6<br>– 0.43 | –<br>–     | – 8.3<br>–   | mA   |
| Input Turn-On Threshold Voltage<br>(T <sub>A</sub> = + 25°C, V <sub>OL</sub> ≤ 0.45 V)<br>MC1489<br>MC1489A                            | V <sub>IH</sub> | 1.0<br>1.75     | –<br>1.95  | 1.5<br>2.25  | Vdc  |
| Input Turn-Off Threshold Voltage<br>(T <sub>A</sub> = + 25°C, V <sub>OH</sub> ≥ 2.5 V, I <sub>L</sub> = – 0.5 mA)<br>MC1489<br>MC1489A | V <sub>IL</sub> | 0.75<br>0.75    | –<br>0.8   | 1.25<br>1.25 | Vdc  |
| Output Voltage High<br>(V <sub>IH</sub> = 0.75 V, I <sub>L</sub> = – 0.5 mA)<br>(Input Open Circuit, I <sub>L</sub> = – 0.5 mA)        | V <sub>OH</sub> | 2.5<br>2.5      | 4.0<br>4.0 | 5.0<br>5.0   | Vdc  |
| Output Voltage Low<br>(V <sub>IL</sub> = 3.0 V, I <sub>L</sub> = 10 mA)                                                                | V <sub>OL</sub> | –               | 0.2        | 0.45         | Vdc  |
| Output Short-Circuit Current                                                                                                           | I <sub>OS</sub> | –               | – 3.0      | – 4.0        | mA   |
| Power Supply Current (All Gates “on,” I <sub>out</sub> = 0 mA, V <sub>IH</sub> = + 5.0 Vdc)                                            | I <sub>CC</sub> | –               | 16         | 26           | mA   |
| Power Consumption<br>(V <sub>IH</sub> = + 5.0 Vdc)                                                                                     | P <sub>C</sub>  | –               | 80         | 130          | mW   |

## SWITCHING CHARACTERISTICS (V<sub>CC</sub> = 5.0 Vdc ± 1%, T<sub>A</sub> = + 25°C, See Figure 3.)

|                                                     |                  |   |     |     |    |
|-----------------------------------------------------|------------------|---|-----|-----|----|
| Propagation Delay Time<br>(R <sub>L</sub> = 3.9 kΩ) | t <sub>PLH</sub> | – | 25  | 85  | ns |
| Rise Time<br>(R <sub>L</sub> = 3.9 kΩ)              | t <sub>TLH</sub> | – | 120 | 175 | ns |
| Propagation Delay Time<br>(R <sub>L</sub> = 390 kΩ) | t <sub>PHL</sub> | – | 25  | 50  | ns |
| Fall Time<br>(R <sub>L</sub> = 390 kΩ)              | t <sub>THL</sub> | – | 10  | 20  | ns |

# MC1489, MC1489A

## TEST CIRCUITS

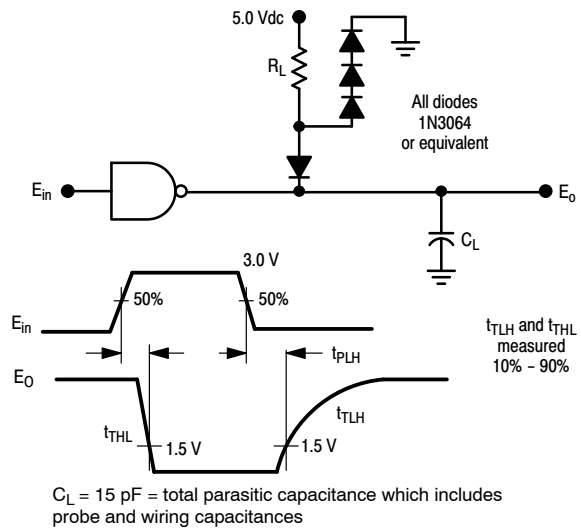
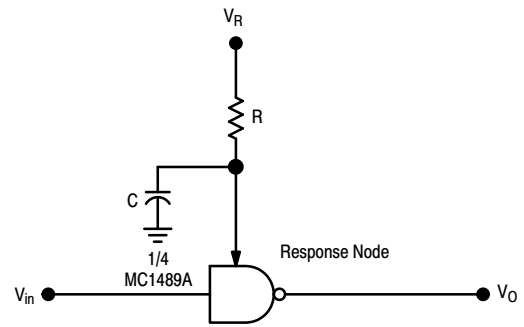


**Figure 3. Switching Response**



C, capacitor is for noise filtering.  
R, resistor is for threshold shifting.

**Figure 4. Response Control Node**

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## TYPICAL CHARACTERISTICS

( $V_{CC} = 5.0$  Vdc,  $T_A = +25^\circ\text{C}$ , unless otherwise noted)

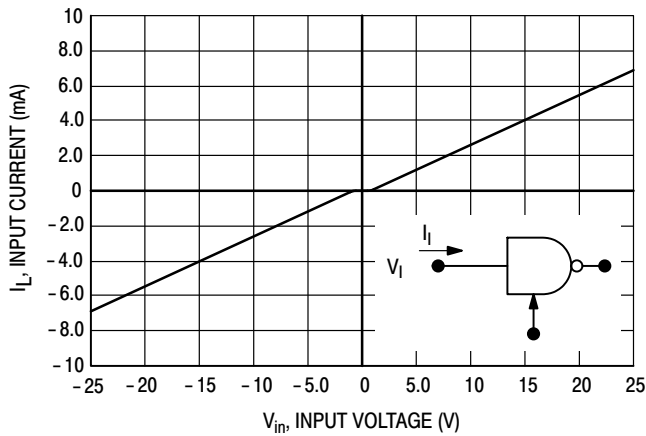


Figure 5. Input Current

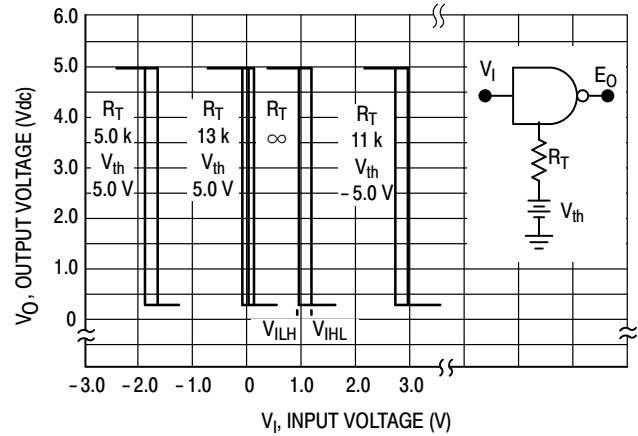


Figure 6. MC1489 Input Threshold Voltage Adjustment

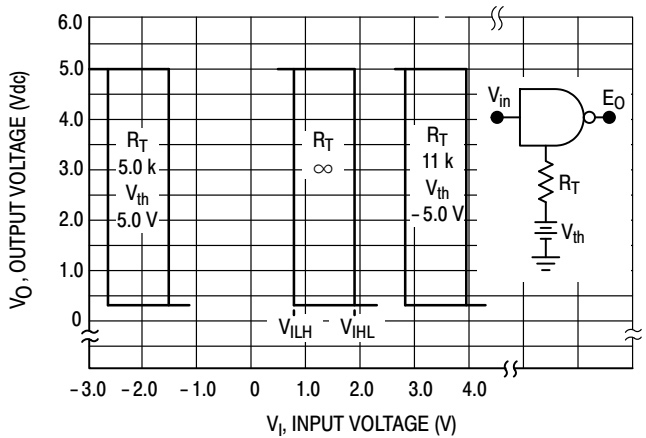


Figure 7. MC1489A Input Threshold Voltage Adjustment

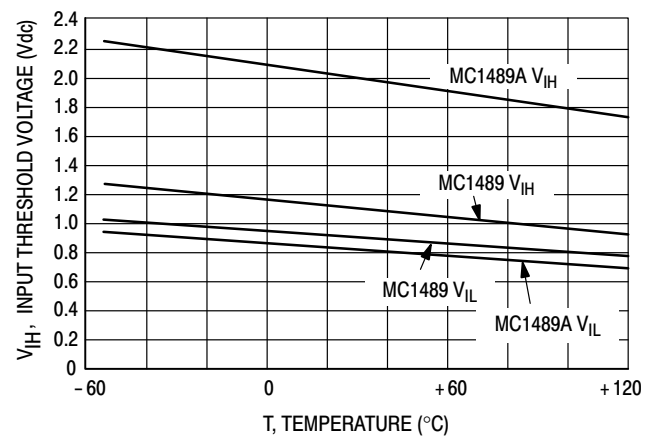


Figure 8. Input Threshold Voltage versus Temperature

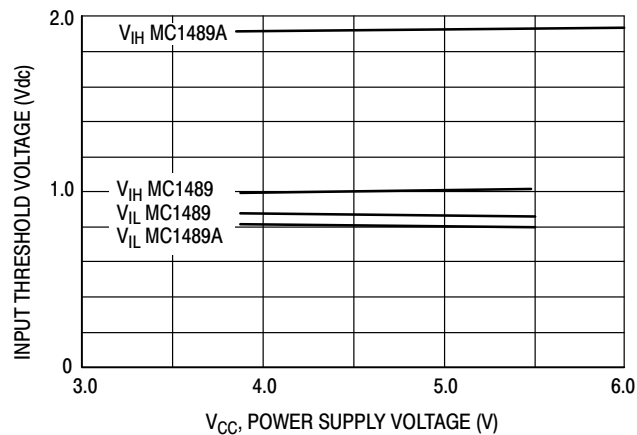


Figure 9. Input Threshold versus Power Supply Voltage

## APPLICATIONS INFORMATION

## General Information

The Electronic Industries Association (EIA) has released the EIA-232D specification detailing the requirements for the interface between data processing equipment and data communications equipment. This standard specifies not only the number and type of interface leads, but also the voltage levels to be used. The MC1488 quad driver and its companion circuit, the MC1489 quad receiver, provide a complete interface system between DTL or TTL logic levels and the EIA-232D defined levels. The EIA-232D requirements as applied to receivers are discussed herein.

The required input impedance is defined as between 3000  $\Omega$  and 7000  $\Omega$  for input voltages between 3.0 and 25 V in magnitude; and any voltage on the receiver input in an open circuit condition must be less than 2.0 V in magnitude. The MC1489 circuits meet these requirements with a maximum open circuit voltage of one  $V_{BE}$ .

The receiver shall detect a voltage between - 3.0 and -25 V as a Logic "1" and inputs between 3.0 and 25 V as a Logic "0." On some interchange leads, an open circuit of power "OFF" condition (300  $\Omega$  or more to ground) shall be decoded as an "OFF" condition or Logic "1." For this reason, the input hysteresis thresholds of the MC1489 circuits are all above ground. Thus an open or grounded input will cause the same output as a negative or Logic "1" input.

## Device Characteristics

The MC1489 interface receivers have internal feedback from the second stage to the input stage providing input hysteresis for noise rejection. The MC1489 input has typical

turn-on voltage of 1.25 V and turn-off of 1.0 V for a typical hysteresis of 250 mV. The MC1489A has typical turn-on of 1.95 V and turn-off of 0.8 V for typically 1.15 V of hysteresis.

Each receiver section has an external response control node in addition to the input and output pins, thereby allowing the designer to vary the input threshold voltage levels. A resistor can be connected between this node and an external power supply. Figures 4, 6 and 7 illustrate the input threshold voltage shift possible through this technique.

This response node can also be used for the filtering of high frequency, high energy noise pulses. Figures 10 and 11 show typical noise pulse rejection for external capacitors of various sizes.

These two operations on the response node can be combined or used individually for many combinations of interfacing applications. The MC1489 circuits are particularly useful for interfacing between MOS circuits and DTL/TTL logic systems. In this application, the input threshold voltages are adjusted (with the appropriate supply and resistor values) to fall in the center of the MOS voltage logic levels (see Figure 12).

The response node may also be used as the receiver input as long as the designer realizes that he may not drive this node with a low impedance source to a voltage greater than one diode above ground or less than one diode below ground. This feature is demonstrated in Figure 13 where two receivers are slaved to the same line that must still meet the EIA-232D impedance requirement.

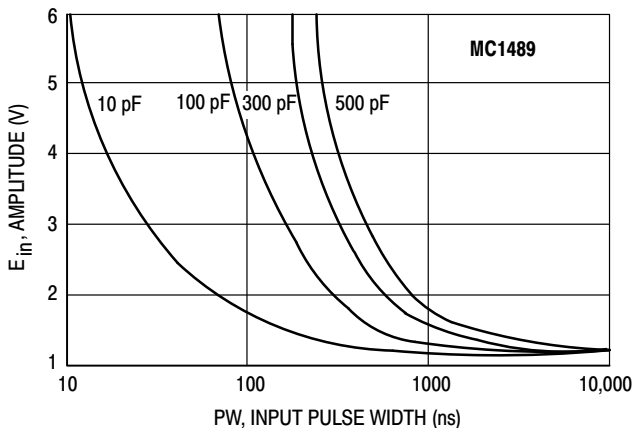


Figure 10. Typical Turn On Threshold versus Capacitance from Response Control Pin to GND

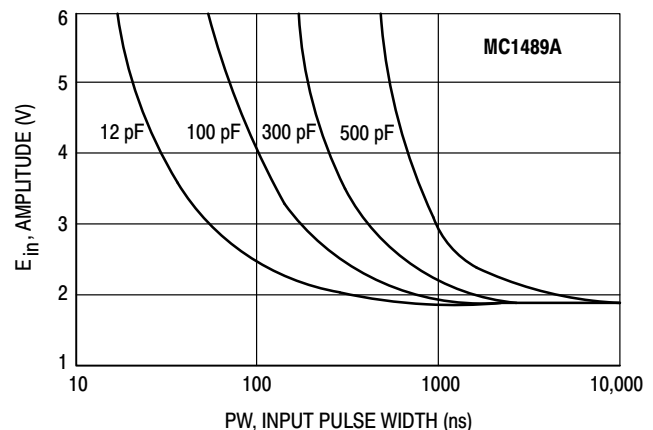


Figure 11. Typical Turn On Threshold versus Capacitance from Response Control Pin to GND

## MC1489, MC1489A

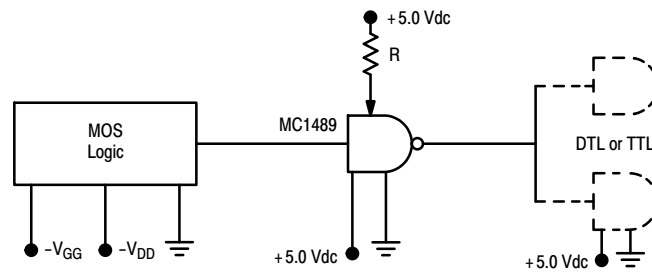


Figure 12. Typical Translator Application – MOS to DTL or TTL

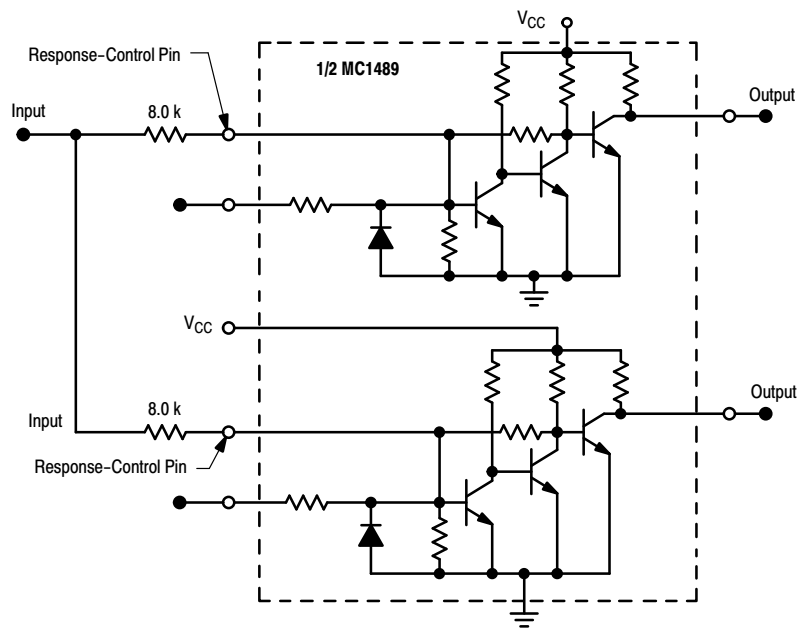


Figure 13. Typical Paralleling of Two MC1489, A Receivers to Meet EIA-232D

# MC1489, MC1489A

## ORDERING INFORMATION

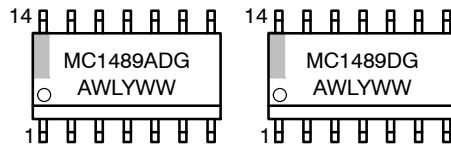
| Device      | Package                | Operating Temperature Range             | Shipping <sup>†</sup> |
|-------------|------------------------|-----------------------------------------|-----------------------|
| MC1489D     | SOIC-14                | $T_A = 0 \text{ to } +75^\circ\text{C}$ | 55 Units/Rail         |
| MC1489DG    | SOIC-14<br>(Pb-Free)   |                                         |                       |
| MC1489DR2   | SOIC-14                |                                         | 2500 Tape & Reel      |
| MC1489DR2G  | SOIC-14<br>(Pb-Free)   |                                         |                       |
| MC1489AD    | SOIC-14                |                                         | 55 Units/Rail         |
| MC1489ADG   | SOIC-14<br>(Pb-Free)   |                                         |                       |
| MC1489ADR2  | SOIC-14                |                                         | 2500 Tape & Reel      |
| MC1489ADR2G | SOIC-14<br>(Pb-Free)   |                                         |                       |
| MC1489P     | PDIP-14                |                                         | 25 Units/Rail         |
| MC1489PG    | PDIP-14<br>(Pb-Free)   |                                         |                       |
| MC1489AP    | PDIP-14                |                                         |                       |
| MC1489APG   | PDIP-14<br>(Pb-Free)   |                                         |                       |
| MC1489M     | SOEIAJ-14              |                                         | 50 Units/Rail         |
| MC1489MG    | SOEIAJ-14<br>(Pb-Free) |                                         |                       |
| MC1489MEL   | SOEIAJ-14              |                                         | 2000 Tape & Reel      |
| MC1489MELG  | SOEIAJ-14<br>(Pb-Free) |                                         |                       |
| MC1489AM    | SOEIAJ-14              |                                         | 50 Units/Rail         |
| MC1489AMG   | SOEIAJ-14<br>(Pb-Free) |                                         |                       |
| MC1489AMEL  | SOEIAJ-14              |                                         | 2000 Tape & Reel      |
| MC1489AMELG | SOEIAJ-14<br>(Pb-Free) |                                         |                       |

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

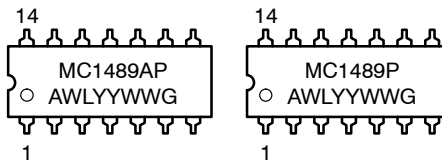
# MC1489, MC1489A

## MARKING DIAGRAMS

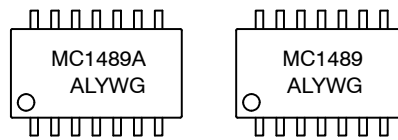
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**D SUFFIX**  
**CASE 751A**



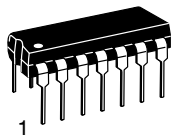
**PDIP-14**  
**P SUFFIX**  
**CASE 646**



**SOEIAJ-14**  
**M SUFFIX**  
**CASE 965**



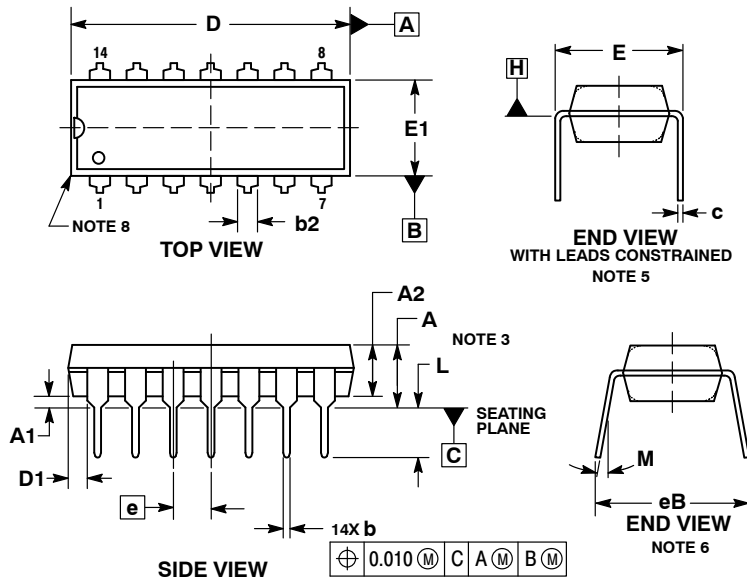
A = Assembly Location  
WL, L = Wafer Lot  
YY, Y = Year  
WW, W = Work Week  
G = Pb-Free Package



SCALE 1:1

**PDIP-14**  
**CASE 646-06**  
**ISSUE 5**

DATE 22 APR 2015



**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: INCHES.
3. DIMENSIONS A, A1 AND L ARE MEASURED WITH THE PACKAGE SEATED IN JEDEC SEATING PLANE GAUGE GS-3.
4. DIMENSIONS D, D1 AND E1 DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS ARE NOT TO EXCEED 0.10 INCH.
5. DIMENSION E IS MEASURED AT A POINT 0.015 BELOW DATUM PLANE H WITH THE LEADS CONSTRAINED PERPENDICULAR TO DATUM C.
6. DIMENSION eB IS MEASURED AT THE LEAD TIPS WITH THE LEADS UNCONSTRAINED.
7. DATUM PLANE H IS COINCIDENT WITH THE BOTTOM OF THE LEADS, WHERE THE LEADS EXIT THE BODY.
8. PACKAGE CONTOUR IS OPTIONAL (ROUNDED OR SQUARE CORNERS).

| DIM | INCHES |       | MILLIMETERS |       |
|-----|--------|-------|-------------|-------|
|     | MIN    | MAX   | MIN         | MAX   |
| A   | 0.015  | 0.210 | 0.38        | 5.33  |
| A1  | 0.115  | 0.195 | 2.92        | 4.95  |
| A2  | 0.014  | 0.022 | 0.35        | 0.56  |
| b   | 0.060  | TYP   | 1.52        | TYP   |
| b2  | 0.008  | 0.014 | 0.20        | 0.36  |
| C   | 0.735  | 0.775 | 18.67       | 19.69 |
| D   | 0.005  | ---   | 0.13        | ---   |
| D1  | 0.300  | 0.325 | 7.62        | 8.26  |
| E   | 0.240  | 0.280 | 6.10        | 7.11  |
| E1  | 0.100  | BSC   | 2.54        | BSC   |
| e   | ---    | 0.430 | ---         | 10.92 |
| eB  | 0.115  | 0.150 | 2.92        | 3.81  |
| L   | ---    | ---   | ---         | 10°   |
| M   | ---    | ---   | ---         | 10°   |

**GENERIC**  
**MARKING DIAGRAM\***



XXXXX = Specific Device Code  
A = Assembly Location  
WL = Wafer Lot  
YY = Year  
WW = Work Week  
G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

STYLES ON PAGE 2

|                         |                    |                                                                                                                                                                                  |
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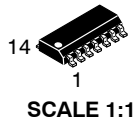
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|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| STYLE 1:<br>PIN 1. COLLECTOR<br>2. BASE<br>3. EMITTER<br>4. NO<br>CONNECTION<br>5. EMITTER<br>6. BASE<br>7. COLLECTOR<br>8. COLLECTOR<br>9. BASE<br>10. EMITTER<br>11. NO<br>CONNECTION<br>12. EMITTER<br>13. BASE<br>14. COLLECTOR                                                                       | STYLE 2:<br>CANCELLED                                                                                                                                                                                                                                                                                            | STYLE 3:<br>CANCELLED                                                                                                                                                                                                                         | STYLE 4:<br>PIN 1. DRAIN<br>2. SOURCE<br>3. GATE<br>4. NO<br>CONNECTION<br>5. GATE<br>6. SOURCE<br>7. DRAIN<br>8. DRAIN<br>9. SOURCE<br>10. GATE<br>11. NO<br>CONNECTION<br>12. GATE<br>13. SOURCE<br>14. DRAIN                                                                                            |
| STYLE 5:<br>PIN 1. GATE<br>2. DRAIN<br>3. SOURCE<br>4. NO CONNECTION<br>5. SOURCE<br>6. DRAIN<br>7. GATE<br>8. GATE<br>9. DRAIN<br>10. SOURCE<br>11. NO CONNECTION<br>12. SOURCE<br>13. DRAIN<br>14. GATE                                                                                                 | STYLE 6:<br>PIN 1. COMMON CATHODE<br>2. ANODE/CATHODE<br>3. ANODE/CATHODE<br>4. NO CONNECTION<br>5. ANODE/CATHODE<br>6. NO CONNECTION<br>7. ANODE/CATHODE<br>8. ANODE/CATHODE<br>9. ANODE/CATHODE<br>10. NO CONNECTION<br>11. ANODE/CATHODE<br>12. ANODE/CATHODE<br>13. NO CONNECTION<br>14. COMMON ANODE        | STYLE 7:<br>PIN 1. NO CONNECTION<br>2. ANODE<br>3. ANODE<br>4. NO CONNECTION<br>5. ANODE<br>6. NO CONNECTION<br>7. ANODE<br>8. ANODE<br>9. ANODE<br>10. NO CONNECTION<br>11. ANODE<br>12. ANODE<br>13. NO CONNECTION<br>14. COMMON<br>CATHODE | STYLE 8:<br>PIN 1. NO CONNECTION<br>2. CATHODE<br>3. CATHODE<br>4. NO CONNECTION<br>5. CATHODE<br>6. NO CONNECTION<br>7. CATHODE<br>8. CATHODE<br>9. CATHODE<br>10. NO CONNECTION<br>11. CATHODE<br>12. CATHODE<br>13. NO CONNECTION<br>14. COMMON ANODE                                                   |
| STYLE 9:<br>PIN 1. COMMON CATHODE<br>2. ANODE/CATHODE<br>3. ANODE/CATHODE<br>4. NO CONNECTION<br>5. ANODE/CATHODE<br>6. ANODE/CATHODE<br>7. COMMON ANODE<br>8. COMMON ANODE<br>9. ANODE/CATHODE<br>10. ANODE/CATHODE<br>11. NO CONNECTION<br>12. ANODE/CATHODE<br>13. ANODE/CATHODE<br>14. COMMON CATHODE | STYLE 10:<br>PIN 1. COMMON<br>CATHODE<br>2. ANODE/CATHODE<br>3. ANODE/CATHODE<br>4. ANODE/CATHODE<br>5. ANODE/CATHODE<br>6. NO CONNECTION<br>7. COMMON ANODE<br>8. COMMON<br>CATHODE<br>9. ANODE/CATHODE<br>10. ANODE/CATHODE<br>11. ANODE/CATHODE<br>12. ANODE/CATHODE<br>13. NO CONNECTION<br>14. COMMON ANODE | STYLE 11:<br>PIN 1. CATHODE<br>2. CATHODE<br>3. CATHODE<br>4. CATHODE<br>5. CATHODE<br>6. CATHODE<br>7. CATHODE<br>8. ANODE<br>9. ANODE<br>10. ANODE<br>11. ANODE<br>12. ANODE<br>13. ANODE<br>14. ANODE                                      | STYLE 12:<br>PIN 1. COMMON CATHODE<br>2. COMMON ANODE<br>3. ANODE/CATHODE<br>4. ANODE/CATHODE<br>5. ANODE/CATHODE<br>6. COMMON ANODE<br>7. COMMON CATHODE<br>8. ANODE/CATHODE<br>9. ANODE/CATHODE<br>10. ANODE/CATHODE<br>11. ANODE/CATHODE<br>12. ANODE/CATHODE<br>13. ANODE/CATHODE<br>14. ANODE/CATHODE |

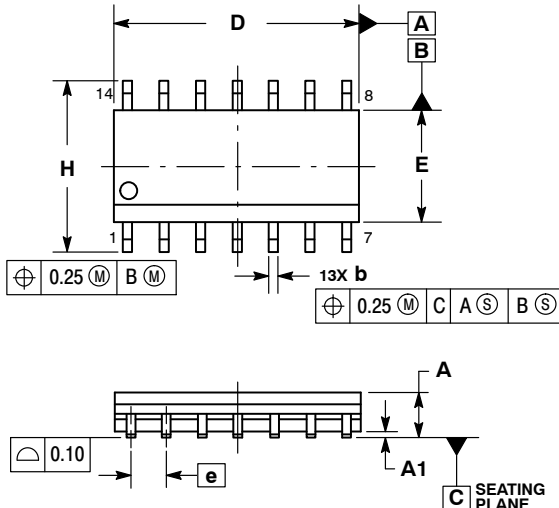
|                  |             |                                                                                                                                                                                     |
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SOIC-14 NB  
CASE 751A-03  
ISSUE L

DATE 03 FEB 2016

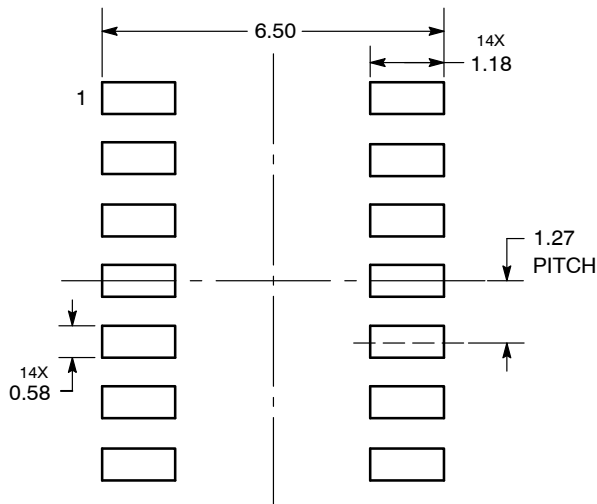


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE PROTRUSION SHALL BE 0.13 TOTAL IN EXCESS OF AT MAXIMUM MATERIAL CONDITION.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSIONS.
5. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.

| DIM | MILLIMETERS |      | INCHES    |       |
|-----|-------------|------|-----------|-------|
|     | MIN         | MAX  | MIN       | MAX   |
| A   | 1.35        | 1.75 | 0.054     | 0.068 |
| A1  | 0.10        | 0.25 | 0.004     | 0.010 |
| A3  | 0.19        | 0.25 | 0.008     | 0.010 |
| b   | 0.35        | 0.49 | 0.014     | 0.019 |
| D   | 8.55        | 8.75 | 0.337     | 0.344 |
| E   | 3.80        | 4.00 | 0.150     | 0.157 |
| e   | 1.27 BSC    |      | 0.050 BSC |       |
| H   | 5.80        | 6.20 | 0.228     | 0.244 |
| h   | 0.25        | 0.50 | 0.010     | 0.019 |
| L   | 0.40        | 1.25 | 0.016     | 0.049 |
| M   | 0°          | 7°   | 0°        | 7°    |

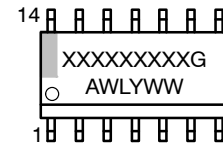
SOLDERING FOOTPRINT\*



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC  
MARKING DIAGRAM\*



XXXXXX = Specific Device Code  
A = Assembly Location  
WL = Wafer Lot  
Y = Year  
WW = Work Week  
G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

STYLES ON PAGE 2

|                  |             |                                                                                                                                                                                     |
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SOIC-14  
CASE 751A-03  
ISSUE L

DATE 03 FEB 2016

STYLE 1:  
PIN 1. COMMON CATHODE  
2. ANODE/CATHODE  
3. ANODE/CATHODE  
4. NO CONNECTION  
5. ANODE/CATHODE  
6. NO CONNECTION  
7. ANODE/CATHODE  
8. ANODE/CATHODE  
9. ANODE/CATHODE  
10. NO CONNECTION  
11. ANODE/CATHODE  
12. ANODE/CATHODE  
13. NO CONNECTION  
14. COMMON ANODE

STYLE 2:  
CANCELLED

STYLE 3:  
PIN 1. NO CONNECTION  
2. ANODE  
3. ANODE  
4. NO CONNECTION  
5. ANODE  
6. NO CONNECTION  
7. ANODE  
8. ANODE  
9. ANODE  
10. NO CONNECTION  
11. ANODE  
12. ANODE  
13. NO CONNECTION  
14. COMMON CATHODE

STYLE 4:  
PIN 1. NO CONNECTION  
2. CATHODE  
3. CATHODE  
4. NO CONNECTION  
5. CATHODE  
6. NO CONNECTION  
7. CATHODE  
8. CATHODE  
9. CATHODE  
10. NO CONNECTION  
11. CATHODE  
12. CATHODE  
13. NO CONNECTION  
14. COMMON ANODE

STYLE 5:  
PIN 1. COMMON CATHODE  
2. ANODE/CATHODE  
3. ANODE/CATHODE  
4. ANODE/CATHODE  
5. ANODE/CATHODE  
6. NO CONNECTION  
7. COMMON ANODE  
8. COMMON CATHODE  
9. ANODE/CATHODE  
10. ANODE/CATHODE  
11. ANODE/CATHODE  
12. ANODE/CATHODE  
13. NO CONNECTION  
14. COMMON ANODE

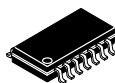
STYLE 6:  
PIN 1. CATHODE  
2. CATHODE  
3. CATHODE  
4. CATHODE  
5. CATHODE  
6. CATHODE  
7. CATHODE  
8. ANODE  
9. ANODE  
10. ANODE  
11. ANODE  
12. ANODE  
13. ANODE  
14. ANODE

STYLE 7:  
PIN 1. ANODE/CATHODE  
2. COMMON ANODE  
3. COMMON CATHODE  
4. ANODE/CATHODE  
5. ANODE/CATHODE  
6. ANODE/CATHODE  
7. ANODE/CATHODE  
8. ANODE/CATHODE  
9. ANODE/CATHODE  
10. ANODE/CATHODE  
11. COMMON CATHODE  
12. COMMON ANODE  
13. ANODE/CATHODE  
14. ANODE/CATHODE

STYLE 8:  
PIN 1. COMMON CATHODE  
2. ANODE/CATHODE  
3. ANODE/CATHODE  
4. NO CONNECTION  
5. ANODE/CATHODE  
6. ANODE/CATHODE  
7. COMMON ANODE  
8. COMMON ANODE  
9. ANODE/CATHODE  
10. ANODE/CATHODE  
11. NO CONNECTION  
12. ANODE/CATHODE  
13. ANODE/CATHODE  
14. COMMON CATHODE

|                  |             |                                                                                                                                                                                     |
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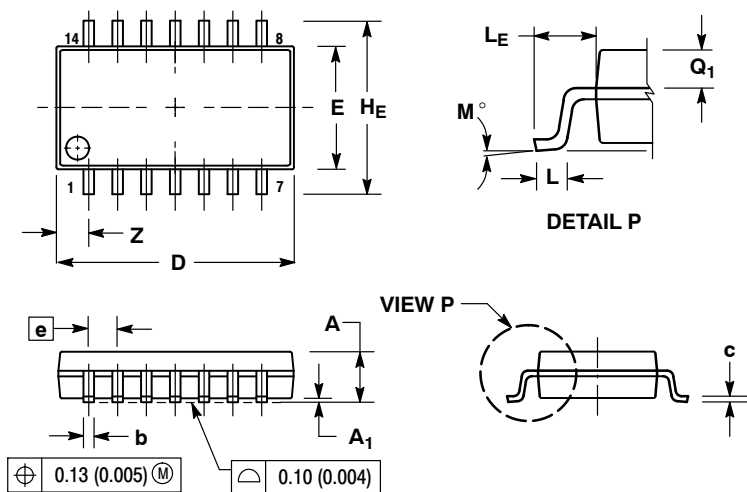
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SCALE 1:1

SOEIAJ-14  
CASE 965-01  
ISSUE B

DATE 29 FEB 2008



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS AND ARE MEASURED AT THE PARTING LINE. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
4. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
5. THE LEAD WIDTH DIMENSION (b) DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE LEAD WIDTH DIMENSION AT MAXIMUM MATERIAL CONDITION. DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OR THE FOOT. MINIMUM SPACE BETWEEN PROTRUSIONS AND ADJACENT LEAD TO BE 0.46 (0.018).

| DIM            | MILLIMETERS |       | INCHES    |       |
|----------------|-------------|-------|-----------|-------|
|                | MIN         | MAX   | MIN       | MAX   |
| A              | ---         | 2.05  | ---       | 0.081 |
| A <sub>1</sub> | 0.05        | 0.20  | 0.002     | 0.008 |
| b              | 0.35        | 0.50  | 0.014     | 0.020 |
| c              | 0.10        | 0.20  | 0.004     | 0.008 |
| D              | 9.90        | 10.50 | 0.390     | 0.413 |
| E              | 5.10        | 5.45  | 0.201     | 0.215 |
| e              | 1.27 BSC    |       | 0.050 BSC |       |
| H <sub>E</sub> | 7.40        | 8.20  | 0.291     | 0.323 |
| L              | 0.50        | 0.85  | 0.020     | 0.033 |
| L <sub>E</sub> | 1.10        | 1.50  | 0.043     | 0.059 |
| M              | 0°          | 10°   | 0°        | 10°   |
| Q <sub>1</sub> | 0.70        | 0.90  | 0.028     | 0.035 |
| Z              | ---         | 1.42  | ---       | 0.056 |

|                  |              |                                                                                                                                                                                     |
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