



Features

- Lead free versions available
- RoHS compliant (lead free version)*
- Low profile package
- Surface mount
- Very low forward voltage drop

Applications

- Cellular phones
- PDAs
- Desktop PCs and notebooks
- Digital cameras
- MP3 players

CD1607-B120 ~ B140L Schottky Barrier Rectifier Chip Diode

General Information

The markets of portable communications, computing and video equipment are challenging the semiconductor industry to develop increasingly smaller electronic components.

Bourns offers Schottky Rectifier Diodes for rectification applications, in compact chip package 1607 (Mini-SMA) size format, which offer PCB real estate savings and are considerably smaller than competitive parts. The Schottky Rectifier Diodes offer a forward current of 1 A with a choice of repetitive peak reverse voltage of 20 V up to 40 V.

Bourns® Chip Diodes conform to JEDEC standards, easy to handle on standard pick and place equipment and their flat configuration makes roll away much more difficult.

Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Parameter	Symbol	CD1607-				Unit
		B120	B120L	B140	B140L	
Forward Voltage (Max.) ($I_f = 1\text{ A}$)	V_F	0.5	0.38	0.5	0.4	V
Typical Junction Capacitance*	C_T	110	100	110	110	pF
Reverse Current (Max.) at Rated V_R	I_R	0.5	1.0	0.5	1.0	mA

* Measured at 1.0 MHz and applied reverse voltage of 4.0 V DC.

Absolute Ratings (@ $T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Parameter	Symbol	CD1607-				Unit
		B120	B120L	B140	B140L	
Repetitive Peak Reverse Voltage	V_{RRM}	20	20	40	40	V
Reverse Voltage	V_R	20	20	40	40	V
Maximum RMS Voltage	V_{RMS}	14	14	28	28	V
Avg. Forward Current	I_O	1				A
Forward Current, Surge Peak (60 Hz, 1 cycle)	I_{surge}	30*				A
Typical Thermal Resistance**	$R_{\theta JL}$	20				$^\circ\text{C/W}$
Storage Temperature	T_{STG}	-55 to +150				$^\circ\text{C}$
Junction Temperature	T_J	-55 to +125				$^\circ\text{C}$

** Thermal resistance junction to lead.

* Condition: 8.3 ms single half sine-wave superimposed on rate load (JEDEC method).



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www.bourns.com

How To Order

CD 1607 - B 1 20 L

Common Code _____
Chip Diode

Package _____
• 1607 = Mini-SMA

Model _____
B = Schottky Barrier Series

Average Forward Current (I_O) Code _____
1 = 1 A (Code x 1000 mA = Average Forward Current)

Reverse Voltage (V_R) Code _____
20 = 20 V
40 = 40 V

Forward Voltage Suffix _____
L = Low Forward Voltage V_f (CD1607-B120L, CD1607-B140L)

Terminations _____
LF = 100 % Sn (lead free)
Blank = Sn/Pb

*RoHS Directive 2002/95/EC Jan 27 2003 including Annex

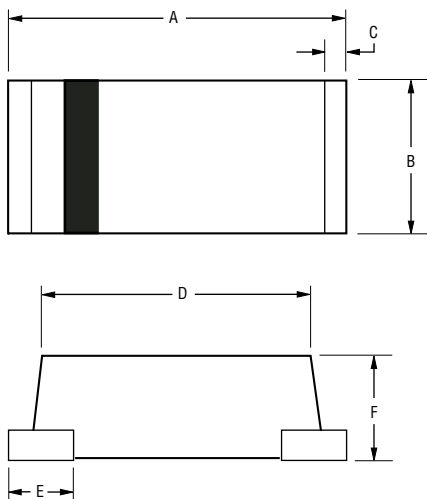
Specifications are subject to change without notice.

Customers should verify actual device performance in their specific applications.

CD1607-B120 ~ B140L Schottky Barrier Rectifier Chip Diode

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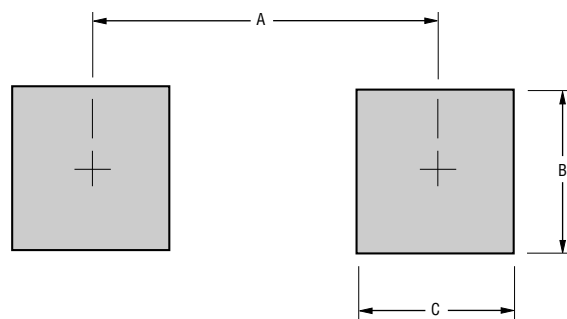
Product Dimensions



Dimension	Mini-SMA
A	$\frac{3.70 - 4.10}{(0.146 - 0.161)}$
B	$\frac{1.40 - 1.80}{(0.055 - 0.071)}$
C	$\frac{0.30}{(0.012)}$ TYP.
D	$\frac{2.40 - 2.80}{(0.094 - 0.110)}$
E	2 PLCS. $\frac{0.90}{(0.035)}$ TYP.
F	$\frac{1.40 - 1.60}{(0.055 - 0.063)}$

DIMENSIONS: $\frac{\text{MM}}{(\text{INCHES})}$

Recommended Pad Layout



Dimension	Mini-SMA
A (Max.)	$\frac{3.50}{(0.138)}$
B (Min.)	$\frac{1.50}{(0.059)}$
C (Min.)	$\frac{1.50}{(0.059)}$

DIMENSIONS: $\frac{\text{MM}}{(\text{INCHES})}$

Physical Specifications

Case.....1607 Molded plastic
PolarityColor band denotes cathode end
Terminals.....Solderable per MIL-STD-750, Method 206
WeightApproximately 0.04 grams

Typical Part Marking

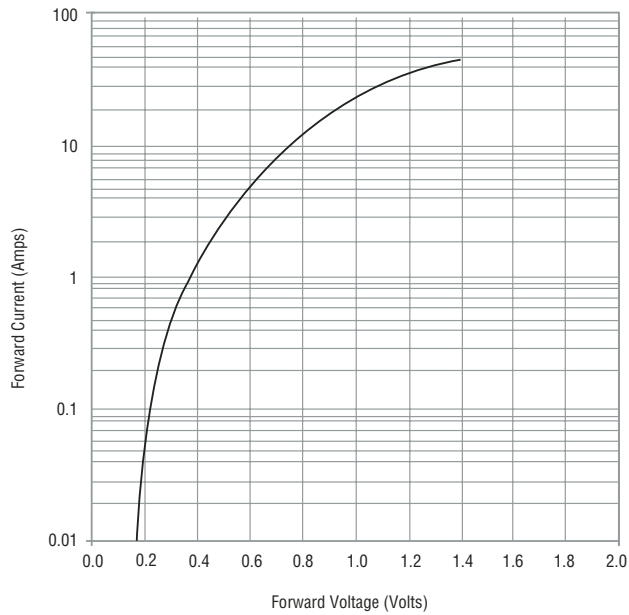
CD1607-B120I2
CD1607-B120LL2
CD1607-B140I4
CD1607-B140LL4

CD1607-B120 ~ B140L Schottky Barrier Rectifier Chip Diode

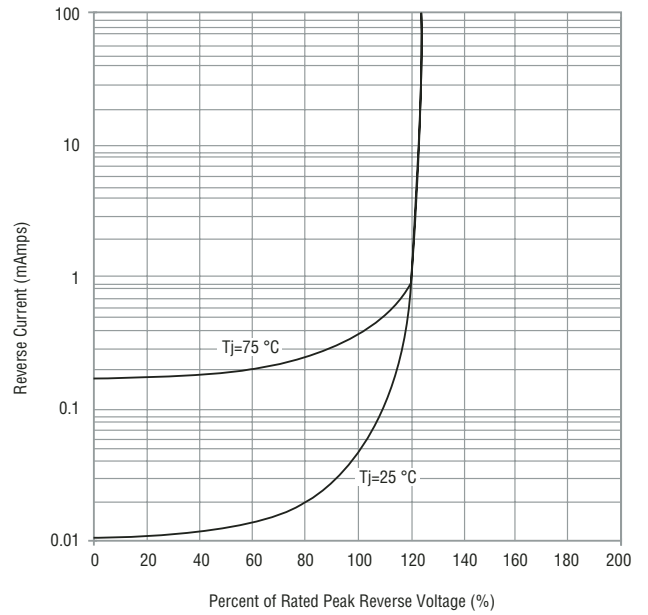
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Rating and Characteristic Curves: CD1607-B120 & CD1607-B140

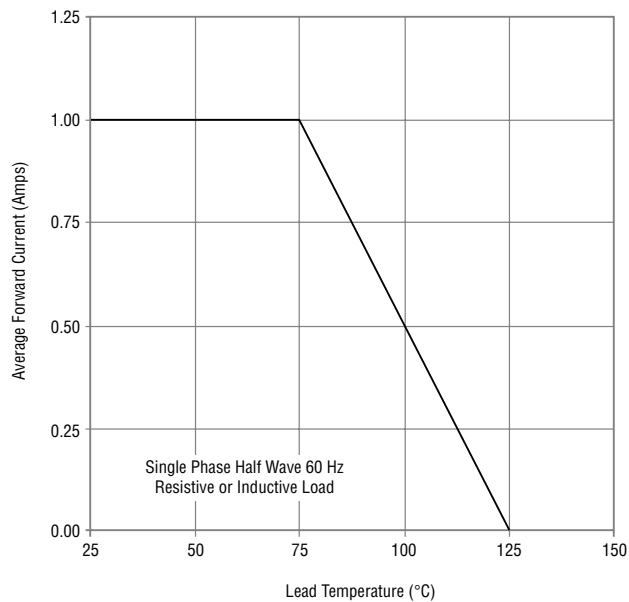
Forward Characteristics



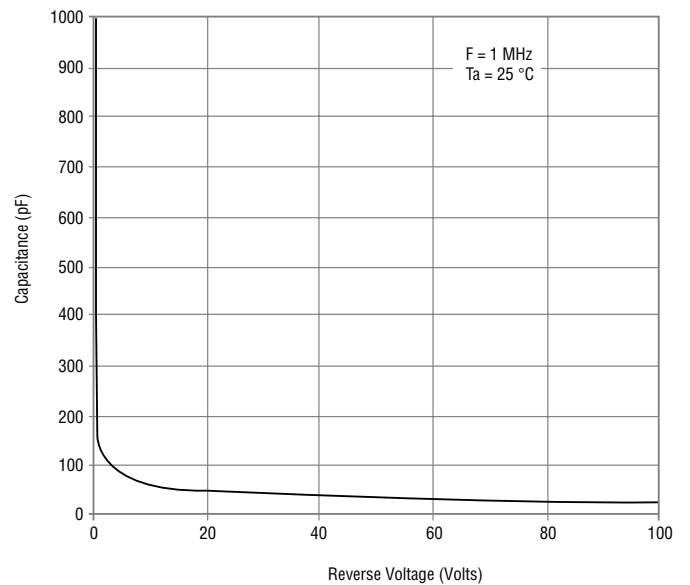
Reverse Characteristics



Derating Curve



Capacitance Between Terminals

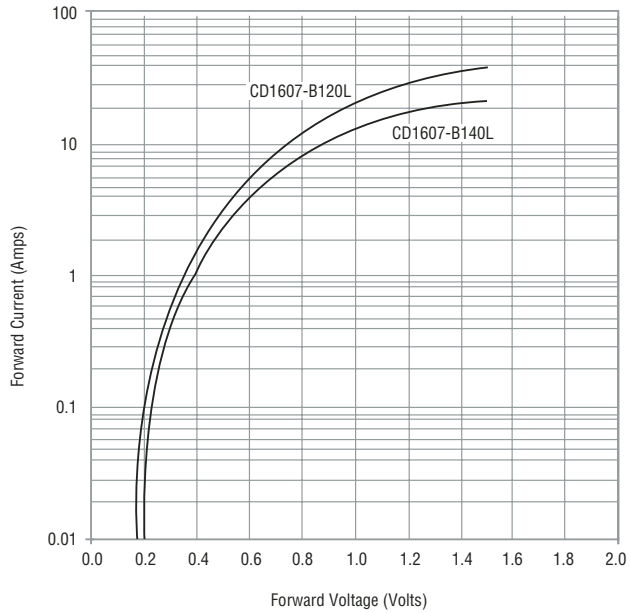


CD1607-B120 ~ B140L Schottky Barrier Rectifier Chip Diode

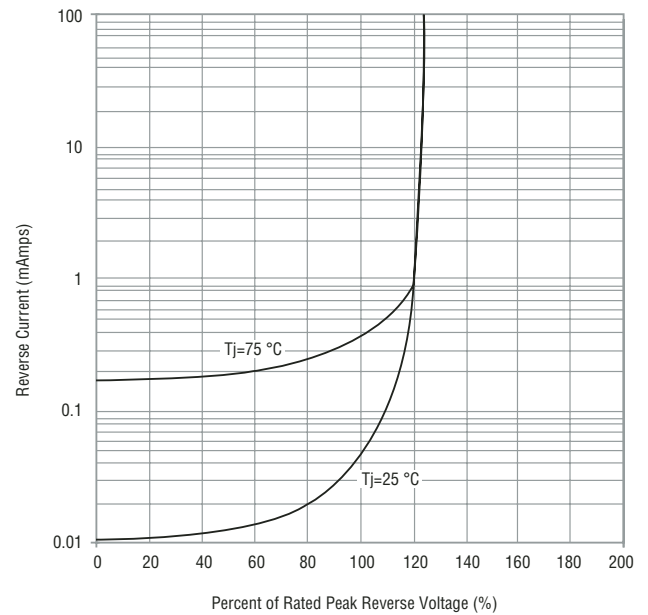
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Rating and Characteristic Curves: CD1607-B120L & CD1607-B140L

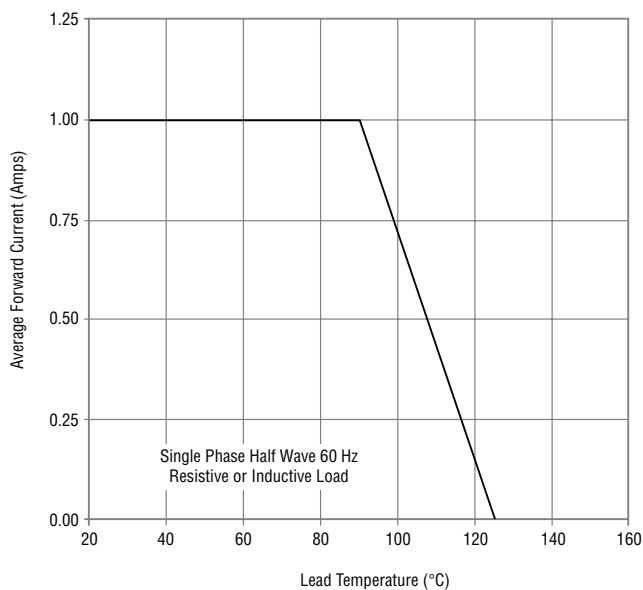
Forward Characteristics



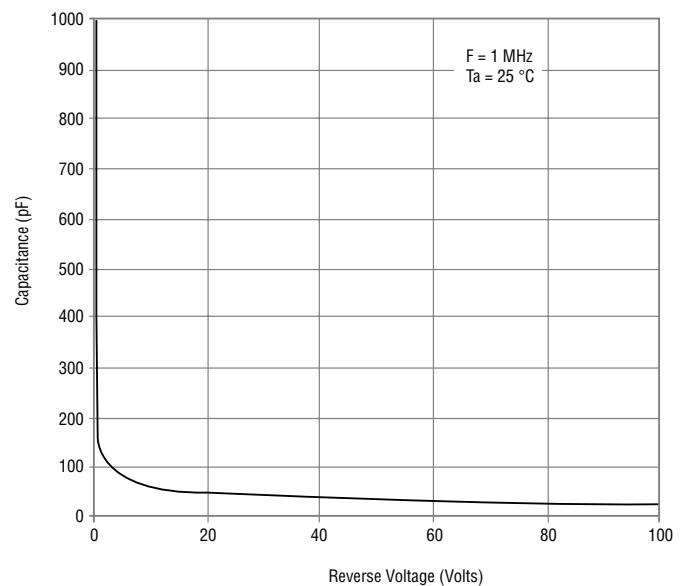
Reverse Characteristics



Derating Curve



Capacitance Between Terminals



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The product will be dispensed in Tape and Reel format (see diagram below).

REV. 02/05

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