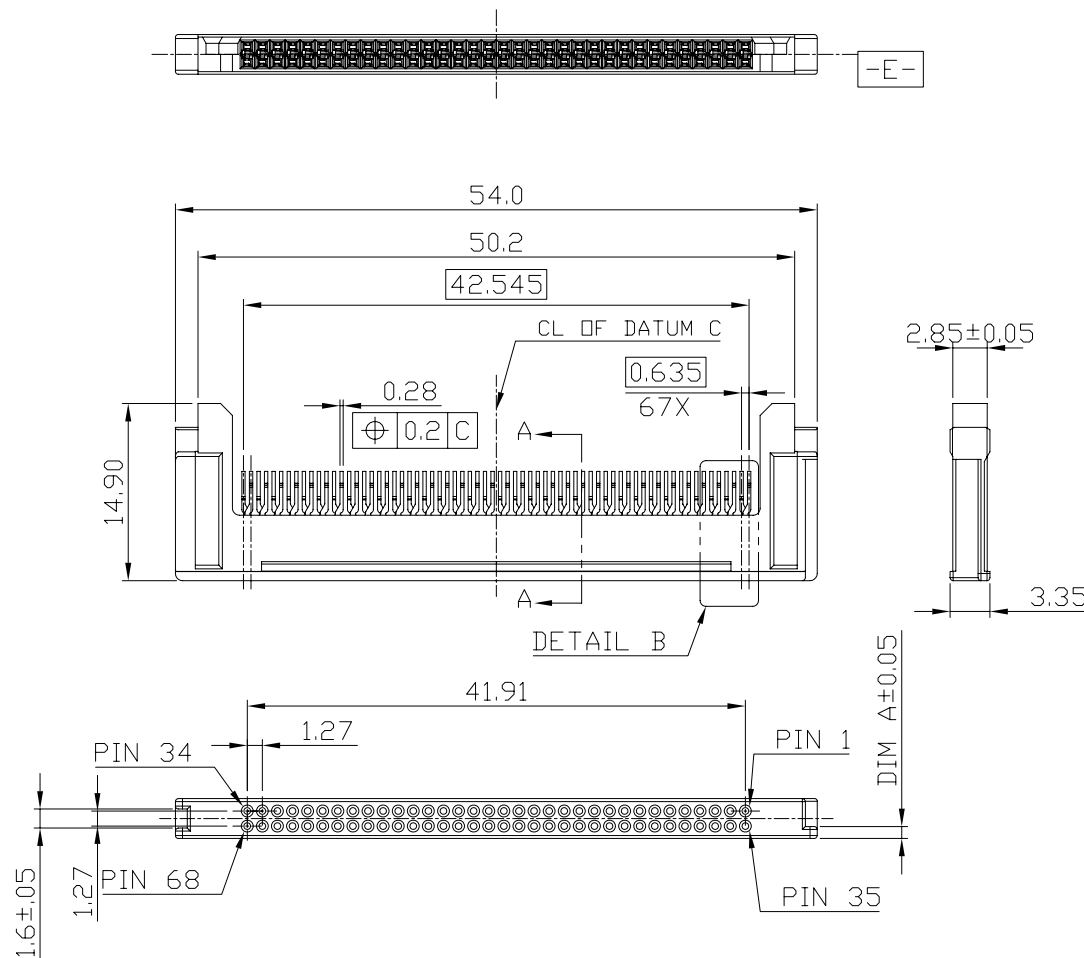
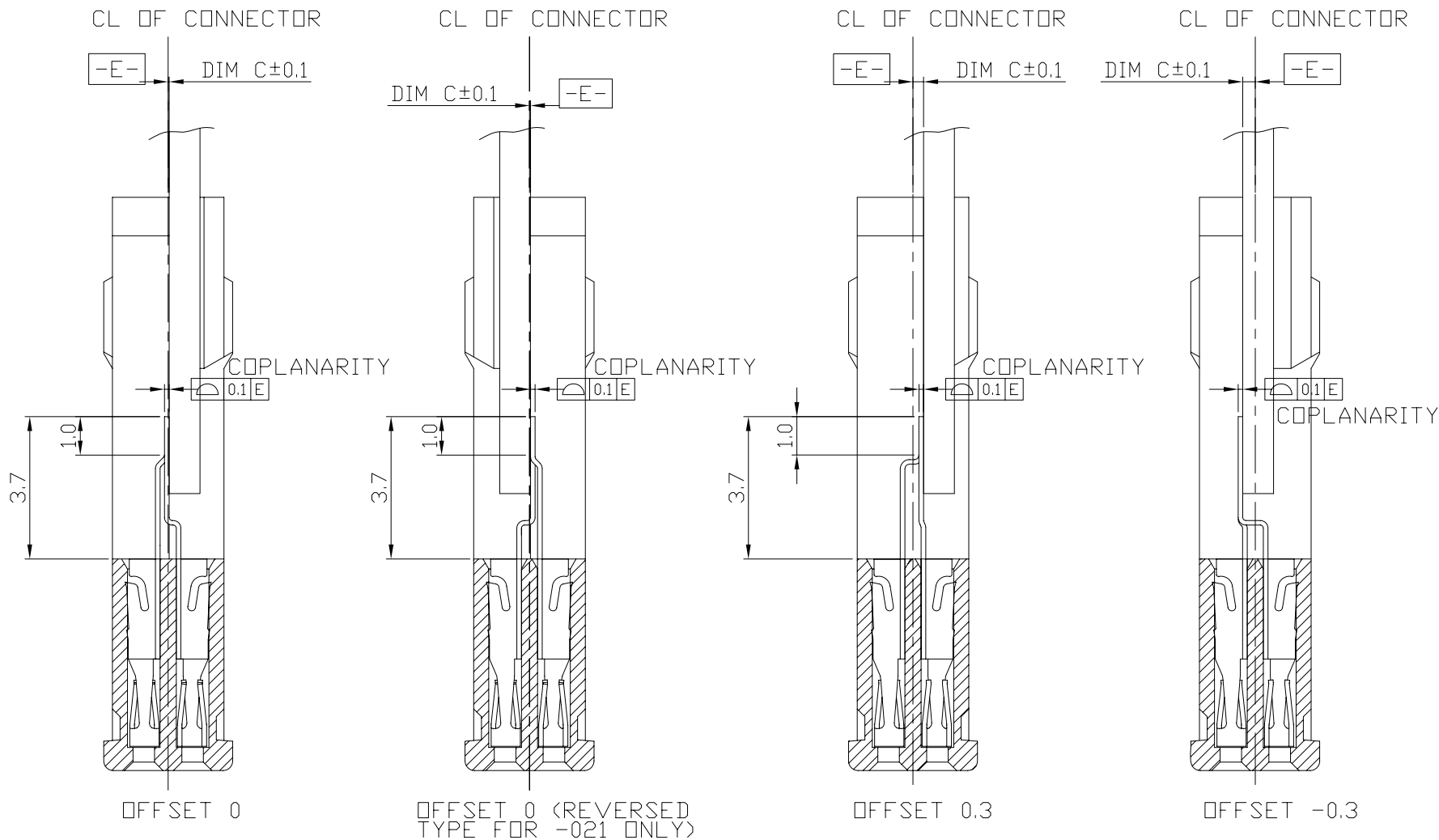


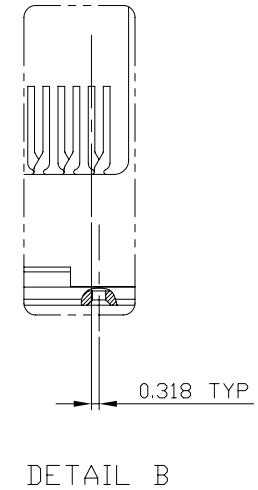
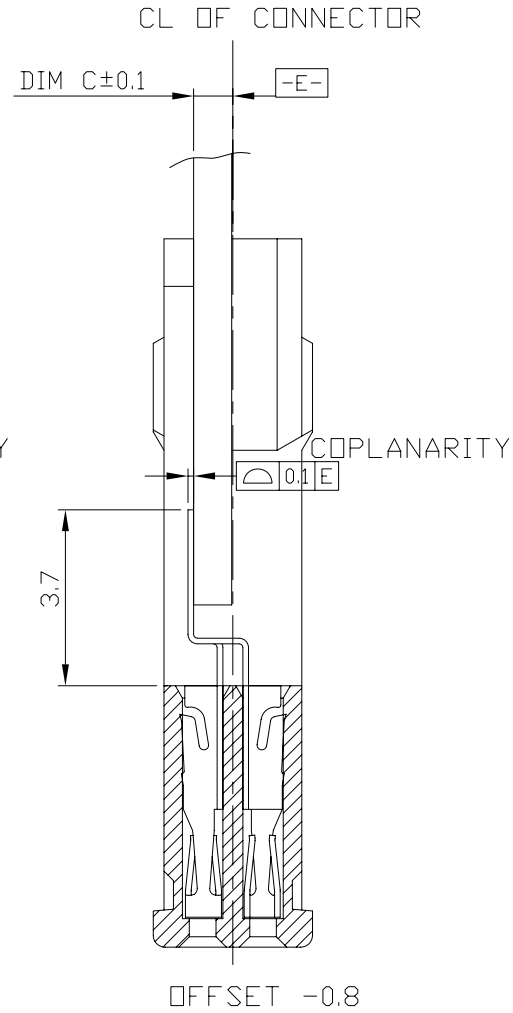
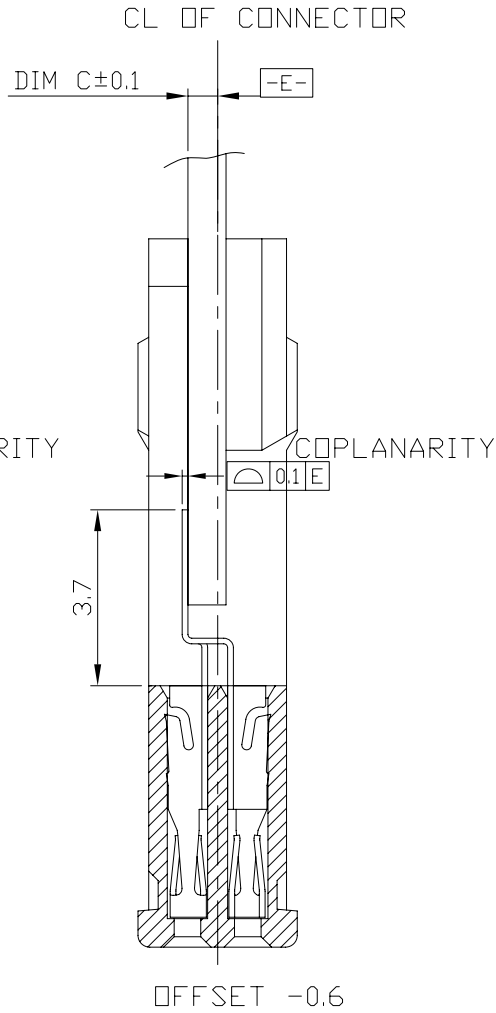
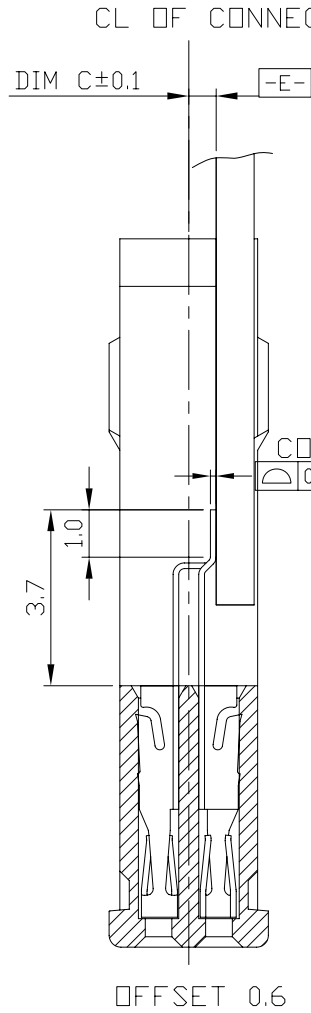


PRODUCT NO. 10014744	DIM A	VOLTAGE KEY	BOARD OFFSET DIM C	PACKING
-002 (-N02)	1.0	5.0V	0.3	SEE NOTE 5 FOR DETAIL DESCRIPTION
-003 (-N03)	1.0	5.0V	0	
-004 (-N04)	1.0	5.0V	-0.3	
-006 (-N06)	2.1	3.3V	0.3	
-007 (-N07)	2.1	3.3V	0	
-008 (-N08)	2.1	3.3V	-0.3	
-011 (N11)	1.0	5.0V	0.6	
-012 (-N12)	2.1	3.3V	0.6	
-013 (-N13)	1.0	5.0V	-0.6	
-014 (-N14)	2.1	3.3V	-0.6	
-015 (-N15)	1.0	5.0V	-0.8	
-021 (-N21)	1.0	5.0V	0	
SEE NOTE 6 FOR -NXX DETAIL				
SEE NOTE 7 FOR LEAD FREE DETAIL				
(NOTE: -021 & -N21 IS REVERSED TYPE)				

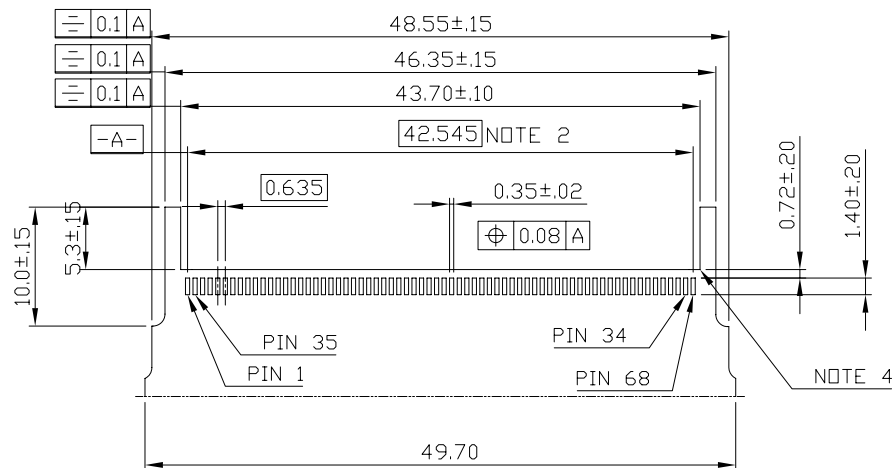


mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY	 www.fciconnect.com		
ltr	ecr no	dr	date						
H	0G08-0116	NZ	05/27/08	linear	.X ±.3	projection	title 68PIN PCMCIA RECEPTACLE ASS'Y product family PCMCIA size dwg no 10014744 A3		
					.XX ±.13				
					.XXX ±.050				
C	T03-0099	TAI	03/21/03	angles	0° ±2°	scale	code — sheet 1 of 4		
D	T03-0186	TAI	05/21/03	dr	KENNY TAI 12/17/02	1:1			
E	T03-0332	TAI	07/22/04	enr	KENNY TAI 12/17/02				
F	T03-0358	TAI	08/23/04	chr	KENNY TAI 12/17/02		cage code 22526		
G	0G06-0154	CS	04/06/06	appd	PAUL WANG 12/17/02				
sheet index	revision sheet	H	H	H	H				
		1	2	3	4				

[illegible]



mat'l. code				tolerances unless otherwise specified			CUSTOMER COPY		www.fciconnect.com	
ltr	ecm no	dr	date	linear	.X	±.3	projection	title		
H					.XX	±.13		68PIN PCMCIA RECEPTACLE ASS'Y		
				angles	.XXX	±.050				
				dr	KENNY TAI	12/17/02		product family	PCMCIA	code
				enrg	KENNY TAI	12/17/02		size	dwg no	sheet
				chr	KENNY TAI	12/17/02		A3	10014744	3 of
				appd	PAUL WANG	12/17/02		scale	1:1	
sheet index	revision sheet									



RECOMMENDED P.C.B. LAYOUT

NOTE:

1. MATERIAL:
CONTACT: PHBR 0.12t
HOUSING: LCP UL94V-0 (BLACK COLOR: -XXX; NATURAL COLOR: -NXX)
- FINISH:
CONTACT: ALL OVER--- Ni UNDER 0.5 MICRON THK.
CONTACT AREA--- Au or GXT 0.38 MICRON THK.
SOLDER TAIL---TIN LEAD 2.5 MICRON THK.
2. MAXIMUM PCB BOW ON THIS DIMENSION IS 0.1mm.
3. PACKING SPEC. REFER TO GS-14-723.
4. ELIMINATE THE CORNER TO PREVENT INTERFERENCE WITH SOLDER TAIL.
5. -XXX (-NXX) USE STD. PACKAGE, 1872 PCS / CARTON;
-XXXT (-NXXT) USE TAPE AND REEL, 750 PCS / REEL;
-XXXS (-NXXS) USE STD. PACKAGE, 200 PCS / CARTON;
-XXXX (-NXXX) USE TAPE AND REEL, 600 PCS / REEL.
6. -NXX USE NATURAL COLOR HOUSING. EX. -N02 IS THE SAME WITH -002 EXCEPT NATURAL COLOR.
7. -XXXXLF IS LEAD FREE PLATING. SOLDER TAIL IS PLATED TIN 2.5 MICRON.
8. PRODUCT SPEC. REFER TO GS-12-228.

mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY	 www.fciconnect.com
ltr	ecn no	dr	date				
H				linear	.X ±.3	projection	title
					.XX ±.13	 MM	68PIN PCMCIA RECEPTACLE ASS'Y
				angles	.XXX ±.050		
					0° ±2°	scale	product family PCMCIA
				dr	KENNY TAI 12/17/02		
				enrg	KENNY TAI 12/17/02	1:1	size dwg no
				chr	KENNY TAI 12/17/02		
				appd	PAUL WANG 12/17/02	A3	10014744
sheet index	revision sheet						
							code
							—
							sheet 4 of

cage code
22526