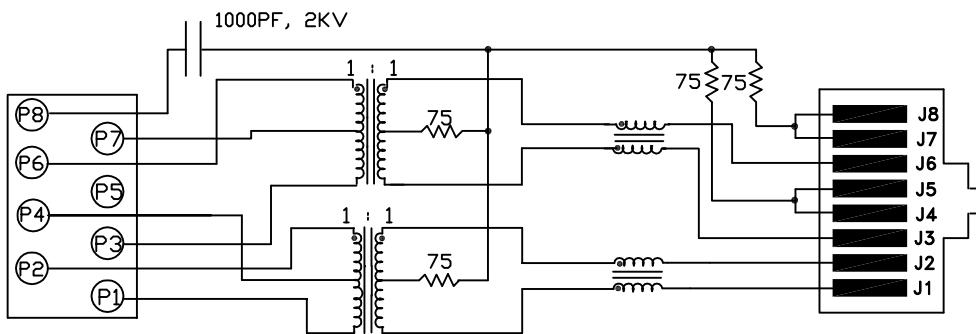
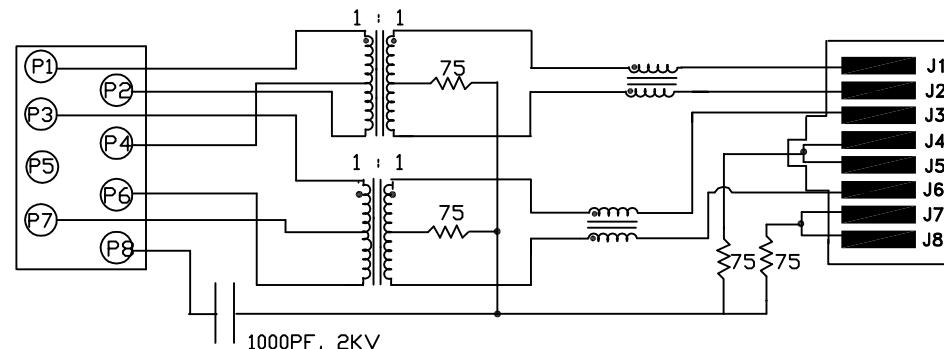


LOWER ROW



UPPER ROW



NOTES

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO: $(P1-P4-P2) : (J1-J2)$: 1CT : 1 $\pm 3\%$
 $(P3-P7-P6) : (J3-J6)$: 1CT : 1 $\pm 3\%$

2.0 INDUCTANCE: $(P6-P3)$: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias
 $(P2-P1)$: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias

3.0 LEAKAGE INDUCTANCE: $P6-P3$ (WITH J6 AND J3 SHORT) : 0.3uH MAX. @ 1MHz
 $P2-P1$ (WITH J2 AND J1 SHORT) : 0.3uH MAX. @ 1MHz

4.0 INTERWINDING CAPACITANCE: $(P6,P7,P3)$ TO $(J6,J3)$: 30pF MAX @ 1MHz
 $(P2,P4,P1)$ TO $(J2,J1)$: 30pF MAX @ 1MHz

5.0 DC RESISTANCE: $(J6-J3)=(J1-J2)$: 1.2 ohms Max.
 $(P6-P3)$: .7 ohms Max.
 $(P2-P1)$: .7 ohms Max.

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DRAWING NO.

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RECEIVE

6.0 RETURN LOSS: 1MHz TO 30MHz
60MHz TO 80MHz

: 18dB MIN.
: 12dB MIN.

7.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P2)
(J3, J6) TO (P3, P6) : 1500 VAC
: 1500 VAC

8.0 INSERTION LOSS: RS=RL=100 ohms
100KHz TO 100MHz : 1.1 dB TYP

9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS
OUTPUT VOLTAGE = 1 V peak : 3.0 nS MAX
PULSE WIDTH= 112nS : 3.0 nS MAX

10.0 CROSS TALK: 1MHz TO 100MHz : 40 dB TYP

11.0 COMMON TO COMMON MODE ATTENUATION: 1MHz TO 100MHz : 35dB TYP

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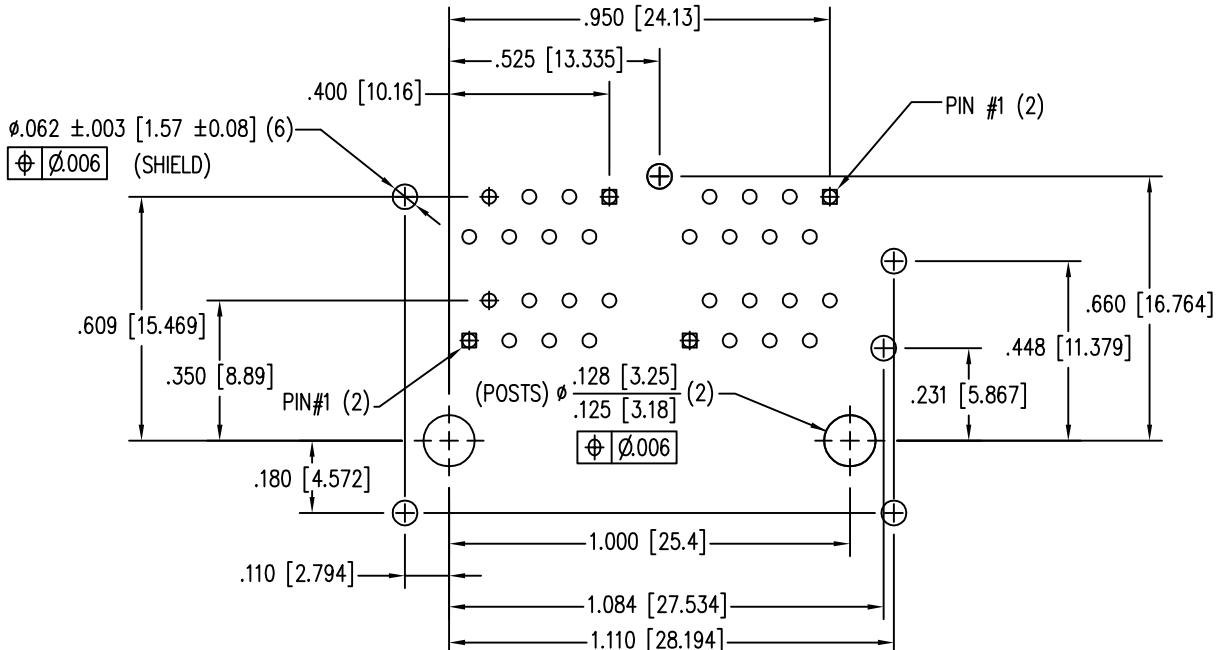
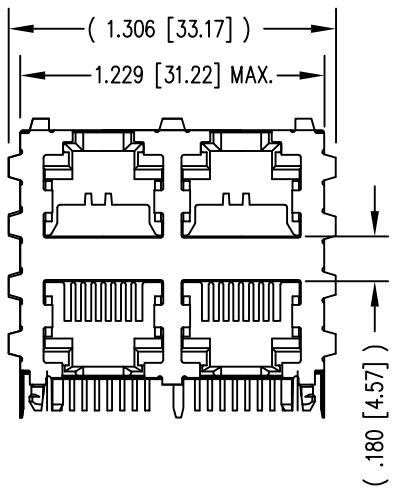
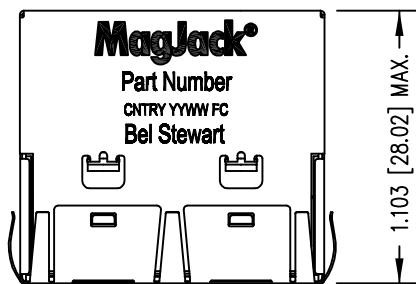
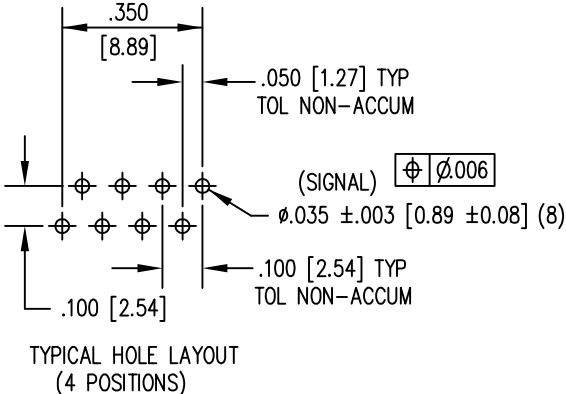
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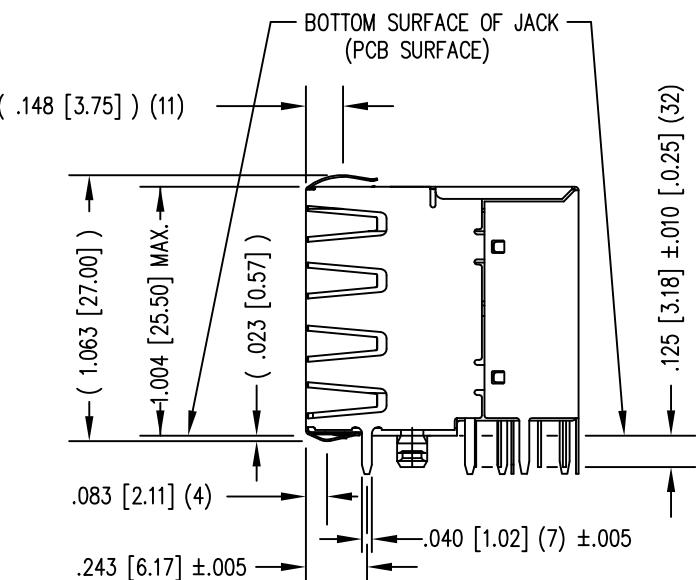
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SI-30135 02



P.C.B. RECOMMENDED HOLE LAYOUT
SEEN FROM COMPONENT SIDE

ALL CENTERLINE DIMENSIONS ARE BASIC.



NOTES:

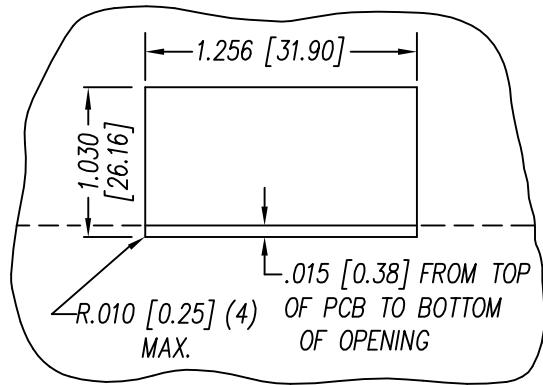
1. CONNECTOR MATERIALS:
HOUSING: THERMOPLASTIC UL94 V-0
CONTACT/SHIELD: COPPER ALLOY
SHIELD PLATING: NICKEL OR TIN
CONTACT PLATING: SELECTIVE GOLD,
50 MICRO-INCHES MIN. IN CONTACT AREA.
2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.
SEE ELECTRICAL DRAWING FOR OMITTED PINS.
3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
4. WAVE SOLDER COMPATIBLE - PREHEAT 125°C/90SECS.

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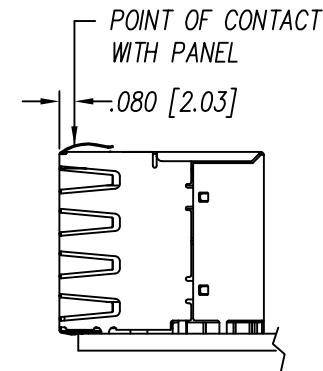
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SUGGESTED PANEL OPENING
(N.T.S.)



THE SUGGESTED PANEL OPENING IS INTENDED
TO GIVE THE USER THE ABILITY TO HAVE
REASONABLE JACK / PANEL CLEARANCES
YET MAINTAIN RELIABLE GROUNDING
CAPABILITY.

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