

TPCP8207

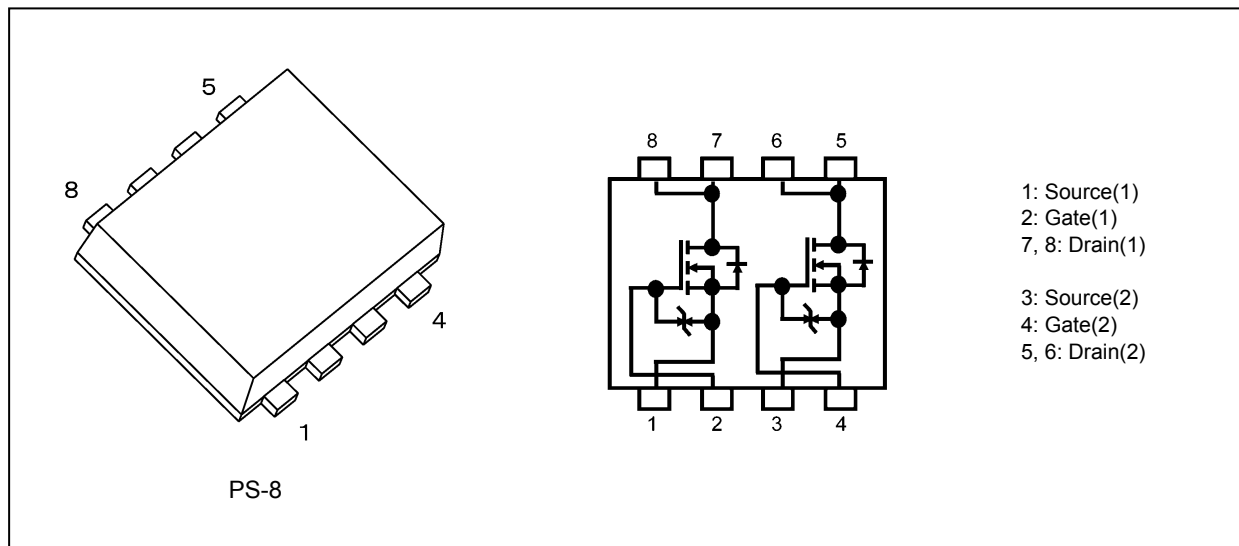
1. Applications

- Motor Drivers
- Mobile Equipment

2. Features

- (1) Small footprint due to a small and thin package
- (2) Small gate charge : $Q_{SW} = 4.7 \text{ nC (typ.)}$
- (3) Low drain-source on-resistance: $R_{DS(ON)} = 29.1 \text{ m}\Omega \text{ (typ.)}$ ($V_{GS} = 10 \text{ V}$)
- (4) Low leakage current: $I_{DSS} = 10 \text{ }\mu\text{A (max)}$ ($V_{DS} = 40 \text{ V}$)
- (5) Enhancement mode: $V_{th} = 2 \text{ to } 3 \text{ V}$ ($V_{DS} = 10 \text{ V}$, $I_D = 0.5 \text{ mA}$)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) ($T_a = 25^\circ\text{C}$ unless otherwise specified)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	40	V
Gate-source voltage	V_{GSS}	± 20	
Drain current (DC) (Note 1)	I_{D}	5	A
Drain current (pulsed) (Note 1)	I_{DP}	20	
Power dissipation (single operation) (t = 5 s) (Note 2), (Note 4)	$P_{\text{D}(1)}$	1.77	W
Power dissipation (per device for dual operation) (t = 5 s) (Note 2), (Note 5)	$P_{\text{D}(2)}$	1.47	
Power dissipation (single operation) (t = 5 s) (Note 3), (Note 4)	$P_{\text{D}(1)}$	0.69	
Power dissipation (per device for dual operation) (t = 5 s) (Note 3), (Note 5)	$P_{\text{D}(2)}$	0.43	
Single-pulse avalanche energy (Note 6)	E_{AS}	26	mJ
Avalanche current	I_{AR}	5	A
Channel temperature (Note 7)	T_{ch}	175	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-ambient thermal resistance (single operation) (t = 5 s) (Note 2), (Note 4)	$R_{\text{th(ch-a)}(1)}$	84.4	$^\circ\text{C/W}$
Channel-to-ambient thermal resistance (per device for dual operation) (t = 5 s) (Note 2), (Note 5)	$R_{\text{th(ch-a)}(2)}$	101.6	
Channel-to-ambient thermal resistance (single operation) (t = 5 s) (Note 3), (Note 4)	$R_{\text{th(ch-a)}(1)}$	215.5	
Channel-to-ambient thermal resistance (per device for dual operation) (t = 5 s) (Note 3), (Note 5)	$R_{\text{th(ch-a)}(2)}$	347.2	

Note 1: Ensure that the channel temperature does not exceed 175°C .

Note 2: Device mounted on a glass-epoxy board (a), Figure 5.1

Note 3: Device mounted on a glass-epoxy board (b), Figure 5.2

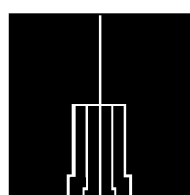
Note 4: Power dissipation and thermal resistance values per device with the other device being off (During single operation, power is supplied to only one of the two devices.)

Note 5: Power dissipation and thermal resistance values per device for dual operation (During dual operation, power is evenly supplied to both devices.)

Note 6: $V_{\text{DD}} = 24\text{ V}$, $T_{\text{ch}} = 25^\circ\text{C}$ (initial), $L = 1.12\text{ mH}$, $R_{\text{G}} = 25\ \Omega$, $I_{\text{AR}} = 5\text{ A}$

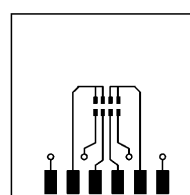
Note 7: Merely channel temperature is guaranteed 175°C .

Storage temperature range is guaranteed as usual (-55 to 150°C).



FR-4
25.4 × 25.4 × 0.8
(Unit: mm)

Fig. 5.1 Device Mounted on a Glass-Epoxy Board (a)



FR-4
25.4 × 25.4 × 0.8
(Unit: mm)

Fig. 5.2 Device Mounted on a Glass-Epoxy Board (b)

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

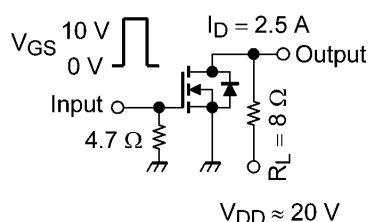
6. Electrical Characteristics

6.1. Static Characteristics ($T_a = 25^\circ\text{C}$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current	I_{GSS}	$V_{GS} = \pm 16\text{ V}$, $V_{DS} = 0\text{ V}$	—	—	± 10	μA
Drain cut-off current	I_{DSS}	$V_{DS} = 40\text{ V}$, $V_{GS} = 0\text{ V}$	—	—	10	
Drain-source breakdown voltage	$V_{(BR)DSS}$	$I_D = 10\text{ mA}$, $V_{GS} = 0\text{ V}$	40	—	—	V
	$V_{(BR)DSX}$	$I_D = 10\text{ mA}$, $V_{GS} = -20\text{ V}$	20	—	—	
Gate threshold voltage	V_{th}	$V_{DS} = 10\text{ V}$, $I_D = 0.5\text{ mA}$	2	2.5	3	
Drain-source on-resistance	$R_{DS(ON)}$	$V_{GS} = 6\text{ V}$, $I_D = 2.5\text{ A}$	—	39.3	51	$\text{m}\Omega$
		$V_{GS} = 10\text{ V}$, $I_D = 2.5\text{ A}$	—	29.1	37.8	

6.2. Dynamic Characteristics ($T_a = 25^\circ\text{C}$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Input capacitance	C_{iss}	$V_{DS} = 10\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1\text{ MHz}$	—	505	—	pF
Reverse transfer capacitance	C_{rss}		—	66	—	
Output capacitance	C_{oss}		—	115	—	
Switching time (rise time)	t_r	See Figure 6.2.1.	—	5.4	—	ns
Switching time (turn-on time)	t_{on}		—	12	—	
Switching time (fall time)	t_f		—	4.5	—	
Switching time (turn-off time)	t_{off}		—	18	—	



Duty $\leq 1\%$, $t_w = 10\text{ }\mu\text{s}$

Fig. 6.2.1 Switching Time Test Circuit

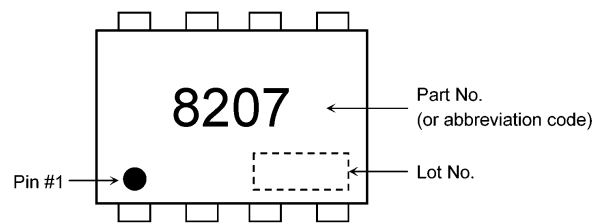
6.3. Gate Charge Characteristics ($T_a = 25^\circ\text{C}$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Q_g	$V_{DD} \approx 32\text{ V}$, $V_{GS} = 10\text{ V}$, $I_D = 5\text{ A}$	—	11.8	—	nC
Gate-source charge 1	Q_{gs1}		—	2.1	—	
Gate-drain charge	Q_{gd}		—	3.9	—	
Gate switch charge	Q_{SW}		—	4.7	—	

6.4. Source-Drain Characteristics ($T_a = 25^\circ\text{C}$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Reverse drain current (pulsed) (Note 8)	I_{DRP}	—	—	—	20	A
Diode forward voltage	V_{DSF}	$I_{DR} = 5\text{ A}$, $V_{GS} = 0\text{ V}$	—	—	-1.2	V

Note 8: Ensure that the channel temperature does not exceed 175°C .

7. Marking**Fig. 7.1 Marking**

8. Characteristics Curves (Note)

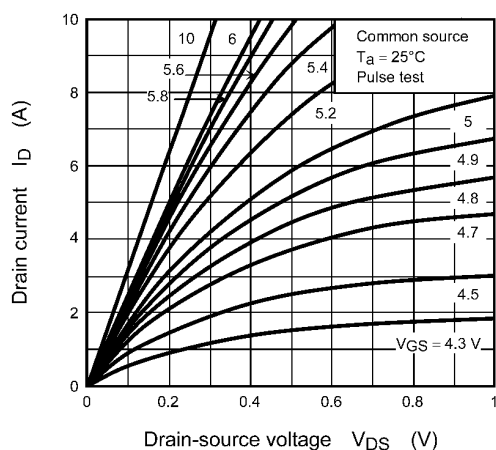


Fig. 8.1 $I_D - V_{DS}$

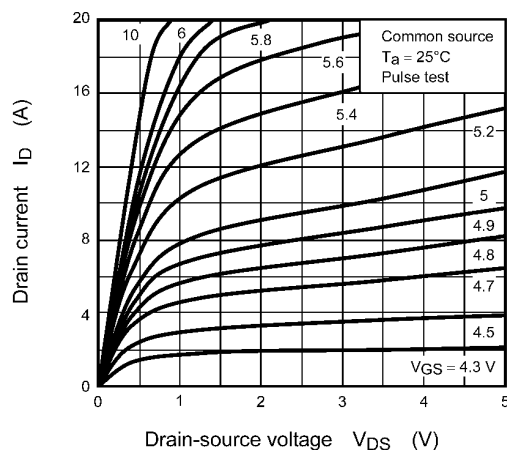


Fig. 8.2 $I_D - V_{DS}$

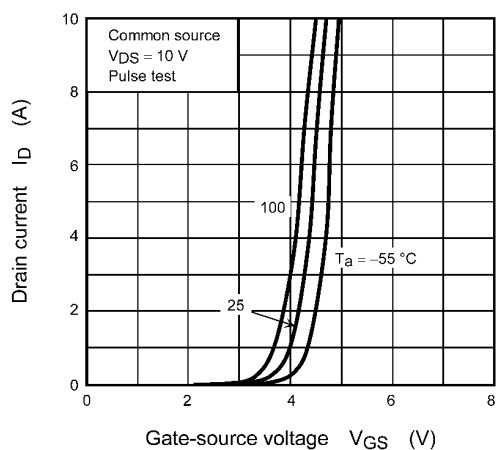


Fig. 8.3 $I_D - V_{GS}$

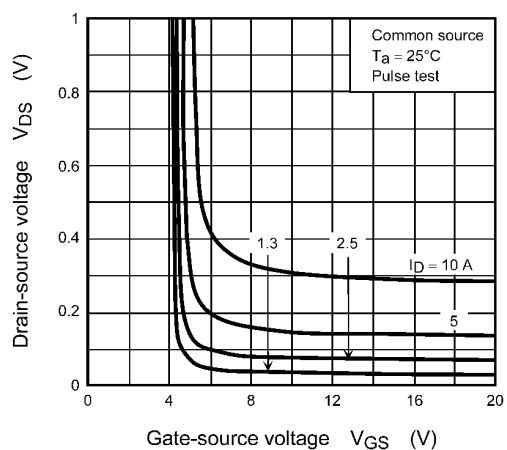


Fig. 8.4 $V_{DS} - V_{GS}$

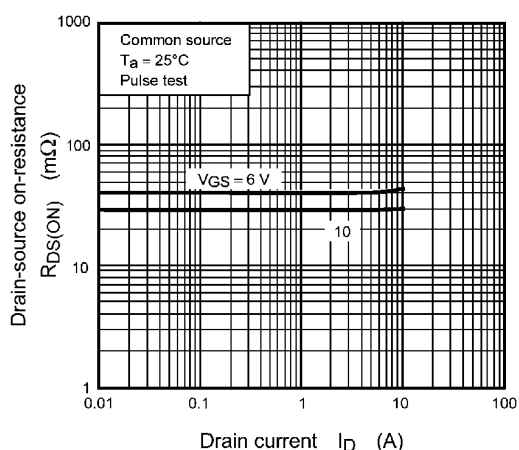


Fig. 8.5 $R_{DS(ON)} - I_D$

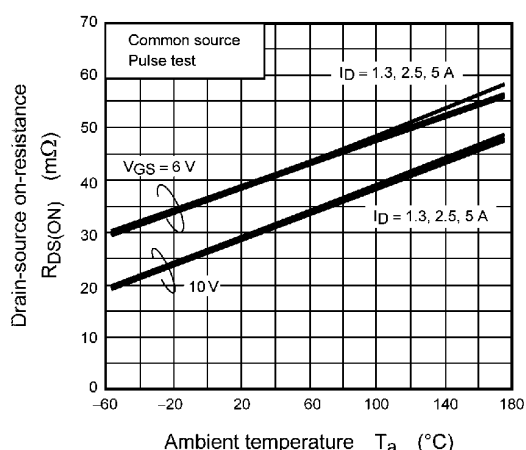


Fig. 8.6 $R_{DS(ON)} - T_a$ (Note 9)

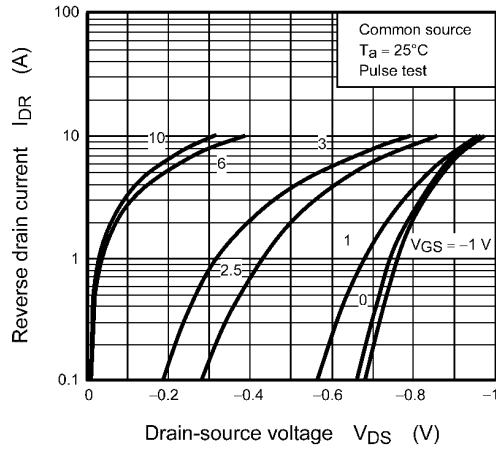


Fig. 8.7 $I_{DR} - V_{DS}$

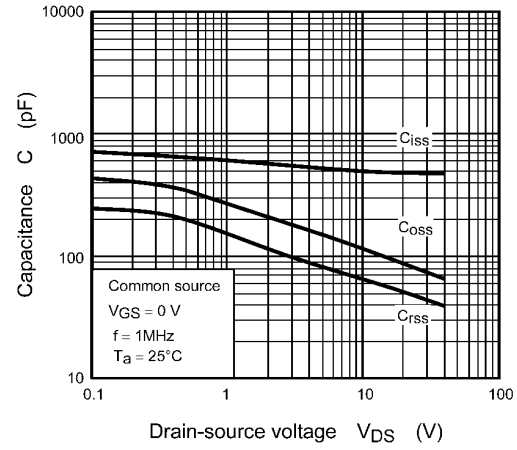


Fig. 8.8 Capacitance - V_{DS}

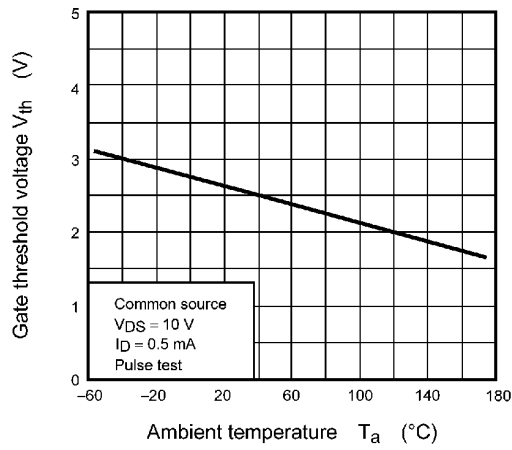


Fig. 8.9 $V_{th} - T_a$ (Note 9)

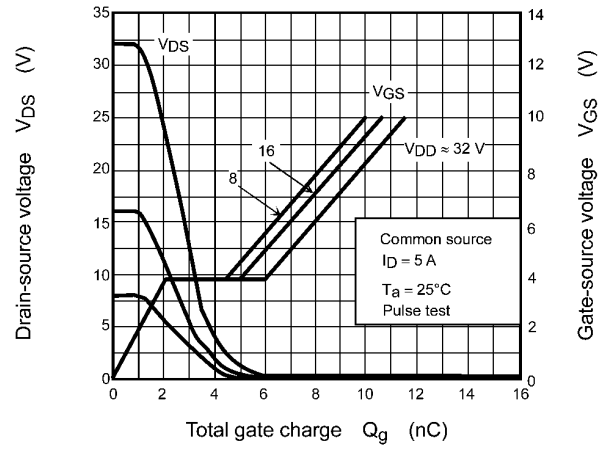
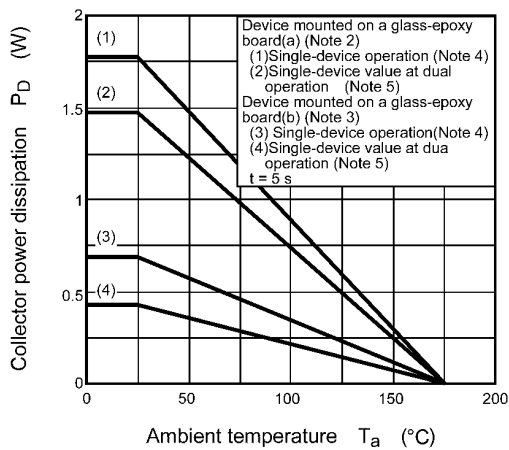


Fig. 8.10 Dynamic Input/Output Characteristics



**Fig. 8.11 $P_D - T_a$ (Note 9)
(Guaranteed Maximum)**

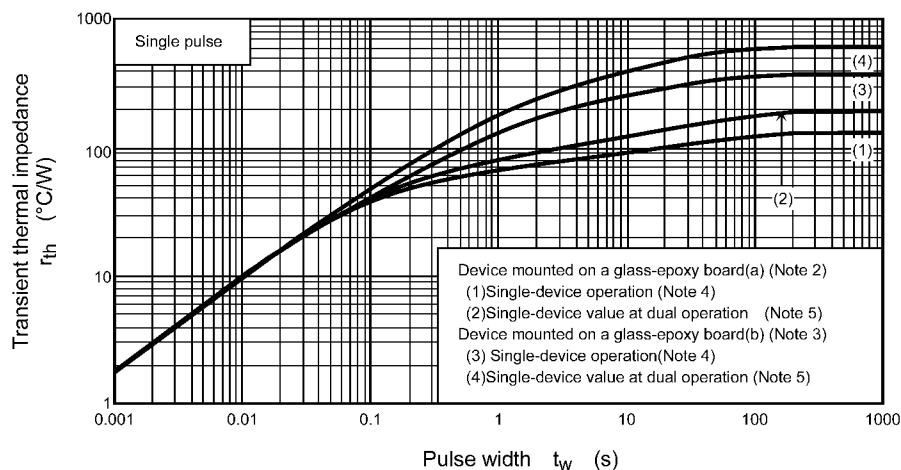


Fig. 8.12 $r_{th} - t_w$
(Guaranteed Maximum)

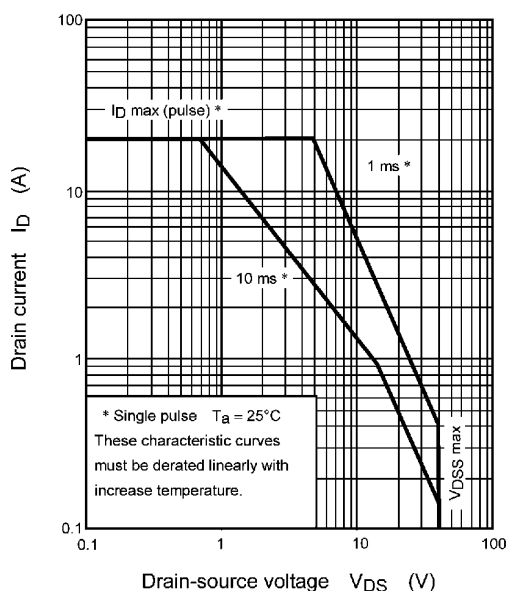


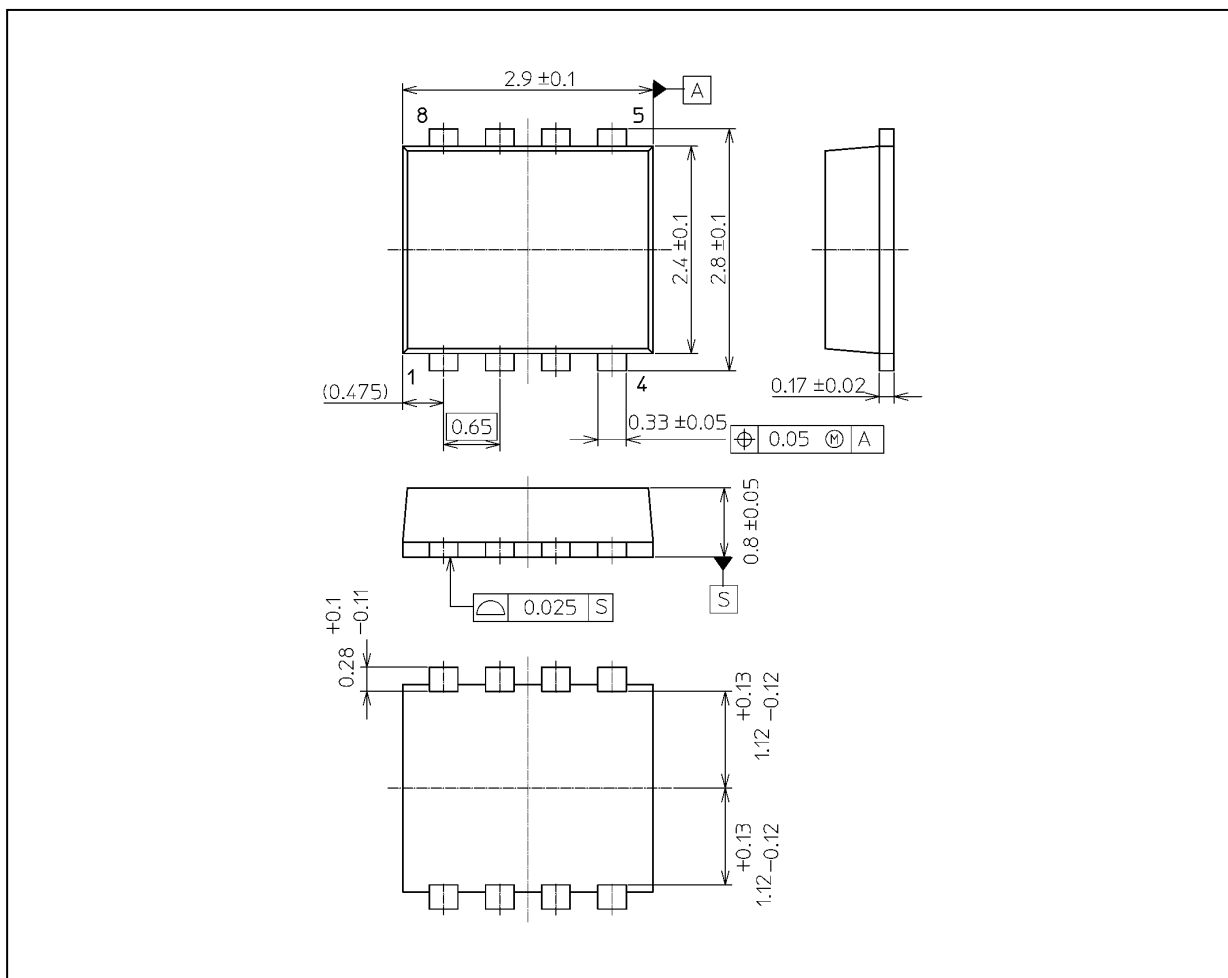
Fig. 8.13 Safe Operating Area
(Guaranteed Maximum)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Note 9: Although several performance curves are shown up to a $T_a = 175^\circ\text{C}$, the device is not guaranteed at storage temperatures up to 175°C . The storage temperature (T_{stg}) range is rated at -55°C to 150°C .

Package Dimensions

Unit: mm



Weight: 0.017 g (typ.)

Package Name(s)
TOSHIBA: 2-3V1S
Nickname: PS-8

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