

Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D $T_A = +25^\circ\text{C}$
-50V	$6\Omega @ V_{GS} = -4\text{ V}$	-200mA
	$8\Omega @ V_{GS} = -2.5\text{ V}$	-160mA

Description

This new generation MOSFET has been designed to minimize the on-state resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays

Features and Benefits

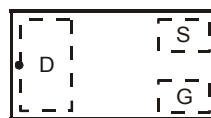
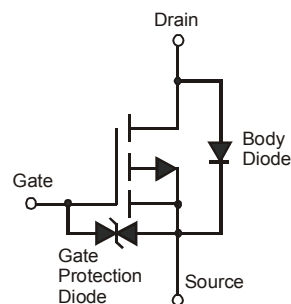
- Low On-Resistance
- ESD Protected Gate
- Low Input/Output Leakage
- Fast Switching Speed
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 (e4)
- Terminal Connections: See Diagram
- Weight: 0.001 grams (approximate)



X-DFN1006-3


 Top View
 Internal Schematic


Equivalent Circuit

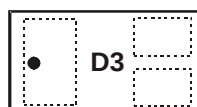
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP56D0UFB-7	X1-DFN1006-3	3000/Tape & Reel
DMP56D0UFB-7B	X1-DFN1006-3	10000/Tape & Reel

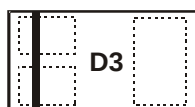
- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>

Marking Information

DMP56D0UFB-7


 Top View
 Dot Denotes
 Drain Side

DMP56D0UFB-7B


 Top View
 Bar Denotes Gate
 and Source Side

D3 = Product Type Marking Code

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V _{DSS}	-50	V
Gate-Source Voltage	V _{GSS}	±8	V
Drain Current (Note 5) Steady T _A = +25°C	I _D	-200	mA
Pulsed Drain Current (Note 6)	I _{DM}	-700	mA

Thermal Characteristics

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P _D	425	mW
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	R _{θJA}	275	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-50	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-10	μA	V _{DS} = -50V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±1	μA	V _{GS} = ±8V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	-0.5	—	-1.2	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	4.6 6	6 8	Ω	V _{GS} = -4.0V, I _D = -100mA V _{GS} = -2.5V, I _D = -80mA
Forward Transfer Admittance	Y _{fs}	100	—	—	mS	V _{DS} = -5V, I _D = -100mA
Diode Forward Voltage (Note 7)	V _{SD}	—	—	-1.2	V	V _{GS} = 0V, I _S = -100mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	50.54	—	pF	V _{DS} = -25V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	3.49	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	2.42	—	pF	
Gate Resistance	R _G	—	201	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz
Total Gate Charge V _{GS} = 4.5V	Q _g	—	0.58	—	nC	V _{GS} = -4V, V _{DS} = -25V, I _D = -100mA
Gate-Source Charge	Q _{gs}	—	0.09	—	nC	
Gate-Drain Charge	Q _{gd}	—	0.14	—	nC	
Turn-On Delay Time	t _{D(on)}	—	4.46	—	nS	V _{DD} = -30V, I _D = -0.27A, V _{GEN} = -4V, R _{GEN} = 6Ω
Turn-On Rise Time	t _r	—	6.63	—	nS	
Turn-Off Delay Time	t _{D(off)}	—	21.9	—	nS	
Turn-Off Fall Time	t _f	—	15.0	—	nS	

- Notes:
5. Device mounted on FR-4 PCB. t ≤ 5 sec.
 6. Pulse width ≤ 10μS, Duty Cycle ≤ 1%.
 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to production testing.

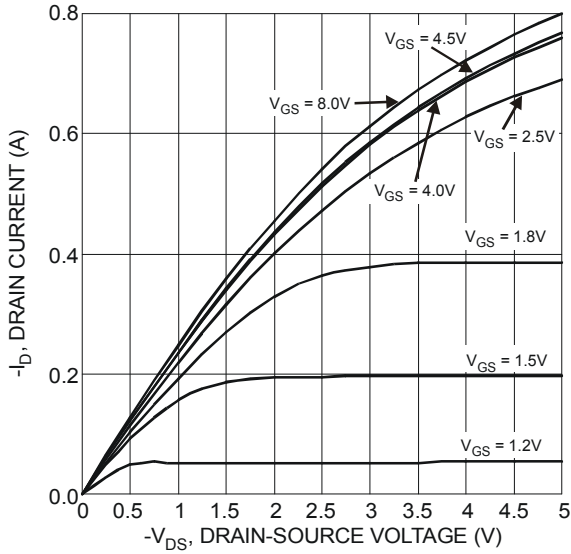


Figure 1 Typical Output Characteristics

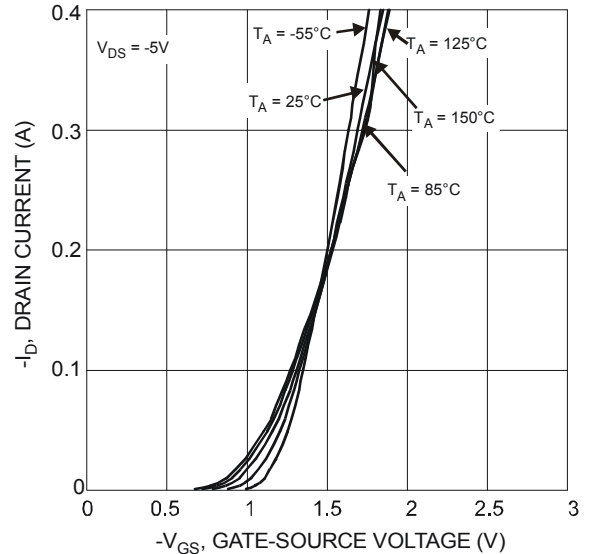


Figure 2 Typical Transfer Characteristics

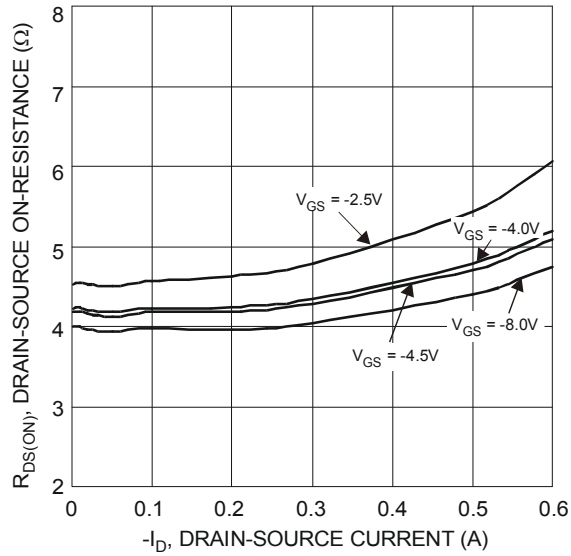


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

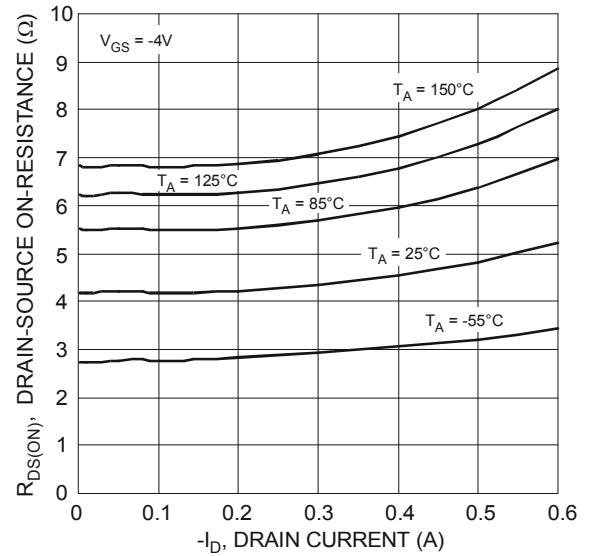


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

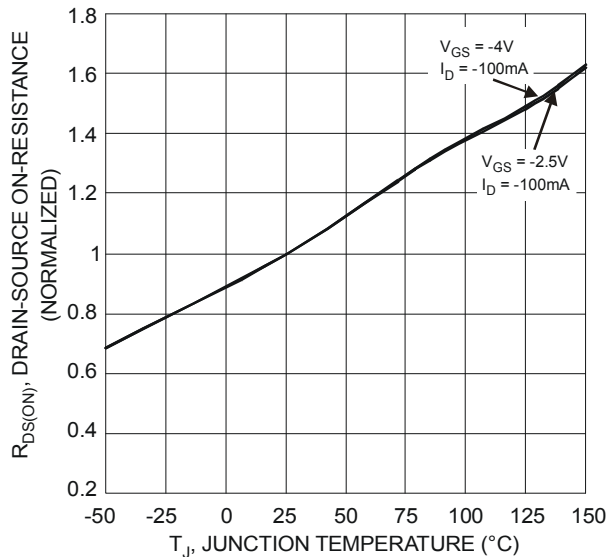


Figure 5 On-Resistance Variation with Temperature

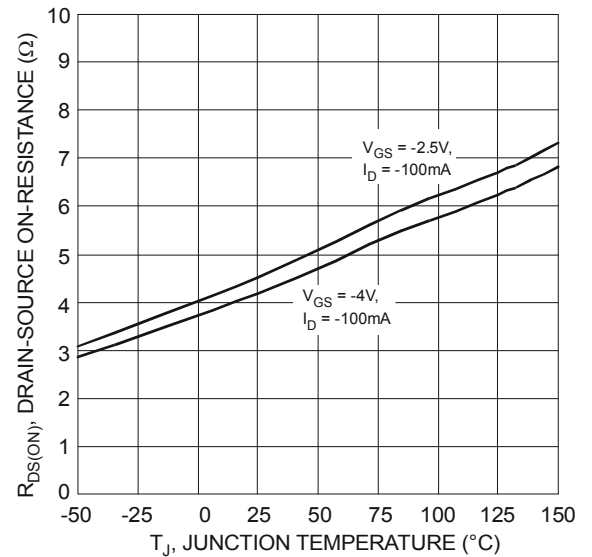


Figure 6 On-Resistance Variation with Temperature

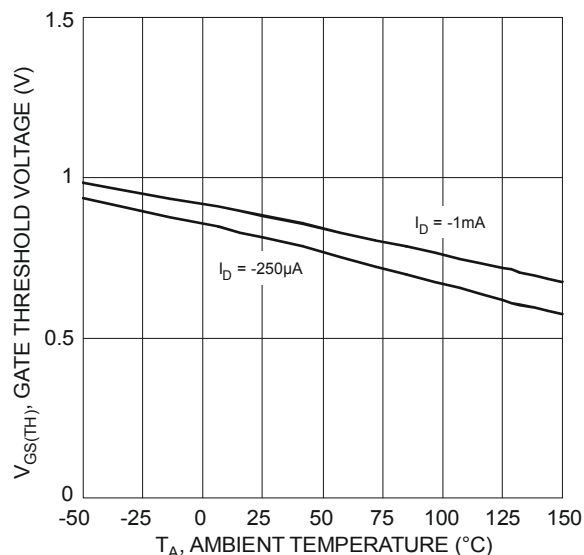


Figure 7 Gate Threshold Variation vs. Ambient Temperature

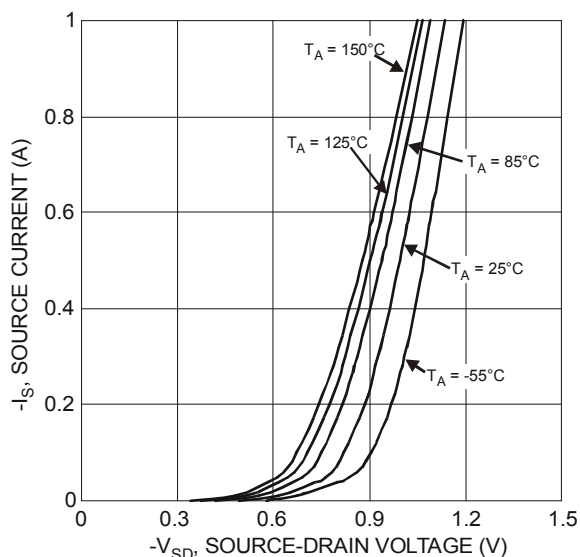


Figure 8 Diode Forward Voltage vs. Current

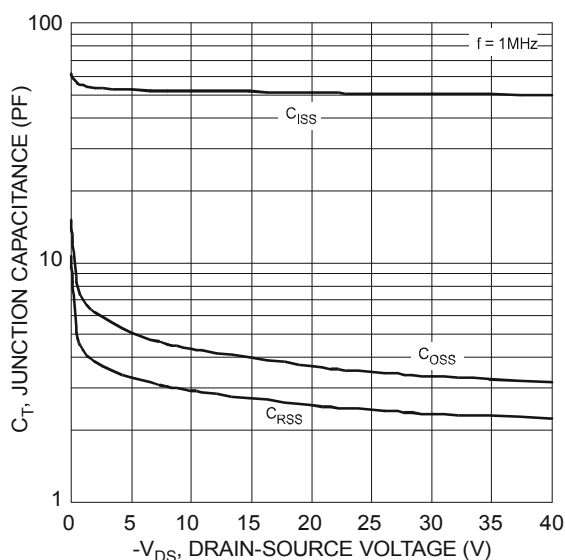


Figure 9 Typical Junction Capacitance

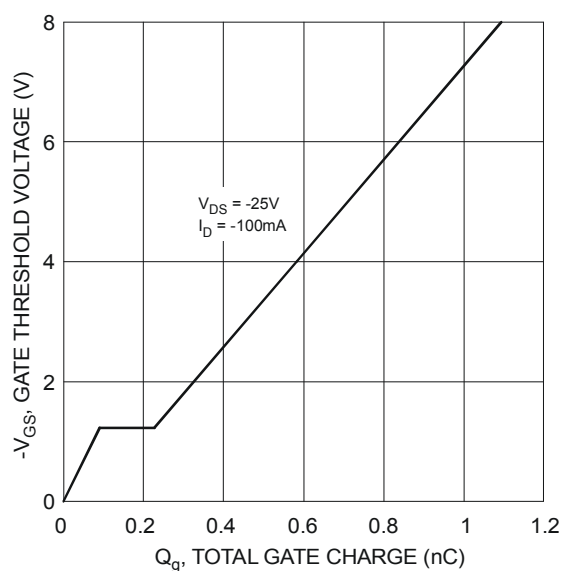
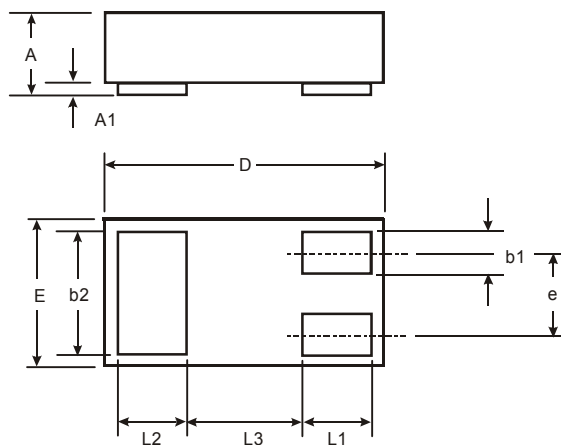


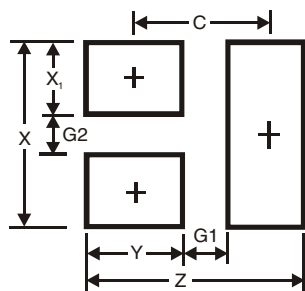
Figure 10 Gate Charge Characteristics

Package Outline Dimensions



X1-DFN1006-3			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0	0.05	0.03
b1	0.10	0.20	0.15
b2	0.45	0.55	0.50
D	0.95	1.075	1.00
E	0.55	0.675	0.60
e	—	—	0.35
L1	0.20	0.30	0.25
L2	0.20	0.30	0.25
L3	—	—	0.40
All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
Z	1.1
G1	0.3
G2	0.2
X	0.7
X1	0.25
Y	0.4
C	0.7

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