



# HEF4543B

BCD to 7-segment latch/decoder/driver

Rev. 9 — 15 August 2024

Product data sheet

## 1. General description

The HEF4543B is a BCD to 7-segment latch/decoder/driver for liquid crystal and LED displays. It has four address inputs (D0 to D3), an active LOW latch enable input (LE), an active HIGH blanking input (BL), an active HIGH phase input (PH) and seven buffered segment outputs (Qa to Qg).

The circuit provides the function of a 4-bit storage latch and an 8-4-2-1 BCD to 7-segment decoder/driver. It can invert the logic levels of the output combination. The phase (PH), blanking (BL) and latch enable (LE) inputs are used to reverse the function table phase, blank the display and store a BCD code, respectively.

For liquid crystal displays, a square-wave is applied to PH and the electrical common back-plane of the display. The outputs of the device are directly connected to the segments of the liquid crystal.

It operates over a recommended  $V_{DD}$  power supply range of 3 V to 15 V referenced to  $V_{SS}$  (usually ground). Unused inputs must be connected to  $V_{DD}$ ,  $V_{SS}$ , or another input.

## 2. Features and benefits

- Wide supply voltage range from 3.0 V to 15.0 V
- CMOS low power dissipation
- High noise immunity
- Fully static operation
- 5 V, 10 V, and 15 V parametric ratings
- Standardized symmetrical output characteristics
- Complies with JEDEC standard JESD 13-B
- ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Specified from -40 °C to +85 °C

## 3. Ordering information

Table 1. Ordering information

Type number	Package				Version
	Temperature range	Name	Description		
HEF4543BT	-40 °C to +85 °C	SO16	plastic small outline package; 16 leads; body width 3.9 mm		<a href="#">SOT109-1</a>

## 4. Functional diagram

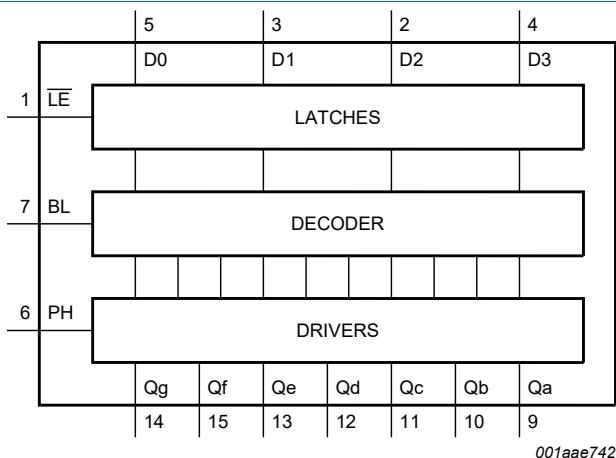


Fig. 1. Functional diagram

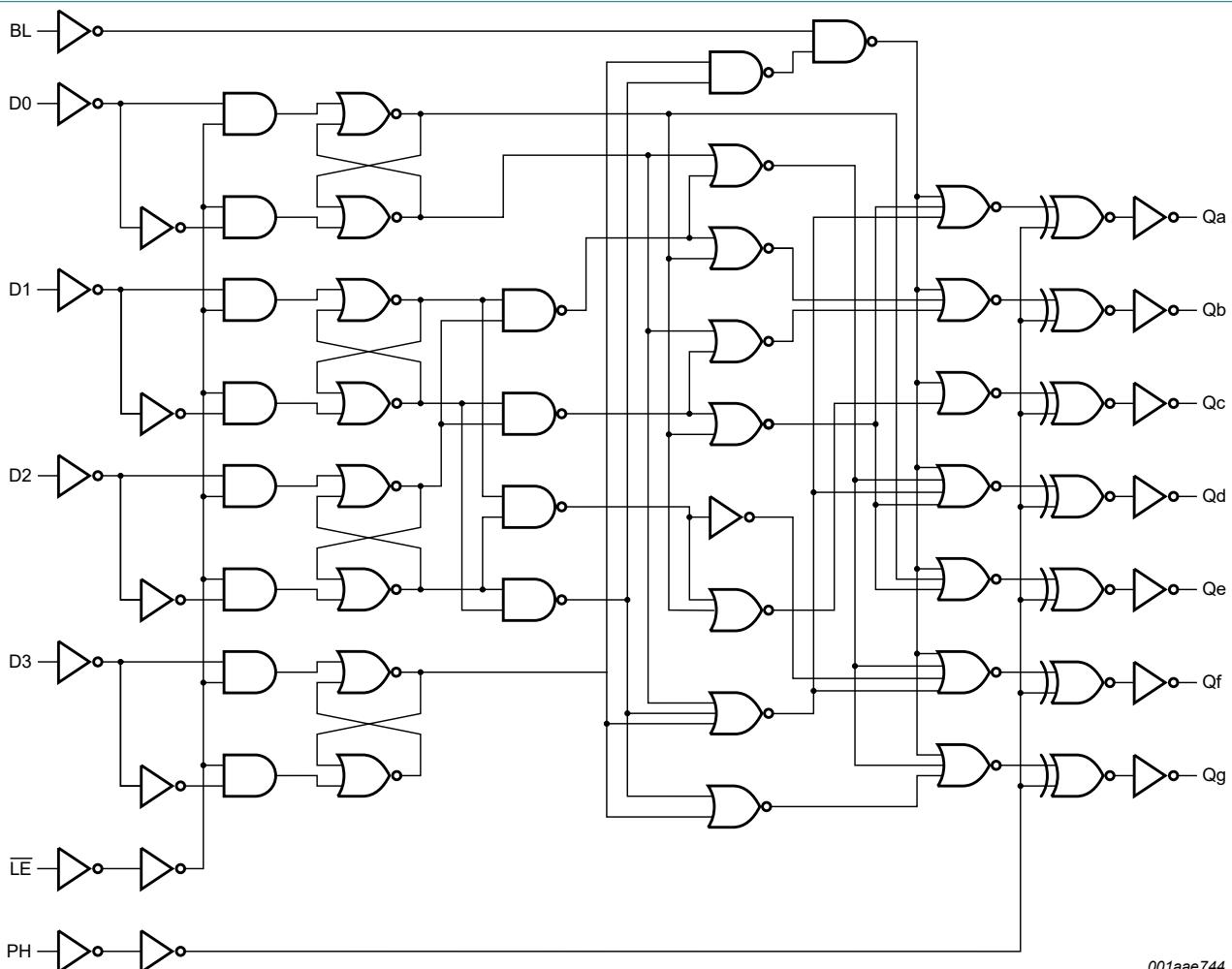
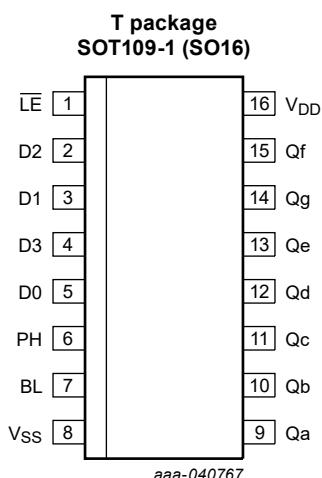


Fig. 2. Logic diagram

## 5. Pinning information

### 5.1. Pinning



### 5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
LE	1	latch enable input (active LOW)
D0, D1, D2, D3	5, 3, 2, 4	address (data) input
PH	6	phase input (active HIGH)
BL	7	blanking input (active HIGH)
Vss	8	ground supply voltage
Qa, Qb, Qc, Qd, Qe, Qf, Qg	9, 10, 11, 12, 13, 14, 15, 16	segment output
Vdd	16	supply voltage

## 6. Functional description

**Table 3. Function table**

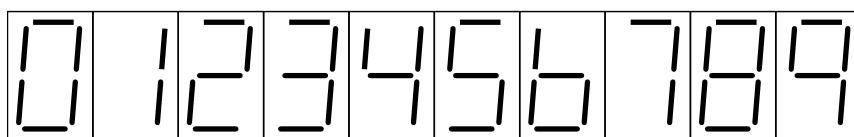
*H = HIGH voltage level; L = LOW voltage level; X = don't care; n.c. = no change.*

Inputs							Outputs							Display
LE	BL	PH[1]	D3	D2	D1	D0	Qa	Qb	Qc	Qd	Qe	Qf	Qg	
X	H	L	X	X	X	X	L	L	L	L	L	L	L	blank
H	L	L	L	L	L	L	H	H	H	H	H	H	L	0
H	L	L	L	L	L	H	L	H	H	L	L	L	L	1
H	L	L	L	L	H	L	H	H	L	H	H	L	H	2
H	L	L	L	L	H	H	H	H	H	H	L	L	H	3
H	L	L	L	H	L	L	H	H	H	L	L	H	H	4
H	L	L	L	H	L	H	H	L	H	H	L	H	H	5
H	L	L	L	H	H	L	H	L	H	H	H	H	H	6
H	L	L	L	H	H	H	H	H	H	L	L	L	L	7
H	L	L	H	L	L	H	H	H	H	H	H	H	H	8
H	L	L	H	L	L	H	H	H	H	H	L	H	H	9
H	L	L	H	L	H	X	L	L	L	L	L	L	L	blank
H	L	L	H	H	X	X	L	L	L	L	L	L	L	blank
L	L	L	X	X	X	X	n.c.							n.c.
as above		H	as above				inverse of above							as above

[1] For liquid crystal displays, apply a square-wave to PH;

For common cathode LED displays, select PH = LOW;

For common anode LED displays, select PH = HIGH.



**Fig. 3. Seven segment digital display with segment designation**

## 7. Limiting values

**Table 4. Limiting values**

*In accordance with the Absolute Maximum Rating System (IEC 60134).*

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DD}$	supply voltage		-0.5	+18	V
$V_I$	input voltage		-0.5	$V_{DD} + 0.5$	V
$I_{I/O}$	input/output current		-	$\pm 10$	mA
$T_{stg}$	storage temperature		-65	+150	°C
$T_{amb}$	ambient temperature		-40	+85	°C
$P_{tot}$	total power dissipation		-	500	mW
$P$	power dissipation	per output	-	100	mW

## 8. Recommended operating conditions

**Table 5. Recommended operating conditions**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{DD}$	supply voltage		3	-	15	V
$V_I$	input voltage		0	-	$V_{DD}$	V
$T_{amb}$	ambient temperature	in free air	-40	-	+85	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{DD} = 5\text{ V}$	-	-	3.75	μs/V
		$V_{DD} = 10\text{ V}$	-	-	0.5	μs/V
		$V_{DD} = 15\text{ V}$	-	-	0.08	μs/V

## 9. Static characteristics

**Table 6. Static characteristics**

$V_{SS} = 0\text{ V}$ ;  $V_I = V_{SS}$  or  $V_{DD}$  unless otherwise specified.

Symbol	Parameter	Conditions	$V_{DD}$	$T_{amb} = -40\text{ °C}$		$T_{amb} = 25\text{ °C}$		$T_{amb} = 85\text{ °C}$		Unit
				Min	Max	Min	Max	Min	Max	
$V_{IH}$	HIGH-level input voltage	$ I_{ol}  < 1\text{ μA}$	5 V	3.5	-	3.5	-	3.5	-	V
			10 V	7.0	-	7.0	-	7.0	-	V
			15 V	11.0	-	11.0	-	11.0	-	V
$V_{IL}$	LOW-level input voltage	$ I_{ol}  < 1\text{ μA}$	5 V	-	1.5	-	1.5	-	1.5	V
			10 V	-	3.0	-	3.0	-	3.0	V
			15 V	-	4.0	-	4.0	-	4.0	V
$V_{OH}$	HIGH-level output voltage	$ I_{ol}  < 1\text{ μA}$	5 V	4.95	-	4.95	-	4.95	-	V
			10 V	9.95	-	9.95	-	9.95	-	V
			15 V	14.95	-	14.95	-	14.95	-	V
$V_{OL}$	LOW-level output voltage	$ I_{ol}  < 1\text{ μA}$	5 V	-	0.05	-	0.05	-	0.05	V
			10 V	-	0.05	-	0.05	-	0.05	V
			15 V	-	0.05	-	0.05	-	0.05	V
$I_{OH}$	HIGH-level output current	$V_O = 2.5\text{ V}$	5 V	-	-1.7	-	-1.4	-	-1.1	mA
		$V_O = 4.6\text{ V}$	5 V	-	-0.52	-	-0.44	-	-0.36	mA
		$V_O = 9.5\text{ V}$	10 V	-	-1.3	-	-1.1	-	-0.9	mA
		$V_O = 13.5\text{ V}$	15 V	-	-3.6	-	-3.0	-	-2.4	mA
$I_{OL}$	LOW-level output current	$V_O = 0.4\text{ V}$	5 V	0.52	-	0.44	-	0.36	-	mA
		$V_O = 0.5\text{ V}$	10 V	1.3	-	1.1	-	0.9	-	mA
		$V_O = 1.5\text{ V}$	15 V	3.6	-	3.0	-	2.4	-	mA
$I_I$	input leakage current		15 V	-	$\pm 0.3$	-	$\pm 0.3$	-	$\pm 1.0$	μA
$I_{DD}$	supply current	$I_O = 0\text{ A}$	5 V	-	20	-	20	-	150	μA
			10 V	-	40	-	40	-	300	μA
			15 V	-	80	-	80	-	600	μA
$C_I$	input capacitance		-	-	-	-	7.5	-	-	pF

## 10. Dynamic characteristics

**Table 7. Dynamic characteristics**

$V_{SS} = 0 \text{ V}$ ;  $T_{amb} = 25 \text{ }^{\circ}\text{C}$  unless otherwise specified; For test circuit see Fig. 6.

Symbol	Parameter	Conditions	$V_{DD}$	Extrapolation formula[1]	Min	Typ	Max	Unit
$t_{PHL}$	HIGH to LOW propagation delay	Dn to Qn; see Fig. 4	5 V	$153 \text{ ns} + (0.55 \text{ ns/pF})C_L$	-	180	360	ns
			10 V	$64 \text{ ns} + (0.23 \text{ ns/pF})C_L$	-	75	150	ns
			15 V	$47 \text{ ns} + (0.16 \text{ ns/pF})C_L$	-	55	110	ns
		$\overline{LE}$ to Qn; see Fig. 4	5 V	$143 \text{ ns} + (0.55 \text{ ns/pF})C_L$	-	170	340	ns
			10 V	$69 \text{ ns} + (0.23 \text{ ns/pF})C_L$	-	80	160	ns
			15 V	$52 \text{ ns} + (0.16 \text{ ns/pF})C_L$	-	60	120	ns
		BL to Qn; see Fig. 4	5 V	$118 \text{ ns} + (0.55 \text{ ns/pF})C_L$	-	145	290	ns
			10 V	$54 \text{ ns} + (0.23 \text{ ns/pF})C_L$	-	65	130	ns
			15 V	$37 \text{ ns} + (0.16 \text{ ns/pF})C_L$	-	45	90	ns
$t_{PLH}$	LOW to HIGH propagation delay	Dn to Qn; see Fig. 4	5 V	$153 \text{ ns} + (0.55 \text{ ns/pF})C_L$	-	180	360	ns
			10 V	$64 \text{ ns} + (0.23 \text{ ns/pF})C_L$	-	75	150	ns
			15 V	$47 \text{ ns} + (0.16 \text{ ns/pF})C_L$	-	55	110	ns
		$\overline{LE}$ to Qn; see Fig. 4	5 V	$163 \text{ ns} + (0.55 \text{ ns/pF})C_L$	-	190	380	ns
			10 V	$69 \text{ ns} + (0.23 \text{ ns/pF})C_L$	-	80	160	ns
			15 V	$52 \text{ ns} + (0.16 \text{ ns/pF})C_L$	-	60	120	ns
		BL to Qn; see Fig. 4	5 V	$98 \text{ ns} + (0.55 \text{ ns/pF})C_L$	-	125	250	ns
			10 V	$54 \text{ ns} + (0.23 \text{ ns/pF})C_L$	-	55	110	ns
			15 V	$32 \text{ ns} + (0.16 \text{ ns/pF})C_L$	-	40	80	ns
$t_t$	transition time	pin Qn; see Fig. 4	5 V	$10 \text{ ns} + (1.00 \text{ ns/pF})C_L$	-	60	120	ns
			10 V	$9 \text{ ns} + (0.42 \text{ ns/pF})C_L$	-	30	60	ns
			15 V	$6 \text{ ns} + (0.28 \text{ ns/pF})C_L$	-	20	40	ns
$t_{su}$	set-up time	Dn to $\overline{LE}$ ; see Fig. 5	5 V		40	20	-	ns
			10 V		20	5	-	ns
			15 V		15	0	-	ns
$t_h$	hold time	Dn to $\overline{LE}$ ; see Fig. 5	5 V		0	-15	-	ns
			10 V		15	0	-	ns
			15 V		20	5	-	ns
$t_w$	pulse width	pin $\overline{LE}$ HIGH; minimum width; see Fig. 5	5 V		60	30	-	ns
			10 V		30	15	-	ns
			15 V		20	10	-	ns

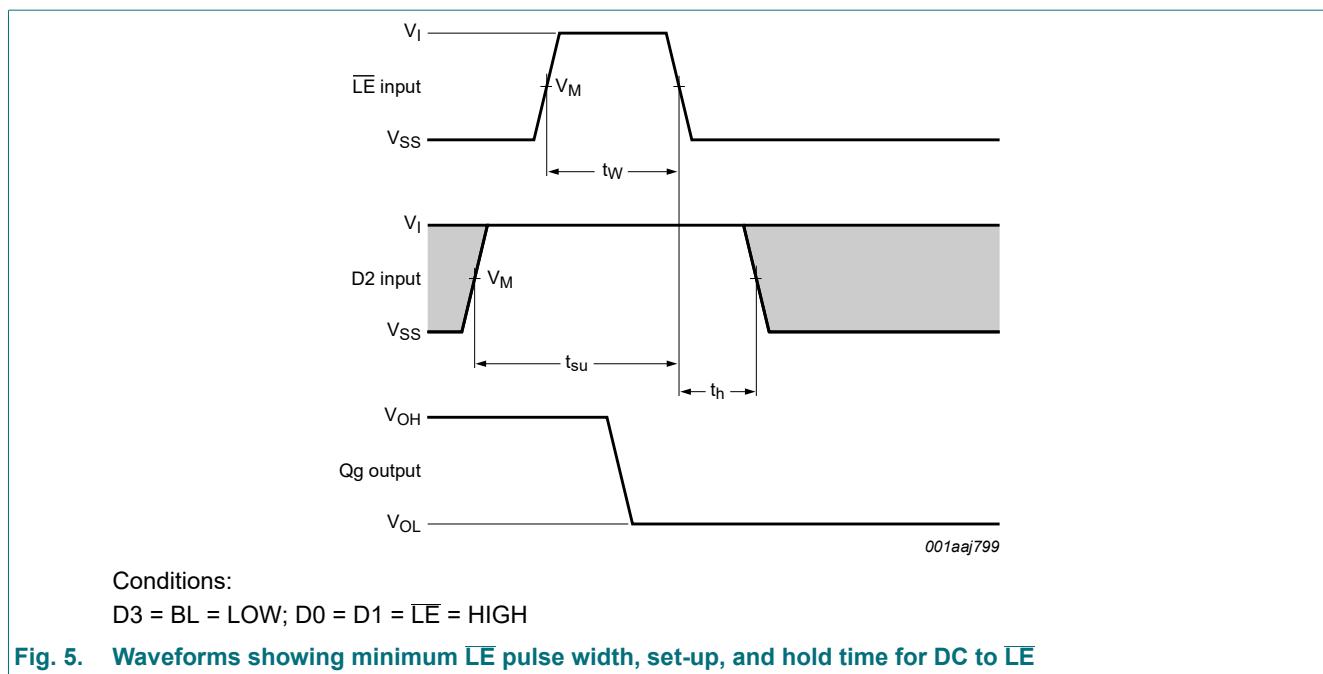
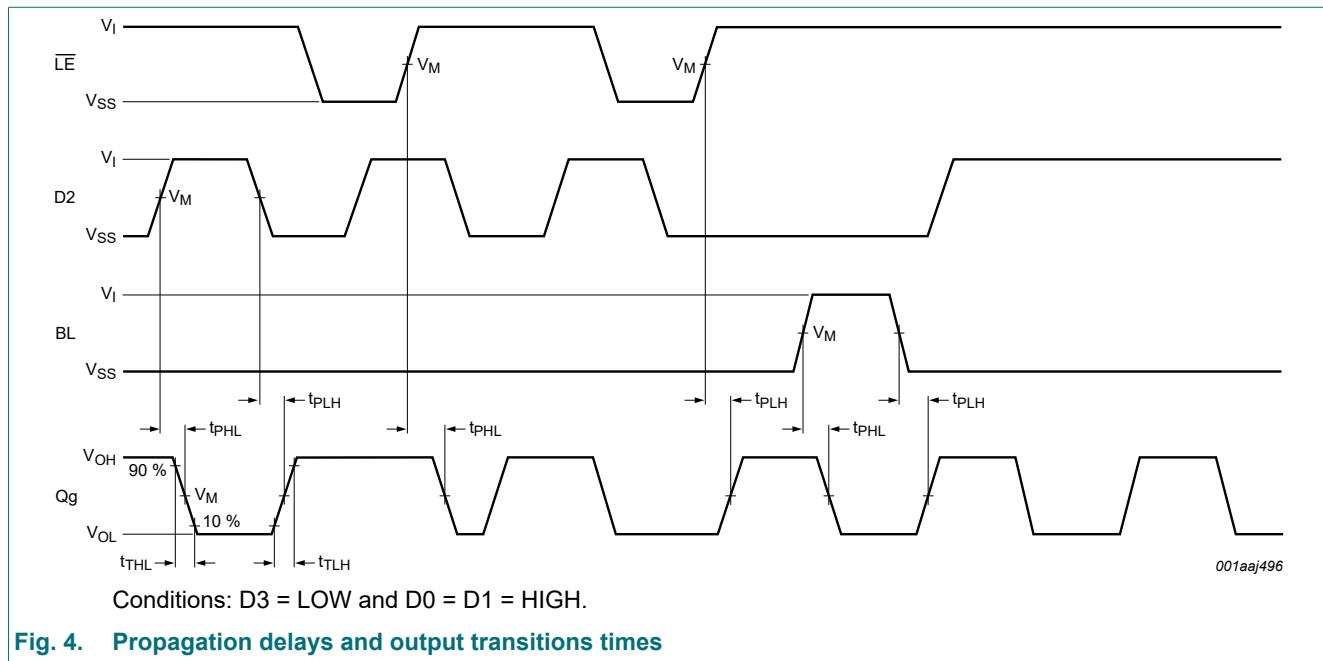
[1] The typical values of the propagation delay and transition times are calculated from the extrapolation formulas shown ( $C_L$  in pF).

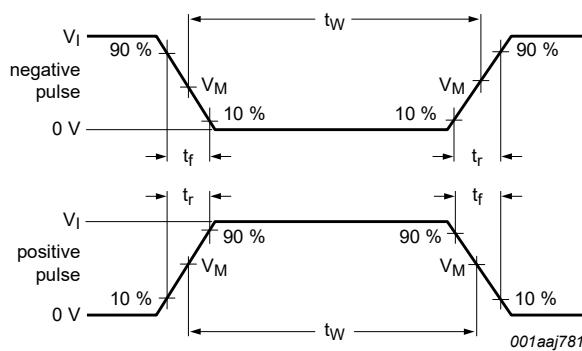
**Table 8. Dynamic power dissipation  $P_D$**

$P_D$  can be calculated from the formulas shown.  $V_{SS} = 0 \text{ V}$ ;  $t_t = t_f \leq 20 \text{ ns}$ ;  $T_{amb} = 25 \text{ }^{\circ}\text{C}$ .

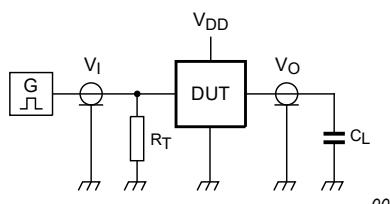
Symbol	Parameter	$V_{DD}$	Typical formula for $P_D$ ( $\mu\text{W}$ )	where:
$P_D$	dynamic power dissipation	5 V	$P_D = 2200 \times f_i + \sum(f_o \times C_L) \times V_{DD}^2$	$f_i$ = input frequency in MHz; $f_o$ = output frequency in MHz; $C_L$ = output load capacitance in pF; $V_{DD}$ = supply voltage in V; $\sum(C_L \times f_o)$ = sum of the outputs.
		10 V	$P_D = 10400 \times f_i + \sum(f_o \times C_L) \times V_{DD}^2$	
		15 V	$P_D = 33000 \times f_i + \sum(f_o \times C_L) \times V_{DD}^2$	

### 10.1. Waveforms and test circuit





a. Input waveforms



b. Test circuit

Test data is given in [Table 9](#).

Definitions for test circuit:

$R_L$  = Load resistance;

$C_L$  = Load capacitance including jig and probe capacitance;

$R_T$  = Termination resistance should be equal to output impedance  $Z_o$  of the pulse generator.

Fig. 6. Test circuit for measuring switching times

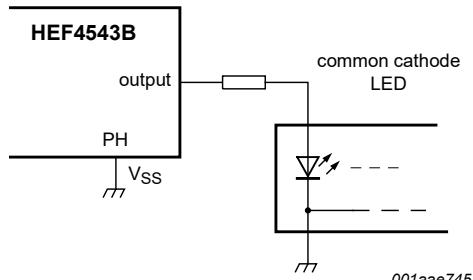
Table 9. Test data

Supply voltage	Input			Load
$V_{DD}$	$V_I$	$V_M$	$t_r, t_f$	$C_L$
5 V to 15 V	$V_{DD}$	$0.5V_I$	$\leq 20$ ns	50 pF

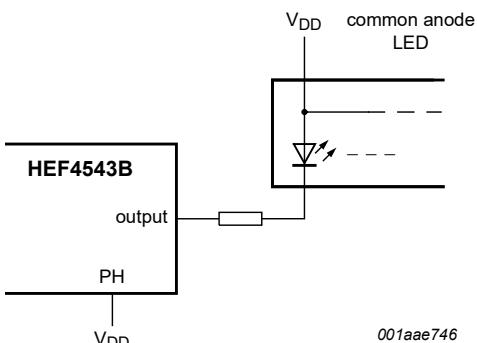
## 11. Application information

Some examples of applications for the HEF4543B are:

- Driving LCD displays
- Driving LED displays
- Driving fluorescent displays
- Driving incandescent displays
- Driving gas discharge displays



a. Common cathode



b. Common anode

Bipolar transistors may be added for gain where  $V_{DD} \leq 10$  V or  $I_O \geq 10$  mA.

Fig. 7. Connection to LED display readout

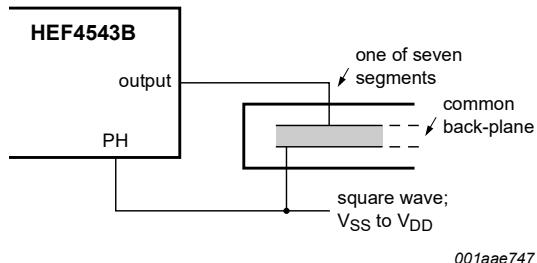


Fig. 8. Connection to LCD readout

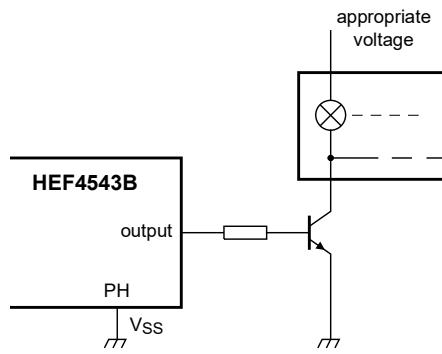


Fig. 9. Connection to incandescent display readout

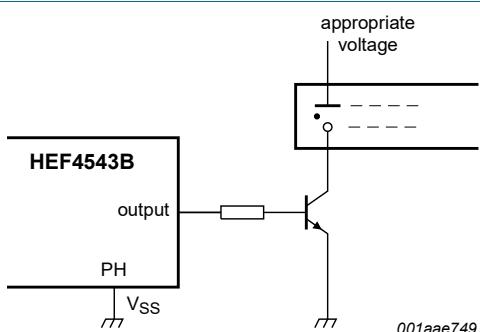


Fig. 10. Connection to gas discharge display readout

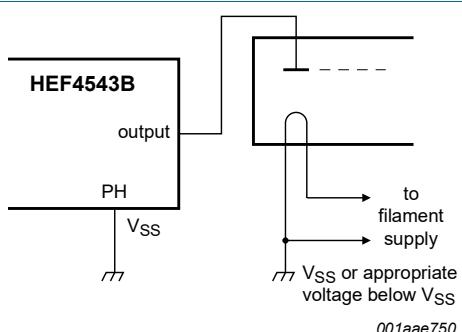
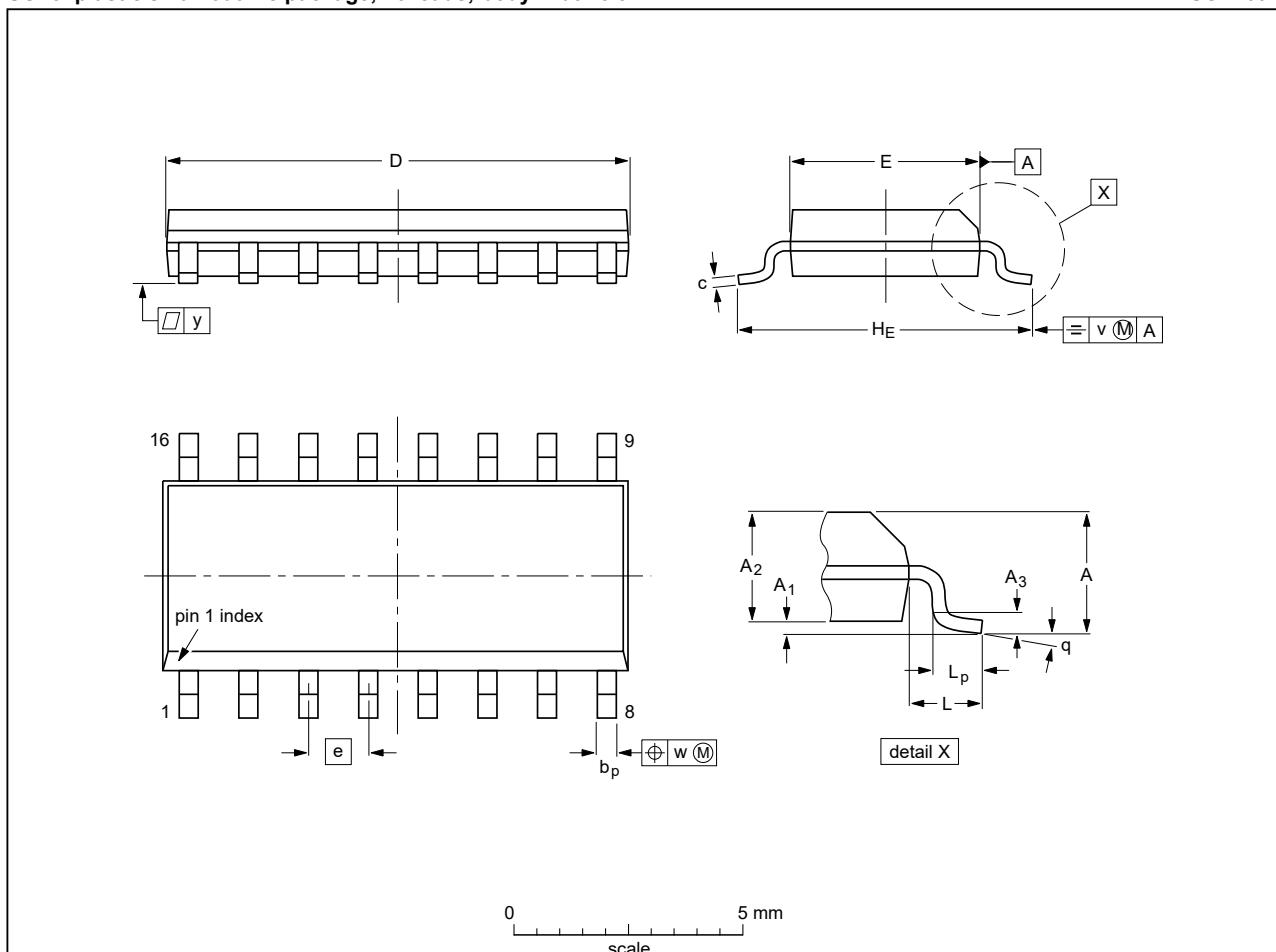


Fig. 11. Connection to fluorescent display readout

## 12. Package outline

SO16: plastic small outline package; 16 leads; body width 3.9 mm

SOT109-1



Dimensions (inch dimensions are derived from the original mm dimensions)

Unit	A	A <sub>1</sub>	A <sub>2</sub>	A <sub>3</sub>	b <sub>p</sub>	c	D <sup>(1)</sup>	E <sup>(1)</sup>	e	H <sub>E</sub>	L	L <sub>p</sub>	v	w	y	θ	
mm	max 1.75	0.25			0.51	0.25	10.0	4.0		6.2		1.27	0.2	0.25	0.1	8°	
mm	nom			0.25					1.27		1.05					0°	
mm	min 0.10	1.25		0.31	0.10	9.8	3.8		5.8		0.4						
inches	max 0.069	0.010			0.020	0.010	0.394	0.16		0.244		0.05		0.008	0.01	0.004	8°
inches	nom			0.01					0.05		0.041						0°
inches	min 0.004	0.049		0.012	0.004	0.386	0.15		0.228		0.016						

Note

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

sot109-1\_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT109-1		MS-012				03-02-19 23-10-27

Fig. 12. Package outline SOT109-1 (SO16)

## 13. Abbreviations

**Table 10. Abbreviations**

Acronym	Description
ANSI	American National Standards Institute
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
ESDA	ElectroStatic Discharge Association
HBM	Human Body Model
JEDEC	Joint Electron Device Engineering Council
LCD	Liquid Crystal Display
LED	Light Emitting Diode

## 14. Revision history

**Table 11. Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes
HEF4543B v.9	20240815	Product data sheet	-	HEF4543B v.8
Modifications:			<ul style="list-style-type: none"> <li>• <a href="#">Section 2</a>: ESD specification updated according to the latest JEDEC standard.</li> <li>• <a href="#">Fig. 12</a>: Aligned SO package outline drawing to JEDEC MS-012</li> </ul>	
HEF4543B v.8	20211124	Product data sheet	-	HEF4543B v.7
Modifications:			<ul style="list-style-type: none"> <li>• The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>• Legal texts have been adapted to the new company name where appropriate.</li> <li>• <a href="#">Section 2</a> updated.</li> <li>• <a href="#">Table 6</a>: <math>V_{OH}</math> condition added (errata).</li> </ul>	
HEF4543B v.7	20160401	Product data sheet	-	HEF4543B v.6
Modifications:			<ul style="list-style-type: none"> <li>• Type number HEF4543BP (SOT38-4) removed.</li> </ul>	
HEF4543B v.6	20111117	Product data sheet	-	HEF4543B v.5
Modifications:			<ul style="list-style-type: none"> <li>• Section Applications removed</li> <li>• <a href="#">Table 6</a>: <math>I_{OH}</math> minimum values changed to maximum</li> <li>• <a href="#">Fig. 5</a>: signal <math>\overline{LT}</math> removed; signal <math>\overline{BL}</math> replaced by <math>BL</math> (inverted)</li> </ul>	
HEF4543B v.5	20091027	Product data sheet	-	HEF4543B v.4
HEF4543B v.4	20090317	Product data sheet	-	HEF4543B_CNV v.3
HEF4543B_CNV v.3	19950101	Product specification	-	HEF4543B_CNV v.2
HEF4543B_CNV v.2	19950101	Product specification	-	-

## 15. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

### Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

**Short data sheet** — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

### Disclaimers

**Limited warranty and liability** — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

**Right to make changes** — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use** — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal

injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

**Limiting values** — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

**Terms and conditions of commercial sale** — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nexperia.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

**Non-automotive qualified products** — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

### Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

## Contents

<b>1. General description.....</b>	<b>1</b>
<b>2. Features and benefits.....</b>	<b>1</b>
<b>3. Ordering information.....</b>	<b>1</b>
<b>4. Functional diagram.....</b>	<b>2</b>
<b>5. Pinning information.....</b>	<b>3</b>
5.1. Pinning.....	3
5.2. Pin description.....	3
<b>6. Functional description.....</b>	<b>4</b>
<b>7. Limiting values.....</b>	<b>4</b>
<b>8. Recommended operating conditions.....</b>	<b>5</b>
<b>9. Static characteristics.....</b>	<b>5</b>
<b>10. Dynamic characteristics.....</b>	<b>6</b>
10.1. Waveforms and test circuit.....	7
<b>11. Application information.....</b>	<b>9</b>
<b>12. Package outline.....</b>	<b>10</b>
<b>13. Abbreviations.....</b>	<b>11</b>
<b>14. Revision history.....</b>	<b>11</b>
<b>15. Legal information.....</b>	<b>12</b>

---

© Nexperia B.V. 2024. All rights reserved

For more information, please visit: <http://www.nexperia.com>

For sales office addresses, please send an email to: [salesaddresses@nexperia.com](mailto:salesaddresses@nexperia.com)

Date of release: 15 August 2024