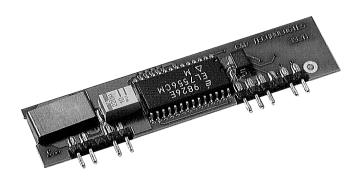


**Product Data Sheet** 

# 5 VDC INPUT, 3.3 VDC OUTPUT DC/DC CONVERTER

# SS26 (SuperSIP<sup>TM</sup>)



#### **Description**

The SS26 (SuperSIP™) DC/DC converter accepts a regulated 5V input (±10%) and provides 1.8Vdc to 3.6Vdc at 6A. The circuit is optimized for high efficiency and fast load transient response needed by telecom, DSP, and microprocessor applications. Advanced thermal design, monolithic power circuitry, planar magnetics, and synchronous rectification result in outstanding performance and value. Pins are staked for wave solderability. Multiple programming, power good and on/ off options allow superior flexibility and drop in compatibility for most existing designs.

#### **Features**

- Non isolated DC/DC Converter designed to operate from a regulated 5V bus
- Output voltage Range: 1.8V 3.6V
- Easy resistive programming for desired output
- No resistive programming gives 3.3 Vdc output
- Wave solderable

More product information and application notes are available on our website at www.cdpowerelectronics.com

Internet: http://www.cdpowerelectronics.com

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#### **Electrical Specifications**

Unless otherwise specified, operating conditions are as follows:  $V_{in}=5V$ ,  $V_{o}=3.3V$ ,  $I_{o}=6A$ ,  $T_{A}=25^{\circ}C$ ,  $C_{in}=100\mu F$ ,  $C_{o}=0F$ .

Input Input Voltage Input Current Ripple Required Capacitance  Output Output Voltage Output Program Range Output Current Output Ripple Output Rise time Cutput Capacitance Range Co Line Regulation Temperature Coefficient Combined variation  Vin  A.5  5.0  200  Aoute 1  O 100  Nominal 3.25 3.3  Note 2 1.8  TA=25°C 0  20 Mhz BW 15  700  0  Load Regulation Temperature Coefficient To Combined variation  Vin min-max TA=25C°-85C° -2	3.35	V <sub>DC</sub> mA <sub>RMS</sub> μF
Input Current Ripple Required Capacitance  Cin  Note 1  Output  Output  Output Voltage  Output Program Range  Output Current  Output Current  Output Ripple  Output Rise time  T  Output Capacitance Range  Co  Line Regulation  Load Regulation  Temperature Coefficient  Combined variation  Input Current  Note 1  Nominal  3.25  Nominal  3.25  3.3  Note 2  1.8  Note 2  1.8  Output B  TA=25°C  0  20 Mhz BW  15  700  0  Line Regulation  ±0.5  ±1.0  Vin min-max  Io min-max	3.35	m <b>A</b> RMS
Required Capacitance Cin Note 1 0 100  Output Output Voltage Vo Nominal 3.25 3.3  Output Program Range Note 2 1.8  Output Current Io TA=25°C 0  Output Ripple 20 Mhz BW 15  Output Rise time T 700  Output Capacitance Range Co Line Regulation ±0.5  Load Regulation Io min-Io max 10.01  Combined variation Vin min-max Io min-max	1	
Output Voltage Vo Nominal 3.25 3.3  Output Program Range Note 2 1.8  Output Current Io TA=25°C 0  Output Ripple 20 Mhz BW 15  Output Rise time T 700  Output Capacitance Range Co Line Regulation Load Regulation Iomerature Coefficient To Combined variation Vin min-max Iomin-max	1	μF
Output Voltage Vo Nominal 3.25 3.3  Output Program Range Note 2 1.8  Output Current Io TA=25°C 0  Output Rispele 20 Mhz BW 15  Output Rise time TO Output Capacitance Range Co Line Regulation Load Regulation Temperature Coefficient To Combined variation Vin min-max  Io min-max Io min-max	1	
Output Program Range Output Current Output Ripple Output Rise time Output Rise time Output Capacitance Range Load Regulation Temperature Coefficient Combined variation  Output Program Range Io Note 2  1.8  Ta=25°C 0  20 Mhz BW 15  700  0  to be in the combined in the co	1	
Output Current Io TA=25°C 0 Output Ripple 20 Mhz BW 15 Output Rise time T 700 Output Capacitance Range Co Line Regulation ±0.5 Load Regulation Io min-Io max ±1.0 Combined variation Vin min-max Io min-max		V <sub>DC</sub>
Output Ripple Output Rise time T Output Capacitance Range Co Line Regulation Load Regulation Temperature Coefficient Combined variation  Output Ripple 20 Mhz BW 15 700 0 ±0.5 Load Regulation Lo min-lo max ±1.0 0.01 Vin min-max Lo min-max	3.6	V <sub>DC</sub>
Output Rise time T Output Capacitance Range Co Line Regulation Load Regulation Temperature Coefficient Combined variation  Output Rise time T To Output Capacitance Range T O  Io min-Io max  ±1.0  O.01  Vin min-max Io min-max	6	Amps
Output Capacitance Range Co Line Regulation Load Regulation Temperature Coefficient Combined variation  O ±0.5  ±1.0  0 0  ±0.5  Vin min-lo max  0 0  0  0  10  10  10  10  10  10  10	50	mVp-p
Line Regulation ±0.5  Load Regulation Io min-Io max ±1.0  Temperature Coefficient To 0.01  Combined variation Vin min-max Io min-max		μS
Load Regulation Temperature Coefficient Combined variation  Lo min-Io max  ±1.0  0.01  V <sub>in</sub> min-max  Io min-max	5000	μF
Temperature Coefficient To 0.01 Combined variation Vin min-max Io min-max		%
Combined variation  V <sub>in</sub> min-max  I <sub>o</sub> min-max		%
l₀ min-max		%/°C
T <sub>A</sub> =25C°-85C°		
=== =	+2	%
Current Limit   V <sub>in</sub> = 4.75Vdc   6.5   9	12	A
General		
Switching Frequency 800		kHz
Dynamic Response		
$\Delta I_0/\Delta t = 1A/10\mu \text{ sec}, V_1 = 5.0V, T_A = 25^{\circ}C$		
Load Change from Io = 0% to Io = 100%		
Peak Deviation 30 Settling time (Vo<10% Peak Deviation) 100		mV
Load change from Io = 100% to Io = 0%		μsec
Peak Deviation 30		mV
Settling time (Vo<10% Peak Deviation)		μsec
Temperature		
Operating Temperature Note 3 0		°C
Storage Temperature -40	+60	
	+60 +125	°C

#### Notes

- 1. Input source<3" from SuperSIP™, Load transient <3A per SIP. 100μF low ESR capacitor for load transients >3A.
- 2. Optional programming 1.8 3.6 or  $\pm 10\%$  available. See Table.
- 3. 100 lfm air,  $V_0$ =3.3 $V_1$ ,  $I_0$ =6A. See Thermal Design Guide for other conditions.

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#### **Programming**

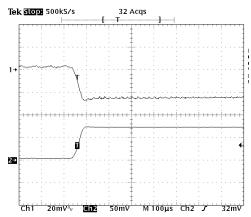
To program the SS26 SuperSIP<sup>TM</sup> for  $V_{out}$ <3.3, connect resistor across pins 8 (TRIM) and 1( $V_o$ ). For  $V_{out}$ >3.3, resistor is connected across pins 8 and 4 (Gnd).

Table 2

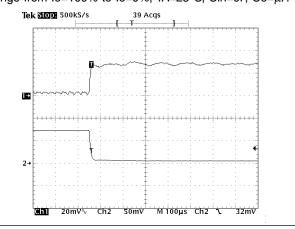
Vout	Resistor Value	Vout	Resistor Value
1.8	576Ω	2.8	18.2k
1.9	1.21k	2.9	24.3k
2.0	1.96k	3.0	34.8k
2.1	2.8k	3.1	54.9k
2.2	3.83k	3.2	110.0k
2.3	4.99k	3.3	OPEN
2.4	6.49k	3.4	66.5k
2.5	8.25k	3.5	29.4k
2.6	10.7k	3.6	18.2k
2.7	13.7k		

### **Transient Response**

Operating conditions are as follows: Vin=5V, Vo=3.3V, Load change from Io=0% to Io=100%, TA=25°C, Cin=0F, Co= $\mu$ F.



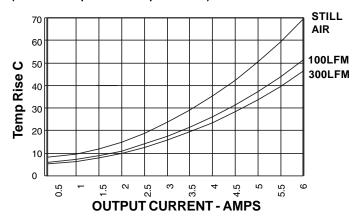
Operating conditions are as follows: Vin=5V, Vo=3.3V, Load change from Io=100% to Io=0%, TA=25°C, Cin=0F, Co=μF.



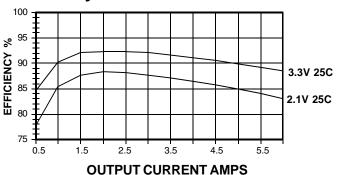
#### **Thermal Design Guide**

Locate your operating current, read the junction temp rise from the graph and add to your maximum ambient. 135°C is the maximum allowable operating junction temperature. Test conditions: Device soldered into 4" x 4" PCB, 2 sided with power and ground planes for heat conduction. Due to the difficulty in predicting the thermal effects of airflow velocity and direction, and thermal conduction through ground planes it is important that the SS26 SuperSIP™ be evaluated thermally in each application. For high ambient temperature/high current application please request our Application Note 35-118-01, "Accurate Measurements of SS26 SuperSIP™ Junction Temperature", for further assistance.

T<sub>j</sub> Rise vs. I<sub>o</sub>
(Junction Temp Rise vs. Output Current)



#### **Efficiency**

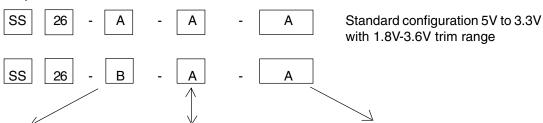


Vo=3.3V, Cout-Oμf, Vi=5V, Cin=100μf

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## **Ordering Information**

Typical examples:



Power Good	Enable	Programming (See Table 2)
<ul><li>A = Pin 7A installed for Power Good option</li><li>B = Pin omitted (industry standard)</li></ul>	A = logic1 or open = ON logic 0 or gnd = OFF B = logic 0 or gnd = ON logic 1 = OFF	A = Standard 3.3V with Pin 8 open or program per Table 2.

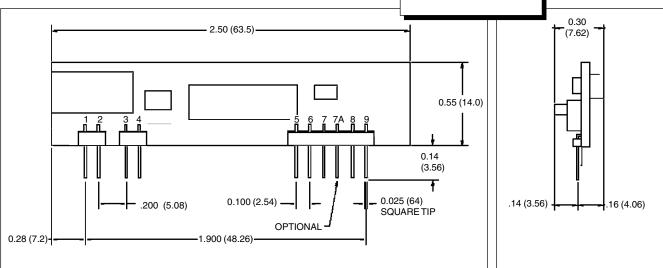
#### **Pin Out**

Pin	Function	Description
1	Vo	Output Voltage
2	Vo	Output Voltage
3	Vo	Output Voltage
4	GND	Ground
5	GND	Ground
6	VIN	Input Voltage
7	VIN	Input Voltage
7A	Pgood	Power Good Option
8	Trim	Output Voltage Adjust
9	Enable	Enable Option

#### **TOLERANCES**

- ±.008" for 3 place decimals
- ± .02" for 2 place decimals ± .002" for pin diameter





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