



Pletronics, Inc.

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Manufacturer of High Quality Frequency Control Products

**Specification For Part Number
SQ30057D-106.25M**

☐ Standard Part* ☒ Special Part**

Description:

3rd overtone, Half Size (8 Pin) Metal, thru-hole clock oscillator for Qlogic

Specifications:

Frequency = 106.25 MHz

Output Logic = CMOS with Enable/Disable

Frequency Stability = ± 50 PPM over operating temperature range of 0 to +70 Deg. C

Supply Voltage Vcc = 3.3V

Symmetry = 45/55% at 50% of Vcc

Logic "1" = 90% of Vcc minimum

Logic "0" = 10% of Vcc maximum

Output Load = 15 pF maximum

Supply Current Icc = 55 mA maximum with 15pF load

Tr and Tf = 1 nS maximum with 15 pF load between Vcc 0.8 V and 2.0 V

Jitter = 8 pS RMS maximum

Carrier Frequency modulation = Down @ 50 dBc from 100 kHz to 1.0 MHz

Use of sealed crystal with special protection of proprietary design to increase survivability against shock and vibration

Design credits for NexGen and PST

Sealed Crystal:

Yes

Special Mechanical Requirements:

special sealed crystals

Special Tests:

None

Non Standard Specs:

jitter, Tr & Tf, Carrier Frequency Modulation

**Special part numbers are controlled through this specification. Revision of this specification is the prepared date.

*Electrical specifications are defined by the standard part number per OP-SLS02 and are shown for your reference only.

(See <http://www.pletronics.com/CMOS.htm> for complete standard part specifications)

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