### **DISCRETE SEMICONDUCTORS**

# DATA SHEET



## PMLL4148L; PMLL4448 High-speed diodes

Product specification Supersedes data of 2000 Nov 15 2002 Jan 23





### **High-speed diodes**

### **PMLL4148L**; **PMLL4448**

#### **FEATURES**

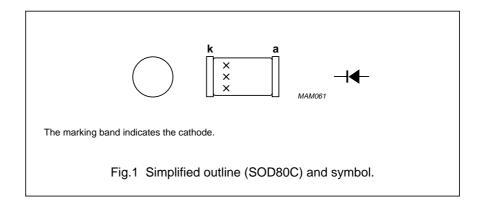
- Small hermetically sealed glass SMD package
- High switching speed: max. 4 ns
- Continuous reverse voltage: max. 75 V
- Repetitive peak reverse voltage: max. 100 V
- Repetitive peak forward current: max. 450 mA.

#### **APPLICATIONS**

- · High-speed switching
- Fast logic applications.

#### **DESCRIPTION**

The PMLL4148L and PMLL4448 are high-speed switching diodes fabricated in planar technology, and encapsulated in small hermetically sealed glass SOD80C SMD packages. PMLL4148L was formerly named PMLL4148 and has no difference to this type in technical specification, processing, packing or labelling.



#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>RRM</sub>	repetitive peak reverse voltage		_	100	V
V <sub>R</sub>	continuous reverse voltage		_	75	V
I <sub>F</sub>	continuous forward current	see Fig.2; note 1	_	200	mA
I <sub>FRM</sub>	repetitive peak forward current		_	450	mA
I <sub>FSM</sub>	non-repetitive peak forward current	square wave; T <sub>j</sub> = 25 °C prior to surge; see Fig.4			
		t = 1 μs	_	4	A
		t = 1 ms	_	1	A
		t = 1 s	_	0.5	A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C; note 1	_	500	mW
T <sub>stg</sub>	storage temperature		-65	+200	°C
Tj	junction temperature		_	200	°C

#### Note

1. Device mounted on an FR4 printed-circuit board.

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#### **ELECTRICAL CHARACTERISTICS**

 $T_j$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>F</sub>	forward voltage	see Fig.3			
	PMLL4148L	I <sub>F</sub> = 10 mA	_	1	V
	PMLL4448	I <sub>F</sub> = 5 mA	620	720	mV
		I <sub>F</sub> = 100 mA	_	1	V
I <sub>R</sub>	reverse current	V <sub>R</sub> = 20 V; see Fig.5		25	nA
		$V_R = 20 \text{ V}; T_j = 150 ^{\circ}\text{C}; \text{ see Fig.5}$	_	50	μΑ
I <sub>R</sub>	reverse current; PMLL4448	$V_R = 20 \text{ V}; T_j = 100 ^{\circ}\text{C}; \text{ see Fig.5}$	_	3	μΑ
C <sub>d</sub>	diode capacitance	$f = 1 \text{ MHz}$ ; $V_R = 0$ ; see Fig.6		4	pF
t <sub>rr</sub>	reverse recovery time	when switched from $I_F$ = 10 mA to $I_R$ = 60 mA; $R_L$ = 100 $\Omega$ ; measured at $I_R$ = 1 mA; see Fig.7		4	ns
V <sub>fr</sub>	forward recovery voltage	when switched from $I_F = 50$ mA; $t_r = 20$ ns; see Fig.8	_	2.5	V

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-tp</sub>	thermal resistance from junction to tie-point		300	K/W
R <sub>th j-a</sub>	thermal resistance from junction to ambient	note 1	350	K/W

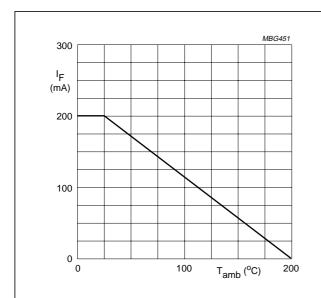
#### Note

1. Device mounted on an FR4 printed-circuit board.

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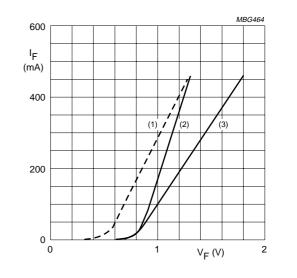
### PMLL4148L; PMLL4448

#### **GRAPHICAL DATA**



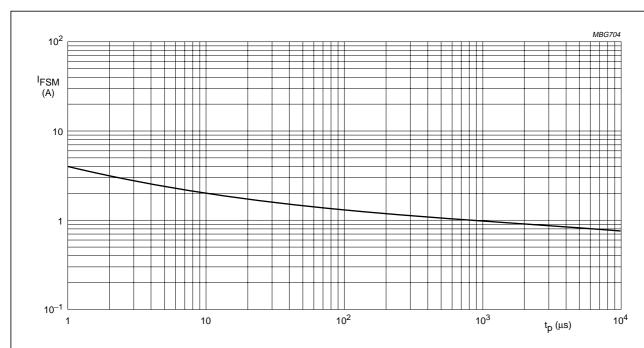
Device mounted on an FR4 printed-circuit board.

Fig.2 Maximum permissible continuous forward current as a function of ambient temperature.



- (1)  $T_j = 175$  °C; typical values.
- (2)  $T_j = 25$  °C; typical values.
- (3)  $T_j = 25$  °C; maximum values.

Fig.3 Forward current as a function of forward voltage.



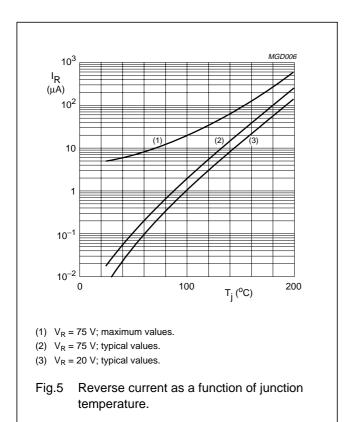
Based on square wave currents.

 $T_j$  = 25 °C prior to surge.

Fig.4 Maximum permissible non-repetitive peak forward current as a function of pulse duration.

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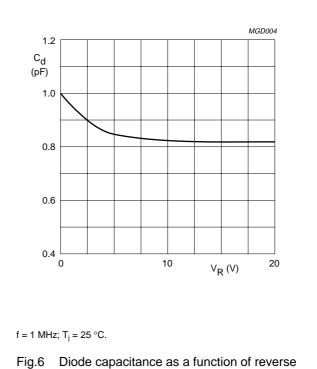
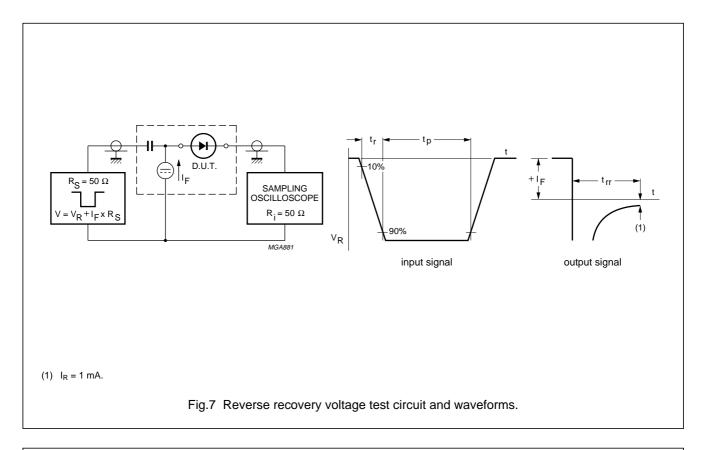
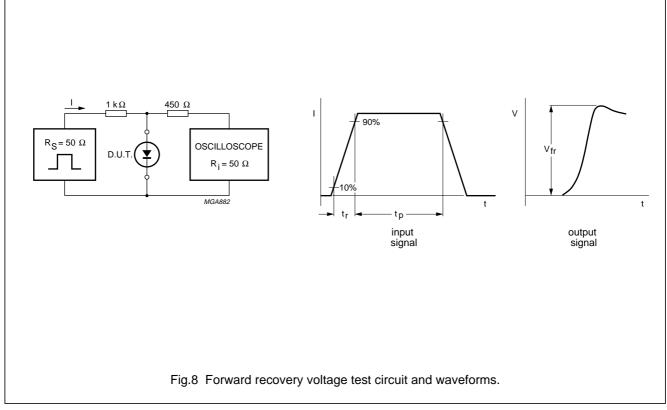


Fig.6 Diode capacitance as a function of reverse voltage; typical values.

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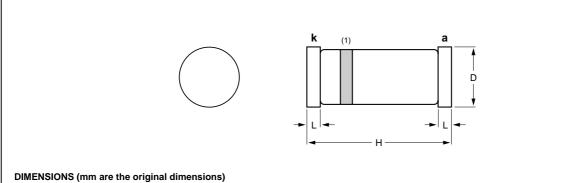
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#### **PACKAGE OUTLINE**

### Hermetically sealed glass surface mounted package; 2 connectors

SOD80C



UNIT	D	н	L	
mm	1.60 1.45	3.7 3.3	0.3	



#### Note

1. The marking band indicates the cathode.

OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	EIAJ		PROJECTION ISSUE DATE	
SOD80C	100H01					97-06-20

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DATA SHEET STATUS(1)	PRODUCT STATUS <sup>(2)</sup>	DEFINITIONS
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NOTES

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NOTES

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