

# BFR30LT1, BFR31LT1

## JFET Amplifiers

### N-Channel

#### Features

- Pb-Free Package is Available

#### MAXIMUM RATINGS

| Rating               | Symbol   | Value | Unit |
|----------------------|----------|-------|------|
| Drain-Source Voltage | $V_{DS}$ | 25    | Vdc  |
| Gate-Source Voltage  | $V_{GS}$ | 25    | Vdc  |

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS

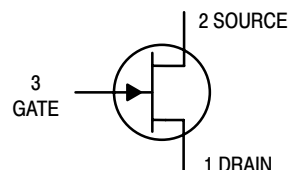
| Characteristic                                                                                                      | Symbol          | Max         | Unit                       |
|---------------------------------------------------------------------------------------------------------------------|-----------------|-------------|----------------------------|
| Total Device Dissipation (Note 1)<br>$T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$                    | $P_D$           | 225<br>1.8  | mW<br>mW/ $^\circ\text{C}$ |
| Thermal Resistance,<br>Junction-to-Ambient                                                                          | $R_{\theta JA}$ | 556         | $^\circ\text{C}/\text{W}$  |
| Total Device Dissipation Alumina<br>Substrate, (Note 2) $T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$ | $P_D$           | 300<br>2.4  | mW<br>mW/ $^\circ\text{C}$ |
| Thermal Resistance,<br>Junction-to-Ambient                                                                          | $R_{\theta JA}$ | 417         | $^\circ\text{C}/\text{W}$  |
| Junction and Storage Temperature                                                                                    | $T_J, T_{stg}$  | -55 to +150 | $^\circ\text{C}$           |

- Device mounted on FR4 glass epoxy printed circuit board using the recommended footprint.
- Alumina =  $0.4 \times 0.3 \times 0.024$  in 99.5% alumina.

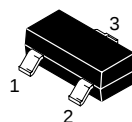


ON Semiconductor®

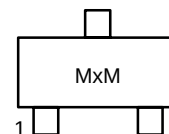
<http://onsemi.com>



#### MARKING DIAGRAM



SOT-23  
CASE 318  
STYLE 10



x = 1 or 2  
M = Date Code

#### ORDERING INFORMATION

| Device    | Package             | Shipping†        |
|-----------|---------------------|------------------|
| BFR30LT1  | SOT-23              | 3000/Tape & Reel |
| BFR30LT1G | SOT-23<br>(Pb-Free) | 3000/Tape & Reel |
| BFR31LT1  | SOT-23              | 3000/Tape & Reel |
| BFR31LT1G | SOT-23<br>(Pb-Free) | 3000/Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# BFR30LT1, BFR31LT1

## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

| Characteristic | Symbol | Min | Max | Unit |
|----------------|--------|-----|-----|------|
|----------------|--------|-----|-----|------|

### OFF CHARACTERISTICS

|                                                                                |               |      |      |      |
|--------------------------------------------------------------------------------|---------------|------|------|------|
| Gate Reverse Current ( $V_{GS} = 10\text{ Vdc}, V_{DS} = 0$ )                  | $I_{GSS}$     | –    | 0.2  | nAdc |
| Gate Source Cutoff Voltage ( $I_D = 0.5\text{ nAdc}, V_{DS} = 10\text{ Vdc}$ ) | $V_{GS(OFF)}$ | –    | 5.0  | Vdc  |
| Gate Source Voltage ( $I_D = 1.0\text{ mAdc}, V_{DS} = 10\text{ Vdc}$ )        | $V_{GS}$      | –0.7 | –3.0 | Vdc  |
|                                                                                |               | –    | –1.3 |      |
| ( $I_D = 50\text{ }\mu\text{Adc}, V_{DS} = 10\text{ Vdc}$ )                    |               | –    | –4.0 |      |
|                                                                                |               | –    | –2.0 |      |

### ON CHARACTERISTICS

|                                                                          |           |     |     |      |
|--------------------------------------------------------------------------|-----------|-----|-----|------|
| Zero-Gate-Voltage Drain Current ( $V_{DS} = 10\text{ Vdc}, V_{GS} = 0$ ) | $I_{DSS}$ | 4.0 | 10  | mAdc |
|                                                                          |           | 1.0 | 5.0 |      |

### SMALL-SIGNAL CHARACTERISTICS

|                                                                                                      |            |      |     |                  |
|------------------------------------------------------------------------------------------------------|------------|------|-----|------------------|
| Forward Transconductance<br>( $I_D = 1.0\text{ mAdc}, V_{DS} = 10\text{ Vdc}, f = 1.0\text{ kHz}$ )  | $ y_{fs} $ | 1.0  | 4.0 | mmhos            |
|                                                                                                      |            | 1.5  | 4.5 |                  |
| ( $I_D = 200\text{ }\mu\text{Adc}, V_{DS} = 10\text{ Vdc}, f = 1.0\text{ kHz}$ )                     |            | 0.5  | –   |                  |
|                                                                                                      |            | 0.75 | –   |                  |
| Output Admittance<br>( $I_D = 1.0\text{ mAdc}, V_{DS} = 10\text{ Vdc}, f = 1.0\text{ kHz}$ )         | $ y_{os} $ | 40   | 25  | $\mu\text{mhos}$ |
| ( $I_D = 200\text{ }\mu\text{Adc}, V_{DS} = 10\text{ Vdc}$ )                                         |            | 20   | 15  |                  |
| Input Capacitance ( $I_D = 1.0\text{ mAdc}, V_{DS} = 10\text{ Vdc}, f = 1.0\text{ MHz}$ )            | $C_{iss}$  | –    | 5.0 | pF               |
| ( $I_D = 200\text{ }\mu\text{Adc}, V_{DS} = 10\text{ Vdc}, f = 1.0\text{ MHz}$ )                     |            | –    | 4.0 |                  |
| Reverse Transfer Capacitance ( $I_D = 1.0\text{ mAdc}, V_{DS} = 10\text{ Vdc}, f = 1.0\text{ MHz}$ ) | $C_{rss}$  | –    | 1.5 | pF               |
| ( $I_D = 200\text{ }\mu\text{Adc}, V_{DS} = 10\text{ Vdc}, f = 1.0\text{ MHz}$ )                     |            | –    | 1.5 |                  |

## TYPICAL CHARACTERISTICS

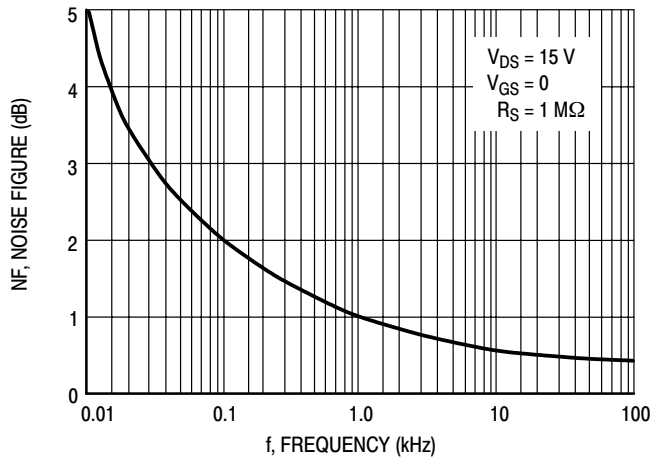


Figure 1. Noise Figure versus Frequency

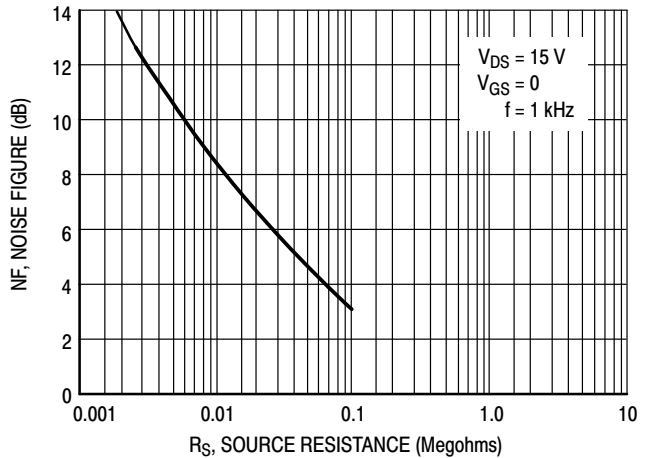


Figure 2. Noise Figure versus Source Resistance

# BFR30LT1, BFR31LT1

## TYPICAL CHARACTERISTICS

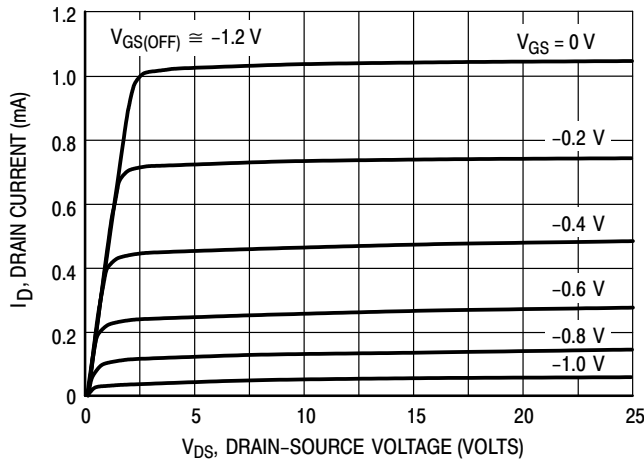


Figure 3. Typical Drain Characteristics

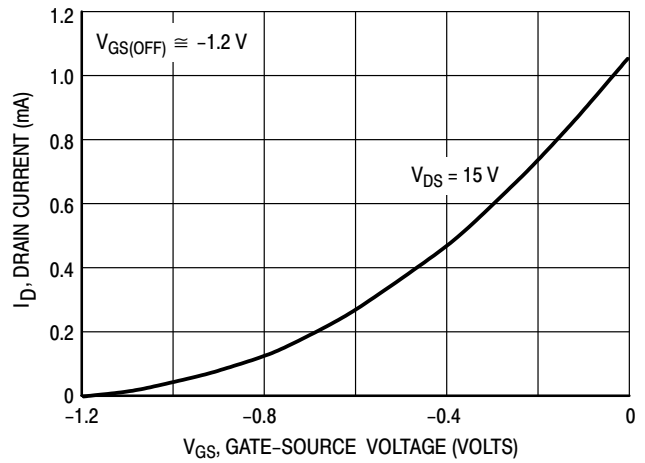


Figure 4. Common Source Transfer Characteristics

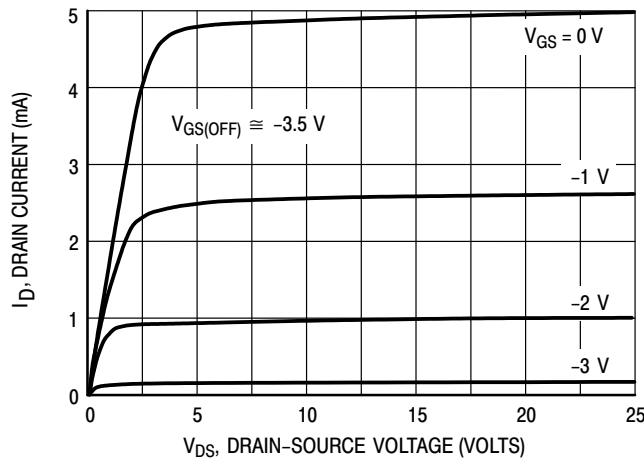


Figure 5. Typical Drain Characteristics

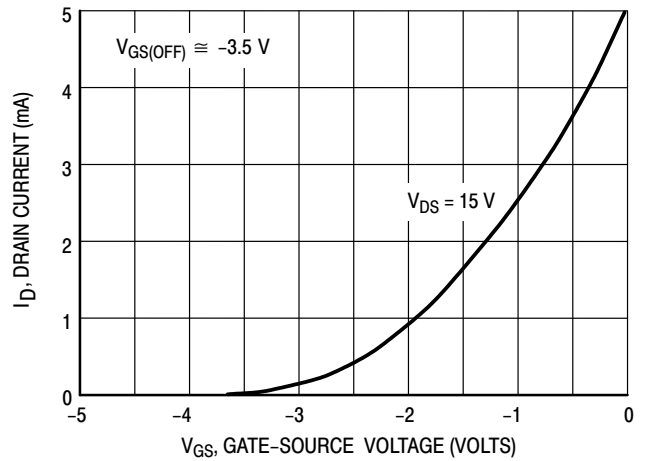


Figure 6. Common Source Transfer Characteristics

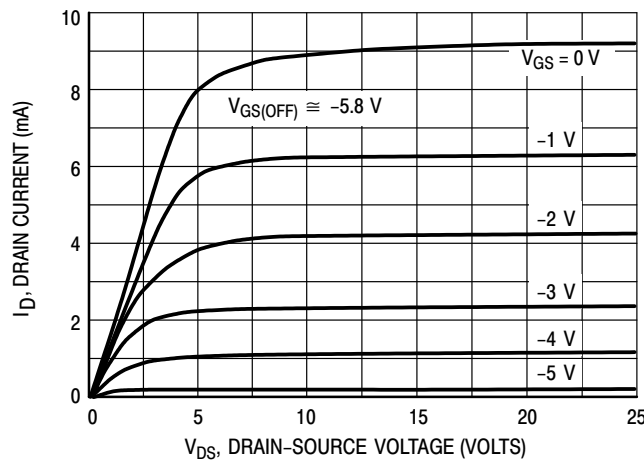


Figure 7. Typical Drain Characteristics

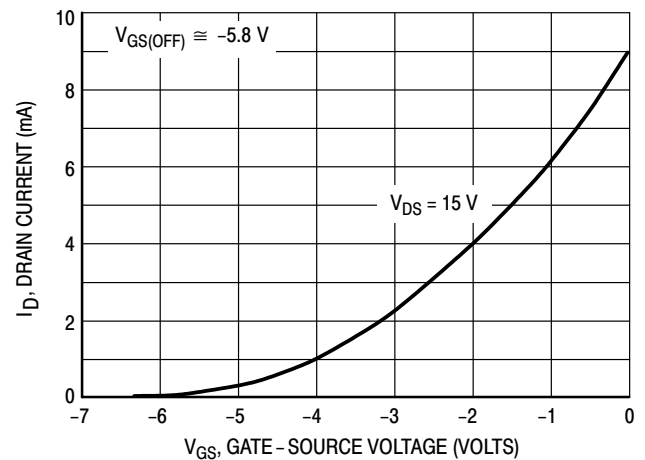
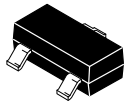


Figure 8. Common Source Transfer Characteristics

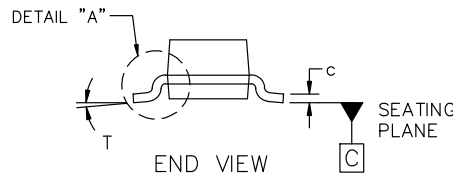
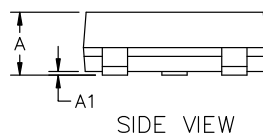
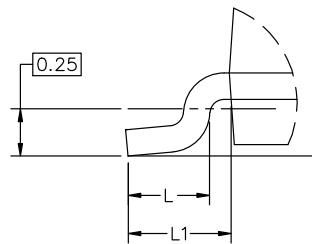
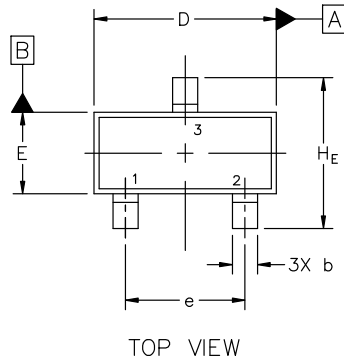
Note: Graphical data is presented for dc conditions. Tabular data is given for pulsed conditions (Pulse Width = 630 ms, Duty Cycle = 10%). Under dc conditions, self heating in higher  $I_{DSS}$  units reduces  $I_{DSS}$ .



SCALE 4:1

**SOT-23 (TO-236) 2.90x1.30x1.00 1.90P**  
**CASE 318**  
**ISSUE AU**

DATE 14 AUG 2024

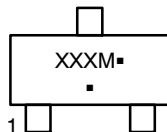


| MILLIMETERS |      |      |      |
|-------------|------|------|------|
| DIM         | MIN  | NOM  | MAX  |
| A           | 0.89 | 1.00 | 1.11 |
| A1          | 0.01 | 0.06 | 0.10 |
| b           | 0.37 | 0.44 | 0.50 |
| c           | 0.08 | 0.14 | 0.20 |
| D           | 2.80 | 2.90 | 3.04 |
| E           | 1.20 | 1.30 | 1.40 |
| e           | 1.78 | 1.90 | 2.04 |
| L           | 0.30 | 0.43 | 0.55 |
| L1          | 0.35 | 0.54 | 0.69 |
| HE          | 2.10 | 2.40 | 2.64 |
| T           | 0°   | ---  | 10°  |

NOTES:

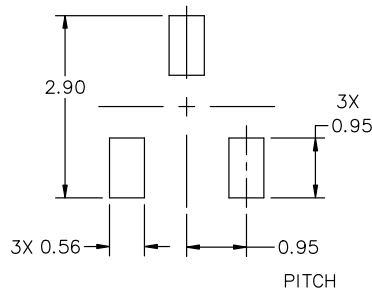
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018.
2. CONTROLLING DIMENSIONS: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

**GENERIC MARKING DIAGRAM\***



XXX = Specific Device Code  
M = Date Code  
▪ = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.



**RECOMMENDED MOUNTING FOOTPRINT**

\* For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERM/D.

**STYLES ON PAGE 2**

|                         |                                             |                                                                                                                                                                                     |
|-------------------------|---------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>DOCUMENT NUMBER:</b> | <b>98ASB42226B</b>                          | Electronic versions are uncontrolled except when accessed directly from the Document Repository.<br>Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |
| <b>DESCRIPTION:</b>     | <b>SOT-23 (TO-236) 2.90x1.30x1.00 1.90P</b> | <b>PAGE 1 OF 2</b>                                                                                                                                                                  |

onsemi and onsemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

**SOT-23 (TO-236) 2.90x1.30x1.00 1.90P**  
**CASE 318**  
**ISSUE AU**

DATE 14 AUG 2024

|                                                         |                                                       |                                                             |                                                             |                                                             |                                                             |
|---------------------------------------------------------|-------------------------------------------------------|-------------------------------------------------------------|-------------------------------------------------------------|-------------------------------------------------------------|-------------------------------------------------------------|
| STYLE 1 THRU 5:<br>CANCELLED                            | STYLE 6:<br>PIN 1. BASE<br>2. EMITTER<br>3. COLLECTOR | STYLE 7:<br>PIN 1. EMITTER<br>2. BASE<br>3. COLLECTOR       | STYLE 8:<br>PIN 1. ANODE<br>2. NO CONNECTION<br>3. CATHODE  |                                                             |                                                             |
| STYLE 9:<br>PIN 1. ANODE<br>2. ANODE<br>3. CATHODE      | STYLE 10:<br>PIN 1. DRAIN<br>2. SOURCE<br>3. GATE     | STYLE 11:<br>PIN 1. ANODE<br>2. CATHODE<br>3. CATHODE-ANODE | STYLE 12:<br>PIN 1. CATHODE<br>2. CATHODE<br>3. ANODE       | STYLE 13:<br>PIN 1. SOURCE<br>2. DRAIN<br>3. GATE           | STYLE 14:<br>PIN 1. CATHODE<br>2. GATE<br>3. ANODE          |
| STYLE 15:<br>PIN 1. GATE<br>2. CATHODE<br>3. ANODE      | STYLE 16:<br>PIN 1. ANODE<br>2. CATHODE<br>3. CATHODE | STYLE 17:<br>PIN 1. NO CONNECTION<br>2. ANODE<br>3. CATHODE | STYLE 18:<br>PIN 1. NO CONNECTION<br>2. CATHODE<br>3. ANODE | STYLE 19:<br>PIN 1. CATHODE<br>2. ANODE<br>3. CATHODE-ANODE | STYLE 20:<br>PIN 1. CATHODE<br>2. ANODE<br>3. GATE          |
| STYLE 21:<br>PIN 1. GATE<br>2. SOURCE<br>3. DRAIN       | STYLE 22:<br>PIN 1. RETURN<br>2. OUTPUT<br>3. INPUT   | STYLE 23:<br>PIN 1. ANODE<br>2. ANODE<br>3. CATHODE         | STYLE 24:<br>PIN 1. GATE<br>2. DRAIN<br>3. SOURCE           | STYLE 25:<br>PIN 1. ANODE<br>2. CATHODE<br>3. GATE          | STYLE 26:<br>PIN 1. CATHODE<br>2. ANODE<br>3. NO CONNECTION |
| STYLE 27:<br>PIN 1. CATHODE<br>2. CATHODE<br>3. CATHODE | STYLE 28:<br>PIN 1. ANODE<br>2. ANODE<br>3. ANODE     |                                                             |                                                             |                                                             |                                                             |

|                  |                                      |                                                                                                                                                                                     |
|------------------|--------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| DOCUMENT NUMBER: | 98ASB42226B                          | Electronic versions are uncontrolled except when accessed directly from the Document Repository.<br>Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |
| DESCRIPTION:     | SOT-23 (TO-236) 2.90x1.30x1.00 1.90P | PAGE 2 OF 2                                                                                                                                                                         |

**onsemi** and **onsemi** are trademarks of Semiconductor Components Industries, LLC dba **onsemi** or its subsidiaries in the United States and/or other countries. **onsemi** reserves the right to make changes without further notice to any products herein. **onsemi** makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. **onsemi** does not convey any license under its patent rights nor the rights of others.

**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## ADDITIONAL INFORMATION

### TECHNICAL PUBLICATIONS:

Technical Library: [www.onsemi.com/design/resources/technical-documentation](http://www.onsemi.com/design/resources/technical-documentation)  
onsemi Website: [www.onsemi.com](http://www.onsemi.com)

### ONLINE SUPPORT: [www.onsemi.com/support](http://www.onsemi.com/support)

For additional information, please contact your local Sales Representative at  
[www.onsemi.com/support/sales](http://www.onsemi.com/support/sales)