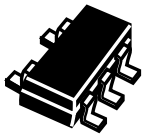
TS880, TS883

Datasheet

Rail-to-rail 0.9 V open-drain output, nanopower comparators

SC70-5



SOT23-5

MiniSO8

DFN8 2x2

Features

- Ultra low current consumption: 250 nA typ./op.
- Propagation delay: 2 µs typ.
- Rail-to-rail inputs
- Open-drain outputs
- Supply operation from 0.9 V to 5.5 V
- Wide temperature range: -40 to +125 °C
- ESD tolerance: 8 kV HBM
- Single version available in SC70-5 and SOT23-5 package
- Dual version available in MiniSO8 and DFN8 2x2 mm package

Applications

- Portable systems
- Signal conditioning
- Medical

Description

The open-drain series of nanopower comparator integrates a single version TS880 and a dual version TS883. They feature an ultra low supply current of 250 nA typical per operator with rail-to-rail input capability and open-drain output. The performance of these comparators allows them to be used in a wide range of portable applications. The TS880 and TS883 minimize battery supply leakage and therefore enhance battery lifetime.

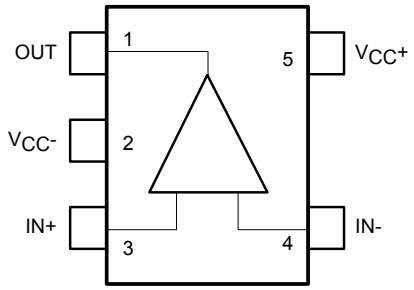
Operating from 0.9 to 5.5V supply voltage, these comparators can be used over a wide temperature range (-40 to +125 °C) keeping the current consumption at an ultra low level.

Product status link
TS880
TS883

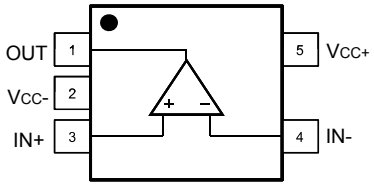
Related products	
TS881	See the datasheet for push-pull output version
TS882	
TS884	

1 Pin description

Figure 1. **Pin connection TS880 (top view)**

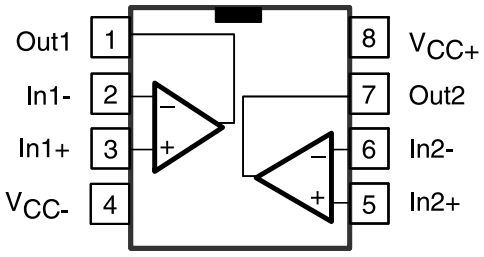


SC70-5



SOT23-5

Figure 2. **Pin connection TS883 (top view)**



MiniSO8/DFN8

2 Absolute maximum ratings and operating conditions

Table 1. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{CC}	Supply voltage(1)	6	V
V_{ID}	Differential input voltage(2)	± V_{CC}	V
V_{IN}	Input voltage range	(V_{CC}-) - 0.3 to (V_{CC}+) + 0.3	V
I_{IN}	Input current(3)	±10	mA
V_{OUT}	Output voltage	6	V
R_{THJA}	Thermal resistance junction to ambient SC70-5	205	°C/W
	Thermal resistance junction to ambient SOT23-5	250	
	Thermal resistance junction to ambient MiniSO8	190	
	Thermal resistance junction to ambient DFN8 2x2 mm	57	
T_{STG}	Storage temperature	-65 to +150	°C
T_J	Junction temperature	150	°C
T_{LEAD}	Lead temperature (soldering 10 s)	260	°C
ESD	Human body model (HBM)(4)	8000	V
	Charged device model (CDM)(5)	1500	

1. *All voltage values, except differential voltages, are referenced to V_{CC}-. V_{CC} is defined as the difference between V_{CC}+ and V_{CC}-.*
2. *The magnitude of input and output voltages must never exceed the supply rail ±0.3 V.*
3. *Input current must be limited by a serial resistor with inputs when the input voltage is beyond supply voltage.*
4. *According to JEDEC standard JESD22-A114F.*
5. *According to ANSI/ESD STM5.3.1.*

Table 2. Operating conditions

Symbol	Parameter	Value	Unit
T_{oper}	Operating temperature range	-40 to +125	°C
V_{CC}	Supply voltage	0.9 to 5.5	V
V_{ICM}	Common mode input voltage range 0.9 < Vcc < 1.1 V (1)	(VCC-) - 0.2 to (VCC-) + 0.2 and (VCC+) - 0.2 to (VCC+) + 0.2	V
	Common mode input voltage range 1.1 ≤ Vcc < 5.5 V	(VCC-) - 0.2 to (VCC+) + 0.2	
V_{OUT}	Output voltage	0 to 5.5	V

1. *See Figure 13. Input offset voltage vs. input common mode voltage, Vcc=0.9 V, Figure 14. Input hysteresis voltage vs. input common mode voltage, Vcc=0.9 V, Figure 27. Propagation delay T_{PLH} vs. input common mode voltage, Vcc=0.9 V and Figure 28. Propagation delay T_{PHL} vs. input common mode voltage, Vcc=0.9 V.*

3 Electrical characteristics

Table 3. Electrical characteristics V_{CC} = +0.9 V, T_{amb} = 25 °C, V_{ICM} = 0 V (unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_{IO}	Input offset voltage (1)	T_{amb} = + 25 °C	-10	1	10	mV
		-40 °C < T_{amb} < +85 °C	-12		12	
ΔV_{IO}	Input offset voltage drift	-40 °C < T_{amb} < +85 °C		3		µV/°C
V_{HYST}	Input hysteresis voltage (2)	T_{amb} = +25 °C		2.6		mV
		-40 °C < T_{amb} < +85 °C	1		4.2	
I_{IO}	Input offset current (3)	T_{amb} = + 25 °C		1	10	pA
		-40 °C < T_{amb}< + 85 °C			100	
I_{IB}	Input bias current (3)	Tamb = + 25 °C		5	10	pA
		- 40 °C < T_{amb} < + 85 °C			100	
I_{CC}	Supply current per operator	Output low, V_{ID} = - 0.1 V		300	480	nA
		- 40 °C < T_{amb} < + 85 °C			500	
		Output high,V_{ID} = + 0.1 V		250	410	
		-40 °C < T_{amb} < + 85 °C			440	
I_{Sink}	Output sink current	V_{OUT} = V_{CC+}, V_{ID} = - 0.1 V		1.2		mA
I_{OH}	High level output current	V_{OUT} = V_{CC+}, V_{ID} = 0.1 V		17	30	pA
		-40 °C < T_{amb} < + 85 °C			15	nA
V_{OL}	Output voltage low	I_{sink} = 50 µA, V_{ID} = - 0.1 V		10	50	mV
		-40 °C < T_{amb} < + 85 °C			70	
T_{PLH}	Propagation delay (4) (low to high)	f = 1 kHz, C_L = 10 pF, overdrive = 10 mV V_{PU} = V_{CC+}				µs
		R_{PU} = 10 kΩ, T_{amb} = +25 °C		7	10	
		R_{PU} = 10 kΩ, 40 °C < T_{amb} < +85 °C			12	
		R_{PU} = 1 MΩ, T_{amb} = +25 °C		20	22	
		R_{PU} = 1 MΩ, -40 °C < T_{amb} < +85 °C			24	
		f = 1 kHz, C_L = 10 pF, overdrive = 100 mV V_{PU} = V_{CC+}				
		R_{PU} = 10 kΩ, T_{amb} = +25 °C		2.5	4	
		R_{PU} = 10 kΩ, 40 °C < T_{amb} < +85 °C			4.5	
		R_{PU} = 1 MΩ, T_{amb} = +25 °C		18	20	
		R_{PU} = 1 MΩ, -40 °C < T_{amb} < +85 °C			21	

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
T_{PHL}	Propagation delay (5) (high to low)	f = 1 kHz, C_L = 10 pF, overdrive = 10 mV $V_{PU} = V_{CC+}$				µs
		R_{PU} = 10 kΩ, T_{amb} = +25 °C		6	9	
		R_{PU} = 10 kΩ, 40 °C < T_{amb} < +85 °C			10	
		R_{PU} = 1 MΩ, T_{amb} = +25 °C		7	9	
		R_{PU} = 1 MΩ, -40 °C < T_{amb} < +85 °C			10	
		f = 1 kHz, C_L = 10 pF, overdrive = 100 mV $V_{PU} = V_{CC+}$				
		R_{PU} = 10 kΩ, T_{amb} = +25 °C		2	3.5	
		R_{PU} = 10 kΩ, 40 °C < T_{amb} < +85 °C			4	
		R_{PU} = 1 MΩ, T_{amb} = +25 °C		2	4	
		R_{PU} = 1 MΩ, -40 °C < T_{amb} < +85 °C			5	
T_R	Rise time (10% to 90%)	C_L = 10 pF, R_{PU} = 10 kΩ, overdrive = 100 mV		500		ns
T_F	Fall time (90% to 10%)	C_L = 10 pF, R_{PU} = 10 kΩ, overdrive = 100 mV		600		ns
T_{ON}	Power-up time			0.9	1.3	ms

1. *The offset is defined as the average value of positive and negative trip points (input voltage differences requested to change the output state in each direction).*
2. *The hysteresis is a built-in feature. It is defined as the voltage difference between the trip points.*
3. *Maximum values include unavoidable inaccuracies of the industrial tests.*
4. *T_{PLH} is measured when the output signal crosses a voltage level at 50% of V_{CC} with the following conditions: inverting input voltage (IN-) = V_{ICM} and non-inverting input voltage (IN+) moving from V_{ICM} - 100 mV to V_{ICM} + overdrive.*
5. *T_{PHL} is measured when the output signal crosses a voltage level at 50% of V_{CC} with the following conditions: inverting input voltage (IN-) = V_{ICM} and non-inverting input voltage (IN+) moving from V_{ICM} + 100 mV to V_{ICM} - overdrive*

Note: All values over the temperature range are guaranteed through correlation and simulation. No production test is performed at the temperature range limits.

Table 4. Electrical characteristics V_{CC} = +1.2 V, T_{amb} = 25 °C, V_{ICM} = V_{CC}/2 (unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_{IO}	Input offset voltage(1)	T_{amb} = + 25 °C	-5	1	5	mV
		-40 °C < T_{amb} < +125 °C	-6		6	
ΔV_{IO}	Input offset voltage drift	-40 °C < T_{amb} < +125 °C		3		µV/°C
V_{HYST}	Input hysteresis voltage (2)	T_{amb} = +25 °C		2.5		mV
		-40 °C < T_{amb} < +125 °C	1.6		4.2	
I_{IO}	Input offset current (3)	T_{amb} = + 25 °C		2	10	pA
		-40 °C < T_{amb}< + 125 °C			100	
I_{IB}	Input bias current (3)	Tamb = + 25 °C		1	10	pA
		- 40 °C < T_{amb} < + 125 °C			100	
I_{CC}	Supply current per operator	Output low, V_{ID} = - 0.1 V		320	490	nA
		- 40 °C < T_{amb} < + 125 °C			510	
		Output high,V_{ID} = + 0.1 V		240	360	
		-40 °C < T_{amb} < + 125 °C			390	
I_{Sink}	Output sink current	V_{OUT} = V_{CC+}, V_{ID} = - 0.1 V		3.6		mA
I_{OH}	High level output current	V_{OUT} = V_{CC+}, V_{ID} = 0.1 V		22	36	pA
		-40 °C < T_{amb} < + 125 °C			15	nA
V_{OL}	Output voltage low	I_{sink} = 0.2 mA		25	50	mV
		-40 °C < T_{amb} < + 125 °C			70	
CMRR	Common mode rejection ratio	0 < V_{ICM} < V_{CC}		68		dB
		-40 °C < T_{amb} < + 125 °C	50			
T_{PLH}	Propagation delay (low to high)	f = 1 kHz, C_L = 10 pF, overdrive = 10 mV V_{PU} = V_{CC+}				µs
		R_{PU}= 10 kΩ,T_{amb}= + 25 °C		5	7.5	
		R_{PU}= 10 kΩ, 40 °C < T_{amb} < + 85 °C			8	
		R_{PU}= 1 MΩ,T_{amb}= + 25 °C		20	21	
		R_{PU}= 1 MΩ, -40 °C < T_{amb} < + 85 °C			22	
		f = 1 kHz, C_L = 10 pF, overdrive = 100 mV V_{PU} = V_{CC+}				
		R_{PU}= 10 kΩ,T_{amb}= + 25 °C		1.9	3	
		R_{PU}= 10 kΩ, 40 °C < T_{amb} < + 85 °C			3.5	
		R_{PU}= 1 MΩ, T_{amb} = + 25 °C		17	18	
		R_{PU}= 1 MΩ, -40 °C < T_{amb} < + 85 °C			19	

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
T_{PHL}	Propagation delay (5) (high to low)	f = 1 kHz, C_L = 10 pF, overdrive = 10 mV $V_{PU} = V_{CC+}$				µs
		R_{PU}= 10 kΩ, T_{amb}= + 25 °C		5	6.5	
		R_{PU}= 10 kΩ, 40 °C < T_{amb} < + 85 °C			7	
		R_{PU}= 1 MΩ, T_{amb} = + 25 °C		5	6	
		R_{PU}= 1 MΩ, -40 °C < T_{amb} < + 85 °C			7	
		f = 1 kHz, C_L = 10 pF, overdrive = 100 mV, $V_{PU} = V_{CC+}$				
		R_{PU}= 10 kΩ,T_{amb}= + 25 °C		1.7	2.5	
		R_{PU}= 10 kΩ, 40 °C < T_{amb} < + 85 °C			3	
		R_{PU}= 1 MΩ, T_{amb} = + 25 °C		2	3	
		R_{PU}= 1 MΩ, -40 °C < T_{amb} < + 85 °C			4	
T_R	Rise time (10% to 90%)	C_L = 10 pF, R_{PU} = 10 kΩ overdrive = 100 mV		800		ns
T_F	Fall time (90% to 10%)	C_L = 10 pF, R_{PU} = 10 kΩ overdrive = 100 mV		250		ns
T_{ON}	Power-up time			0.9	1.3	ms

1. *The offset is defined as the average value of positive and negative trip points (input voltage differences requested to change the output state in each direction*
2. *The hysteresis is a built-in feature. It is defined as the voltage difference between the trip points.*
3. *Maximum values include unavoidable inaccuracies of the industrial tests.*
4. *T_{PLH} is measured when the output signal crosses a voltage level at 50% of V_{CC} with the following conditions: inverting input voltage (IN-) = V_{ICM} and non-inverting input voltage (IN+) moving from V_{ICM} - 100 mV to V_{ICM} + overdrive.*
5. *T_{PHL} is measured when the output signal crosses a voltage level at 50% of V_{CC} with the following conditions: inverting input voltage (IN-) = V_{ICM} and non-inverting input voltage (IN+) moving from V_{ICM} + 100 mV to V_{ICM} - overdrive.*

Note: All values over the temperature range are guaranteed through correlation and simulation. No production test is performed at the temperature range limits.

Table 5. Electrical characteristics V_{CC} = +2.7 V, T_{amb} = 25 °C, V_{ICM} = V_{CC}/2 (unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_{IO}	Input offset voltage	T_{amb} = + 25 °C	-5	1	5	mV
		-40 °C < T_{amb} < +125 °C	-6		6	
ΔV_{IO}	Input offset voltage drift	-40 °C < T_{amb} < +125 °C		3		µV/°C
V_{HYST}	Input hysteresis voltage	T_{amb} = +25 °C		2.7		mV
		-40 °C < T_{amb} < +125 °C	1.6		4.2	
I_{IO}	Input offset current (3)	T_{amb} = + 25 °C		1	10	pA
		-40 °C < T_{amb}< + 125 °C			100	
I_{IB}	Input bias current (3)	Tamb = + 25 °C		5	10	pA
		- 40 °C < T_{amb} < + 125 °C			100	
I_{CC}	Supply current per operator	Output low, V_{ID} = - 0.1 V		350	520	nA
		- 40 °C < T_{amb} < + 125 °C			540	
		Output high, V_{ID} = + 0.1 V		250	370	
		-40 °C < T_{amb} < + 125 °C			400	
I_{Sink}	Output sink current	V_{OUT} = V_{CC+}, V_{ID} = - 0.1 V		22		mA
I_{OH}	High level output current	V_{OUT} = V_{CC+}, V_{ID} = 0.1 V		40	60	pA
		-40 °C < T_{amb} < + 125 °C			18	nA
V_{OL}	Output voltage low	I_{sink} = 2 mA		140	220	mV
		-40 °C < T_{amb} < + 125 °C			290	
CMRR	Common mode rejection ratio	0 < V_{ICM} < V_{CC}		74		dB
		-40 °C < T_{amb} < + 125 °C	55			
T_{PLH}	Propagation delay (low to high)	f = 1 kHz, C_L = 10 pF, overdrive = 10 mV V_{PU} = V_{CC+}				µs
		R_{PU}= 10 kΩ,T_{amb}= + 25 °C		5.8	8.5	
		R_{PU}= 10 kΩ, 40 °C < T_{amb}< +85 °C			10	
		R_{PU}= 1 MΩ, T_{amb} = + 25 °C		21	23	
		R_{PU}= 1 MΩ, -40 °C < T_{amb}< +85 °C			24	
		f = 1 kHz, C_L = 10 pF, overdrive = 100 mV V_{PU}= V_{CC+}				
		R_{PU}= 10 kΩ, T_{amb}= + 25 °C		2	3.3	
		R_{PU}= 10 kΩ, 40 °C < T_{amb}< +85 °C			3.5	
		R_{PU}= 1 MΩ, T_{amb}= + 25 °C		18	19	
		R_{PU} = 1 MΩ, -40 °C < T_{amb}< +85 °C			20	

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
T_{PHL}	Propagation delay (high to low)	f = 1 kHz, C_L = 10 pF, overdrive = 10 mV $V_{PU} = V_{CC+}$				µs
		R_{PU}= 10 kΩ, T_{amb}= + 25 °C		5.8	8.5	
		R_{PU}= 10 kΩ, 40 °C < T_{amb}< +85 °C			10	
		R_{PU}= 1 MΩ, T_{amb} = + 25 °C		6	7	
		R_{PU}= 1 MΩ, -40 °C < T_{amb}< +85 °C			10	
		f = 1 kHz, C_L = 10 pF, overdrive = 100 mV V_{PU}= V_{CC+}				
		R_{PU}= 10 kΩ, T_{amb}= + 25 °C		1.9	3	
		R_{PU}= 10 kΩ, 40 °C < T_{amb}< +85 °C			3.5	
		R_{PU}= 1 MΩ, T_{amb}= + 25 °C		2	3.5	
		R_{PU} = 1 MΩ, -40 °C < T_{amb}< +85 °C			4.5	
T_R	Rise time (10% to 90%)	C_L = 10 pF, R_{PU} = 10 kΩ overdrive = 100 mV		1800		ns
T_F	Fall time (90% to 10%)	C_L = 10 pF, R_{PU} = 10 kΩ overdrive = 100 mV		170		ns
T_{ON}	Power-up time			0.9	1.3	ms

1. *The offset is defined as the average value of positive and negative trip points (input voltage differences requested to change the output state in each direction*
2. *The hysteresis is a built-in feature. It is defined as the voltage difference between the trip points.*
3. *Maximum values include unavoidable inaccuracies of the industrial tests.*
4. *T_{PLH} is measured when the output signal crosses a voltage level at 50% of V_{CC} with the following conditions: inverting input voltage (IN-) = V_{ICM} and non-inverting input voltage (IN+) moving from V_{ICM} - 100 mV to V_{ICM} + overdrive.*
5. *T_{PHL} is measured when the output signal crosses a voltage level at 50% of V_{CC} with the following conditions: inverting input voltage (IN-) = V_{ICM} and non-inverting input voltage (IN+) moving from V_{ICM} + 100 mV to V_{ICM} - overdrive.*

Note: All values over the temperature range are guaranteed through correlation and simulation. No production test is performed at the temperature range limits.

Table 6. Electrical characteristics V_{CC} = +5 V, T_{amb} = 25 °C, V_{ICM} = V_{CC}/2 (unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_{IO}	Input offset voltage	T_{amb} = + 25 °C	-5	1	5	mV
		-40 °C < T_{amb} < +125 °C	-6		6	
ΔV_{IO}	Input offset voltage drift	-40 °C < T_{amb} < +125 °C		3		µV/°C
V_{HYST}	Input hysteresis voltage (2)	T_{amb} = +25 °C		3.1		mV
		-40 °C < T_{amb} < +125 °C	1.6		4.2	
I_{IO}	Input offset current (3)	T_{amb} = + 25 °C		1	10	pA
		-40 °C < T_{amb}< + 125 °C			100	
I_{IB}	Input bias current (3)	Tamb = + 25 °C		10	20	pA
		- 40 °C < T_{amb} < + 125 °C			100	
I_{CC}	Supply current per operator	Output low, V_{ID} = - 0.1 V		400	600	nA
		- 40 °C < T_{amb} < + 125 °C			630	
		Output high, V_{ID} = + 0.1 V		290	430	
		-40 °C < T_{amb} < + 125 °C			480	
I_{Sink}	Output sink current	V_{OUT} = V_{CC+}, V_{ID} = - 0.1 V		50		mA
I_{OH}	High level output current	V_{OUT} = V_{CC+}, V_{ID} = 0.1 V		80	100	pA
		-40 °C < T_{amb} < + 125 °C			20	nA
V_{OL}	Output voltage low	I_{sink} = 2 mA		110	180	mV
		-40 °C < T_{amb} < + 125 °C			240	
CMRR	Common mode rejection ratio	0 < V_{ICM} < V_{CC}		78		dB
		-40 °C < T_{amb} < + 125 °C	55			
SVR	Supply voltage rejection	ΔV_{CC}=1.2 V to 5 V		80		dB
		-40 °C < T_{amb} < + 125 °C	65			
T_{PLH}	Propagation delay (4) (low to high)	f = 1 kHz, C_L = 10 pF, overdrive = 10 mV V_{PU} = V_{CC+}				µs
		R_{PU}= 10 kΩ, T_{amb}= + 25 °C		7	11	
		R_{PU}= 10 kΩ, 40 °C < T_{amb}< +85 °C			13	
		R_{PU}= 1 MΩ, T_{amb} = + 25 °C		21	23	
		R_{PU}= 1 MΩ, -40 °C < T_{amb}< +85 °C			24	
		f = 1 kHz, C_L = 10 pF, overdrive = 100 mV V_{PU}= V_{CC+}				
		R_{PU}= 10 kΩ, T_{amb}= + 25 °C		3	4.4	
		R_{PU}= 10 kΩ, 40 °C < T_{amb}< +85 °C			5	
		R_{PU}= 1 MΩ, T_{amb}= + 25 °C		18	20	
		R_{PU} = 1 MΩ, -40 °C < T_{amb}< +85 °C			21	

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
T_{PHL}	Propagation delay (5) (high to low)	f = 1 kHz, C_L = 10 pF, overdrive = 10 mV $V_{PU} = V_{CC+}$				µs
		R_{PU}= 10 kΩ, T_{amb}= + 25 °C		7.5	11	
		R_{PU}= 10 kΩ, 40 °C < T_{amb}< +85 °C			12	
		R_{PU}= 1 MΩ, T_{amb} = + 25 °C		9	11	
		R_{PU}= 1 MΩ, -40 °C < T_{amb}< +85 °C			13	
		f = 1 kHz, C_L = 10 pF, overdrive = 100 mV V_{PU}= V_{CC+}				
		R_{PU}= 10 kΩ, T_{amb}= + 25 °C		2	3.3	
		R_{PU}= 10 kΩ, 40 °C < T_{amb}< +85 °C			3.5	
		R_{PU}= 1 MΩ, T_{amb}= + 25 °C		2	3	
		R_{PU} = 1 MΩ, -40 °C < T_{amb}< +85 °C			4	
T_R	Rise time (10% to 90%)	C_L = 10 pF, R_{PU} = 10 kΩ overdrive = 100 mV		3.7		µs
T_F	Fall time (90% to 10%)	C_L = 10 pF, R_{PU} = 10 kΩ overdrive = 100 mV		190		ns
T_{ON}	Power-up time			0.9	1.3	ms

1. *The offset is defined as the average value of positive and negative trip points (input voltage differences requested to change the output state in each direction).*
2. *The hysteresis is a built-in feature. It is defined as the voltage difference between the trip points.*
3. *Maximum values include unavoidable inaccuracies of the industrial tests*
4. *T_{PLH} is measured when the output signal crosses a voltage level at 50% of V_{cc} with the following conditions: inverting input voltage (IN-) = V_{ICM} and non-inverting input voltage (IN+) moving from V_{ICM} - 100 mV to V_{ICM} + overdrive.*
5. *T_{PHL} is measured when the output signal crosses a voltage level at 50% of V_{cc} with the following conditions:inverting input voltage (IN-) = V_{ICM} and non-inverting input voltage (IN+) moving from V_{ICM} + 100 mV to V_{ICM} - overdrive.*

Note: All values over the temperature range are guaranteed through correlation and simulation. No production test is performed at the temperature range limits.

4 Typical characteristics and curves

Figure 3. Current consumption vs. supply voltage, Vicm=Vcc, output low

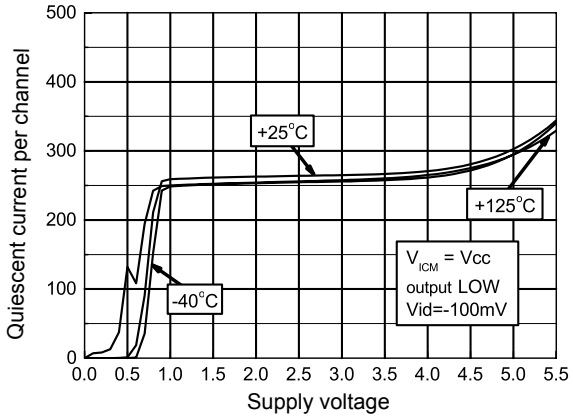


Figure 4. Current consumption vs. supply voltage, Vicm=Vcc, output high

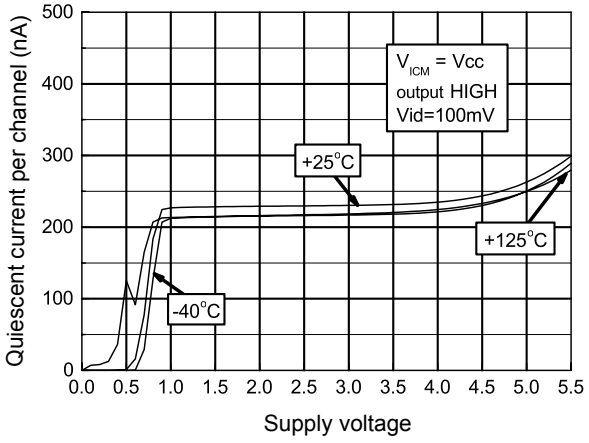


Figure 5. Current consumption vs. supply voltage, Vicm=0 V, output low

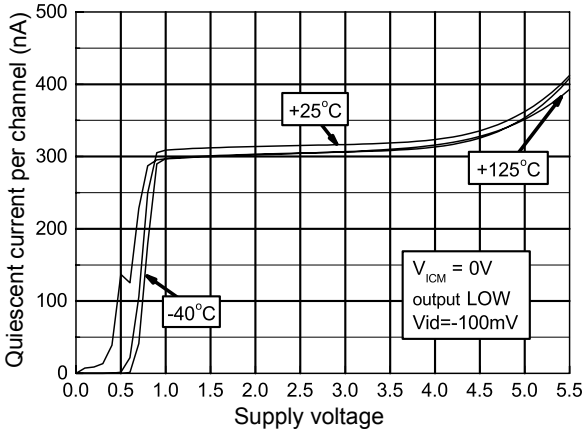


Figure 6. Current consumption vs. supply voltage, Vicm=0 V, output high

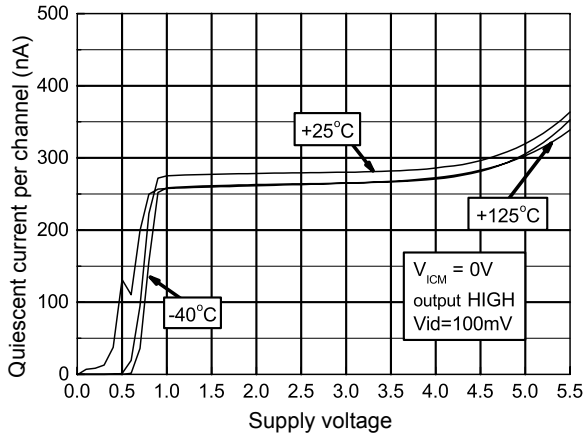


Figure 7. Current consumption vs. input common mode voltage, Vcc=0.9 V, output low

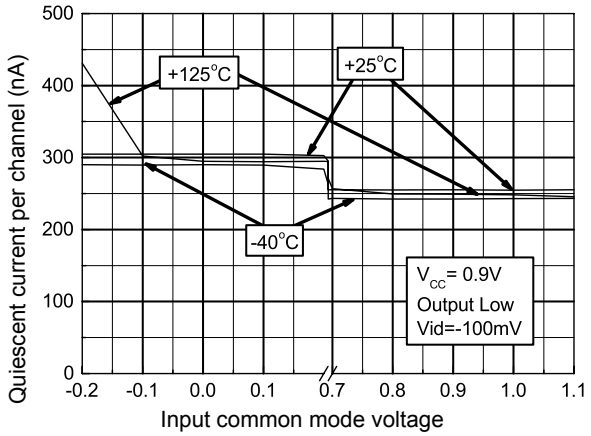


Figure 8. Current consumption vs. input common mode voltage, Vcc=0.9 V, output high

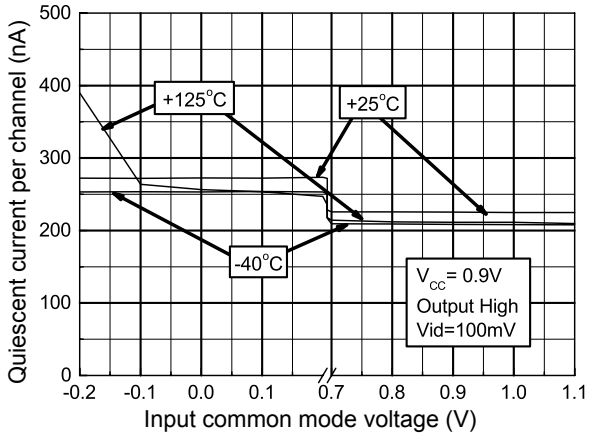


Figure 9. Current consumption vs. input common mode voltage, Vcc=5 V, output low

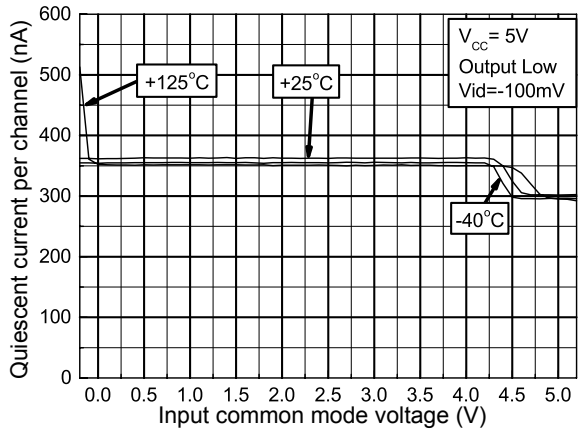


Figure 10. Current consumption vs. input common mode voltage, Vcc=5 V, output high

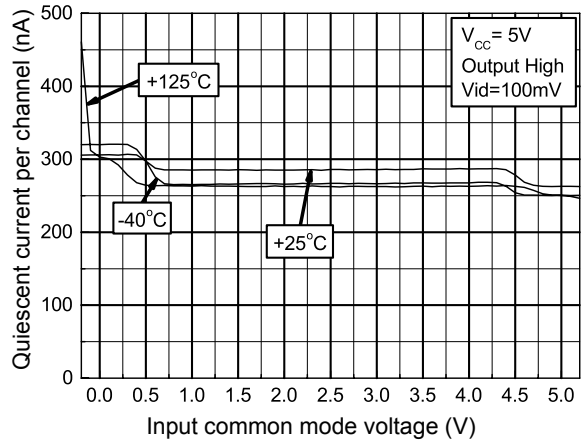


Figure 11. Current consumption vs. temperature

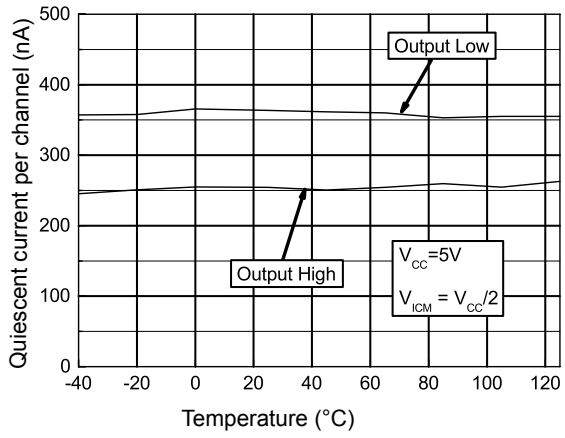


Figure 12. Current consumption vs. toggle frequency

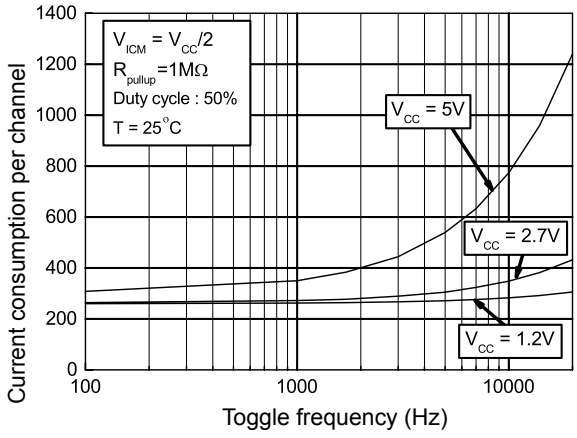


Figure 13. Input offset voltage vs. input common mode voltage, Vcc=0.9 V

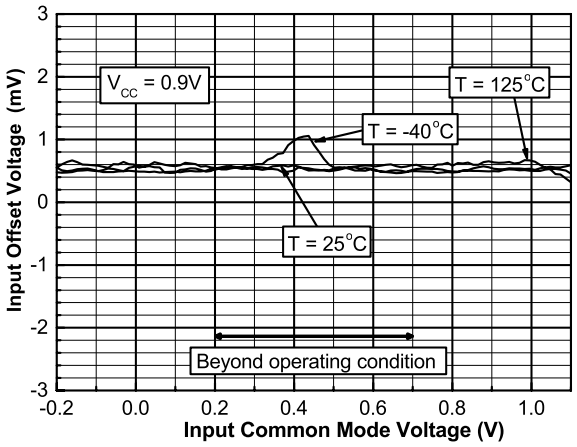


Figure 14. Input hysteresis voltage vs. input common mode voltage, Vcc=0.9 V

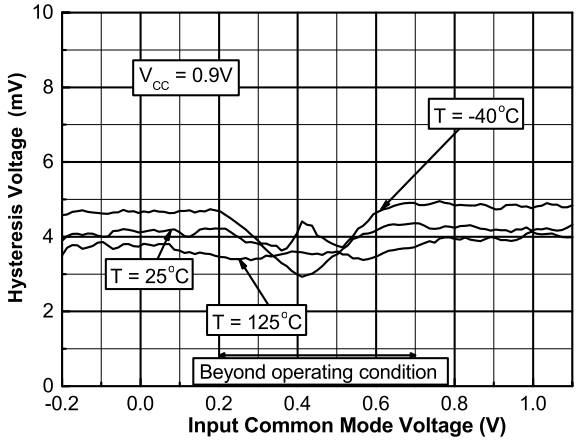


Figure 15. Input offset voltage vs. input common mode voltage, Vcc=1.2 V

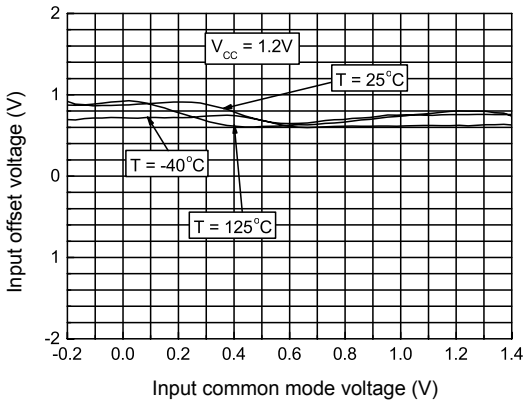


Figure 16. Input hysteresis voltage vs. input common mode voltage, Vcc=1.2 V

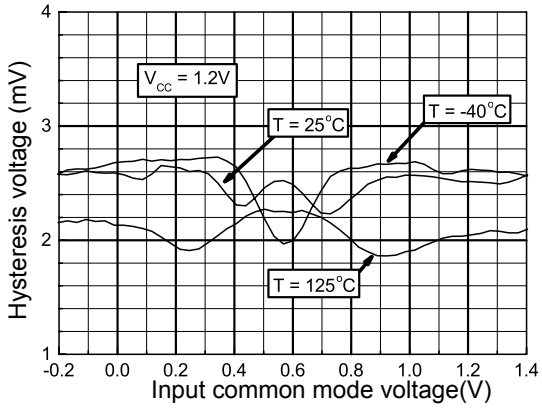


Figure 17. Input offset voltage vs. input common mode voltage, Vcc=5 V

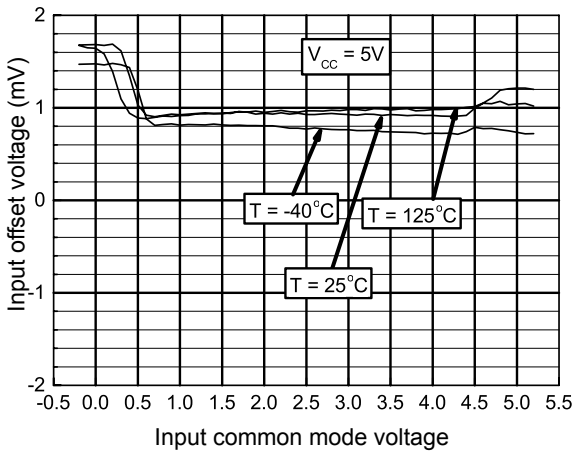


Figure 18. Input hysteresis voltage vs. input common mode voltage, Vcc=5 V

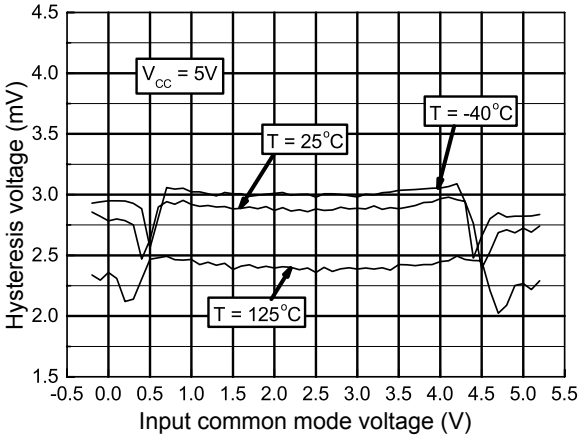


Figure 19. Input offset voltage vs. temperature

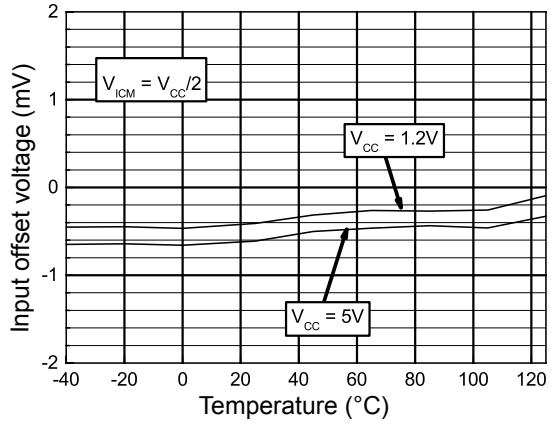


Figure 20. Input hysteresis voltage vs. temperature

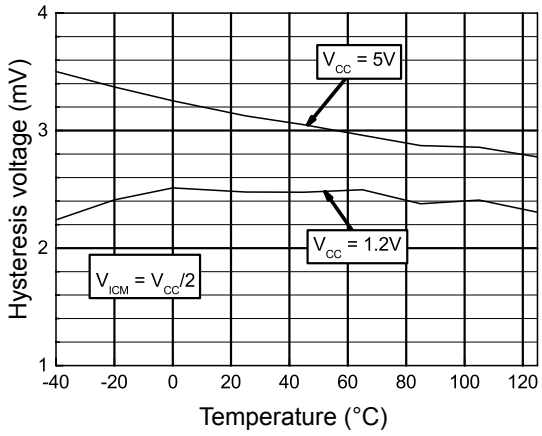


Figure 21. Output voltage drop vs. sink current, Vcc=1.2 V

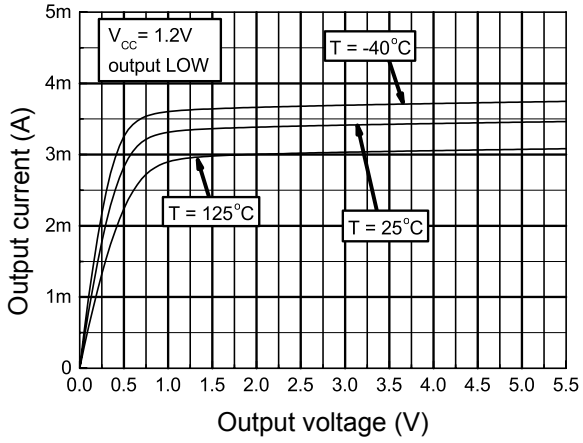


Figure 22. Output voltage drop vs. source current, Vcc=1.2 V

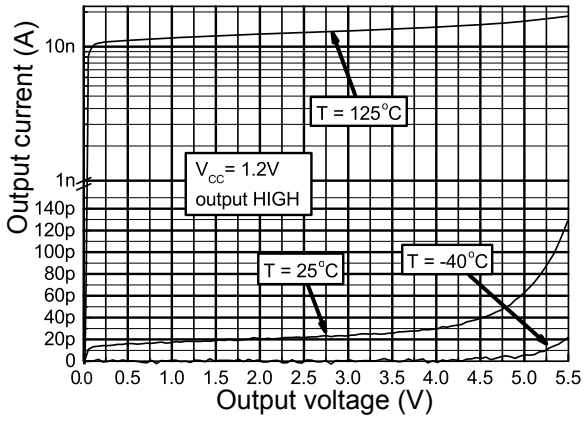


Figure 23. Output voltage drop vs. sink current, Vcc=2.7 V

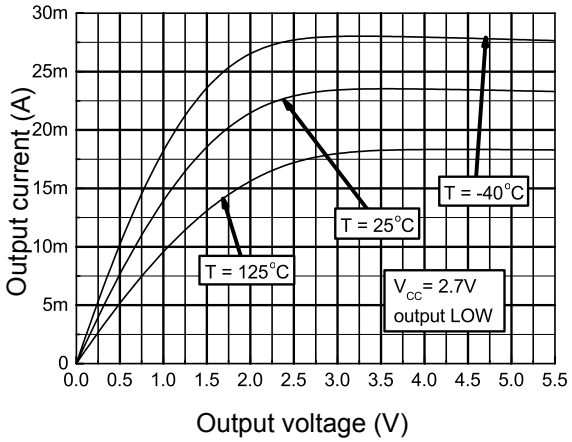


Figure 24. Output voltage drop vs. source current, Vcc=2.7 V

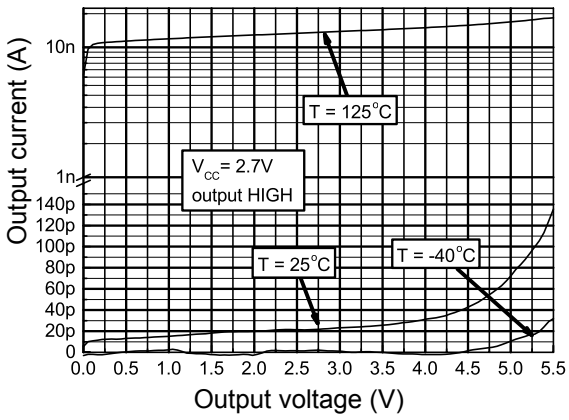


Figure 25. Output voltage drop vs. sink current, Vcc=5 V

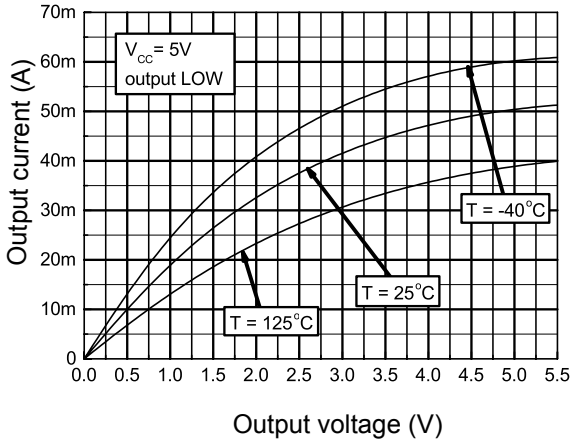


Figure 26. Output voltage drop vs. source current, Vcc=5 V

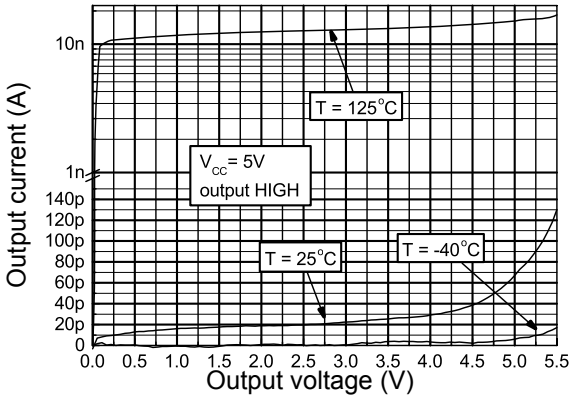


Figure 27. Propagation delay T_{PLH} vs. input common mode voltage, Vcc=0.9 V

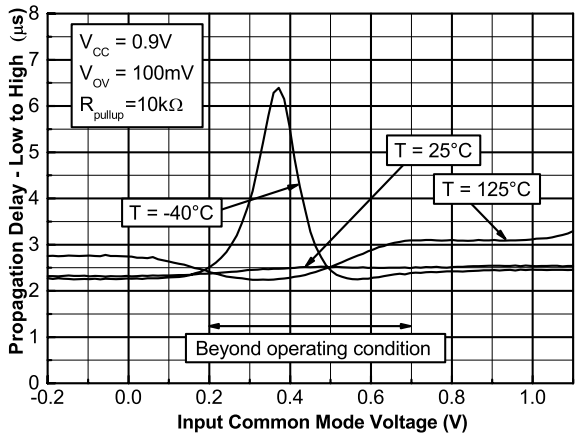


Figure 28. Propagation delay T_{PHL} vs. input common mode voltage, Vcc=0.9 V

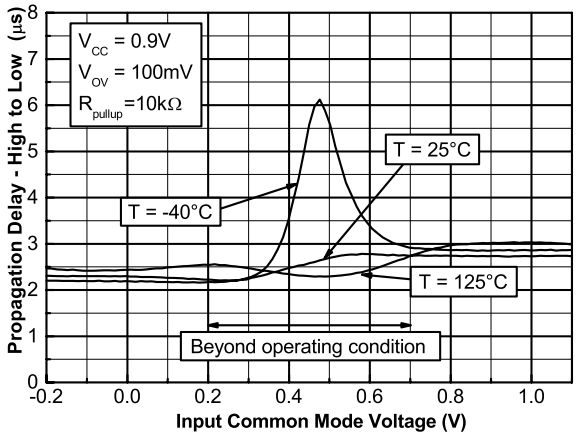


Figure 29. Propagation delay T_{PLH} vs. input common mode voltage, Vcc=1.2 V

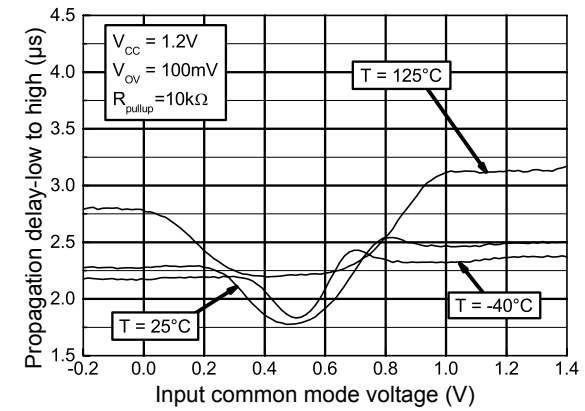


Figure 30. Propagation delay T_{PHL} vs. input common mode voltage, Vcc=1.2 V

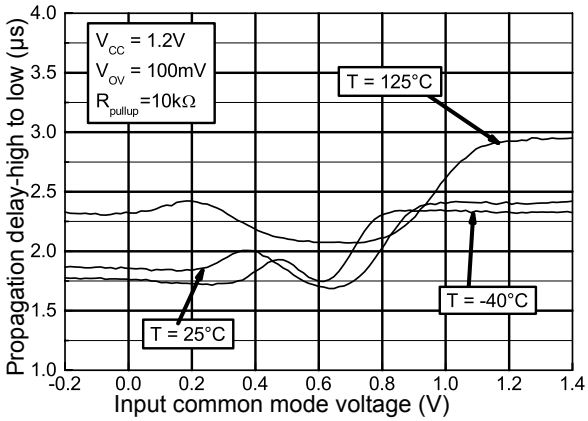


Figure 31. Propagation delay T_{PLH} vs. input common mode voltage, Vcc=5 V

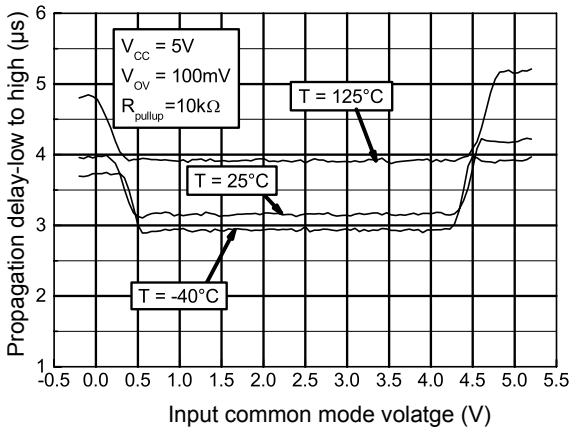


Figure 32. Propagation delay T_{PHL} vs. input common mode voltage, Vcc=5 V

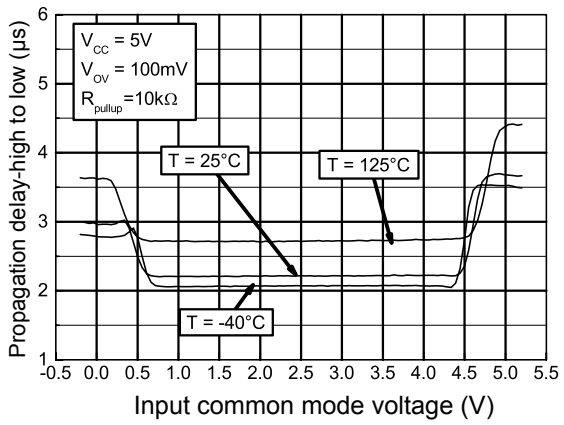


Figure 33. Propagation delay T_{PLH} vs. input signal overdrive, Vcc=1.2 V

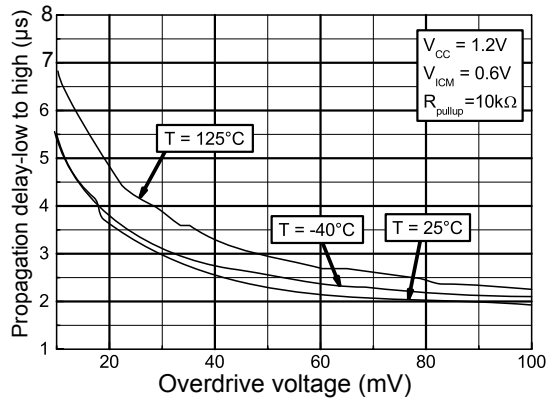


Figure 34. Propagation delay T_{PHL} vs. input signal overdrive, Vcc=1.2 V

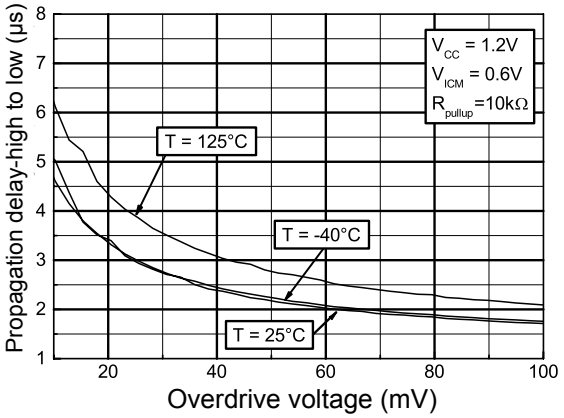


Figure 35. Current consumption Vs. input common mode voltage, Vcc=0.9 V, output low

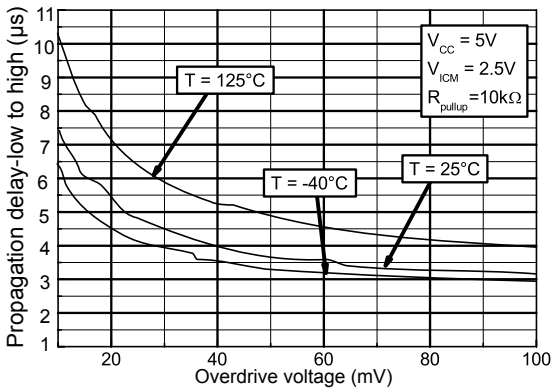


Figure 36. Current consumption Vs. input common mode voltage, Vcc=0.9 V, output high

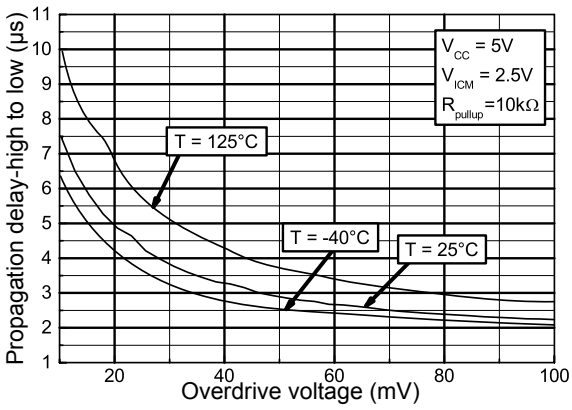


Figure 37. Propagation delay T_{PLH} vs. input signal overdrive, Vcc=5 V

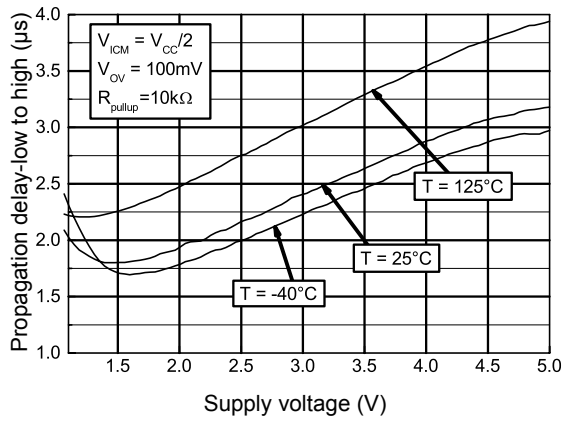


Figure 38. Propagation delay T_{PHL} vs. input signal overdrive, Vcc=5 V

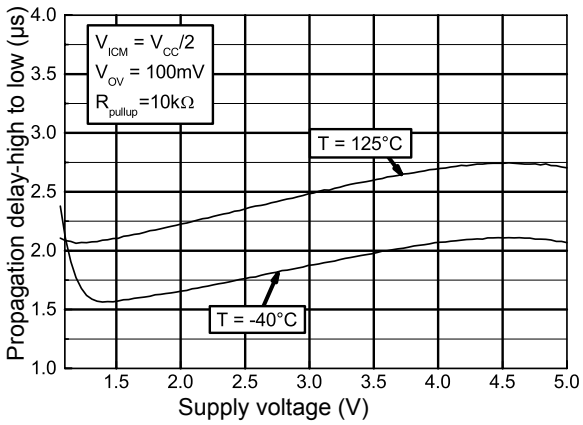


Figure 39. Propagation delay T_{PLH} vs. supply voltage

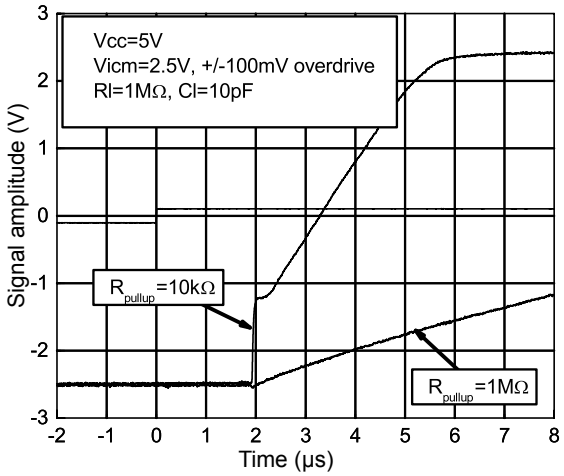
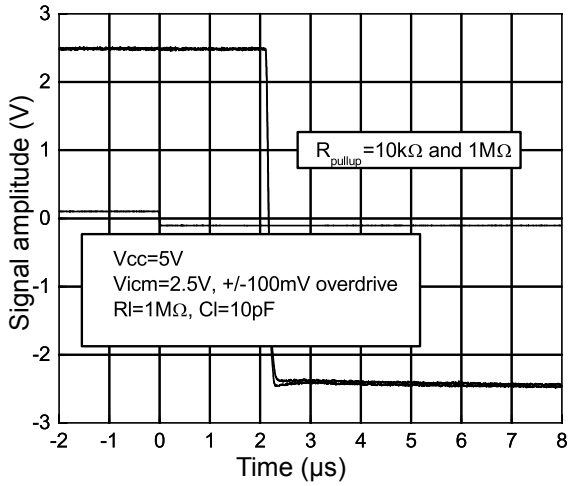


Figure 40. Propagation delay T_{PHL} vs. supply voltage



5 Ordering information

Table 7. **Order code**

Order code	Temp. range	Package	Packing	Marking
TS880ICT	-40 to +125 °C	SC70-5	Tape and reel	K5P
TS880ILT		SOT23-5		K534
TS883IST		MiniSO8		K5Q
TS883IQ2T		DFN8 2x2 mm		

6 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

6.1 SC70-5 (or SOT323-5) package information

Figure 41. **SC70-5 (or SOT323-5) package outline**

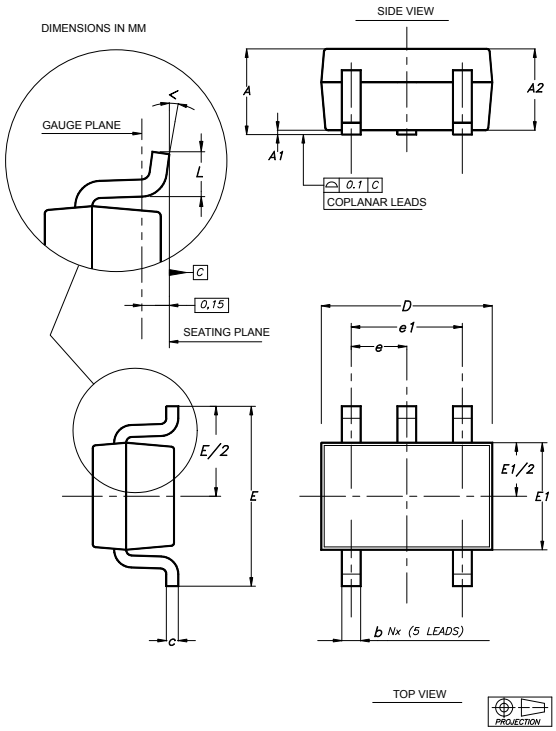


Table 8. **SC70-5 (or SOT323-5) package mechanical data**

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.80		1.10	0.032		0.043
A1			0.10			0.004
A2	0.80	0.90	1.00	0.032	0.035	0.039
b	0.15		0.30	0.006		0.012
c	0.10		0.22	0.004		0.009
D	1.80	2.00	2.20	0.071	0.079	0.087
E	1.80	2.10	2.40	0.071	0.083	0.094
E1	1.15	1.25	1.35	0.045	0.049	0.053
e		0.65			0.025	
e1		1.30			0.051	
L	0.26	0.36	0.46	0.010	0.014	0.018
<	0°		8°	0°		8°

6.2 SOT23-5 package information

Figure 42. **SOT23-5 package outline**

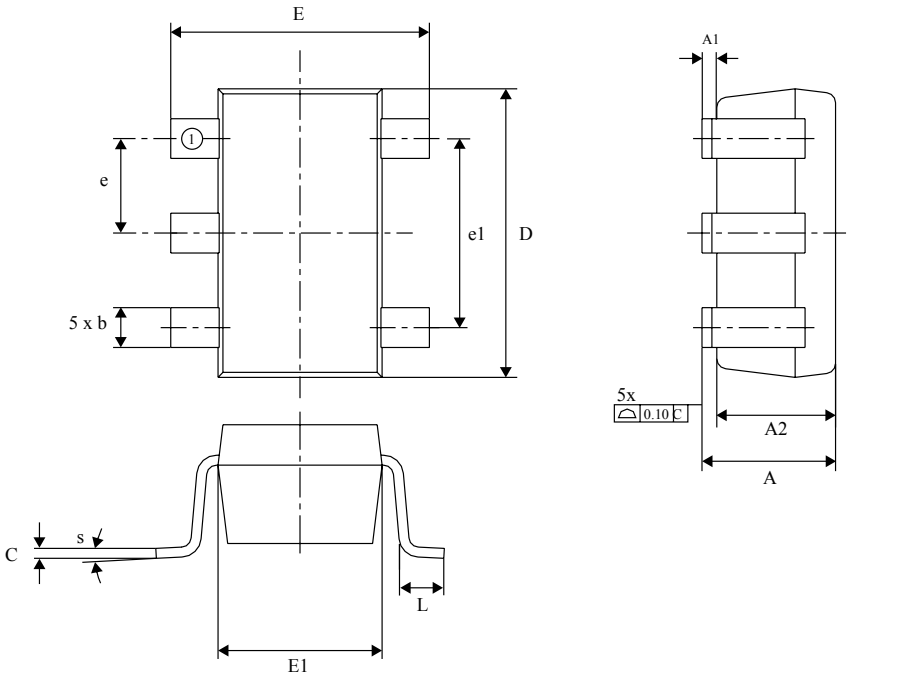
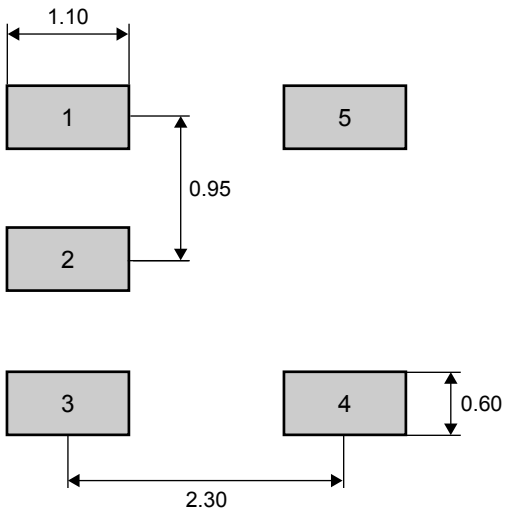


Table 9. **SOT23-5 mechanical data**

Symbol	Milimeters			Inches[1]		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			1.45			0.057
A1	0.00		0.15	0.000		0.006
A2	0.90	1.15	1.30	0.035	0.045	0.051
b	0.30		0.50	0.012		0.020
c	0.08		0.22	0.003		0.009
D		2.90			0.114	
E		2.80			0.110	
E1		1.60			0.063	
e		0.95			0.037	
e1		1.90			0.075	
L	0.30	0.45	0.60	0.012	0.018	0.024
θ	0	4	8	0	4	8

1. *Values in inches are converted from mm and rounded to 4 decimal digits.*

Figure 43. **SOT23-5 recommended footprint**



6.3 DFN8 2x2 package information

Figure 44. **DFN8 2x2 package outline**

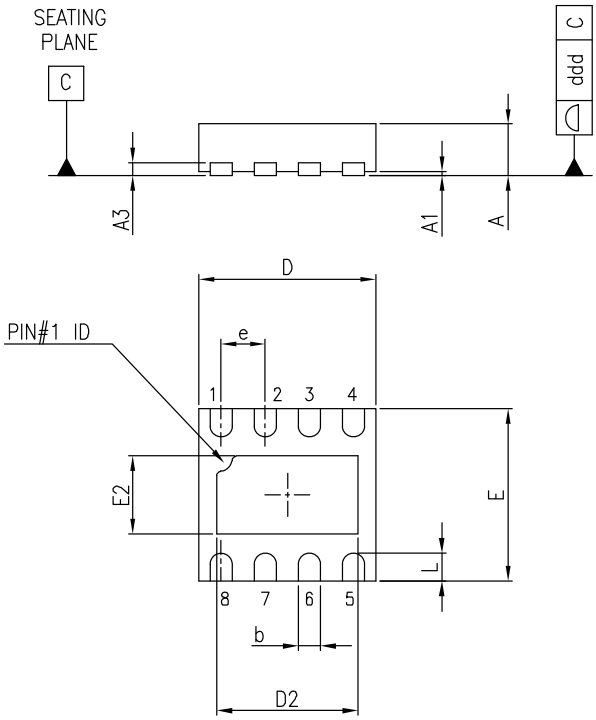
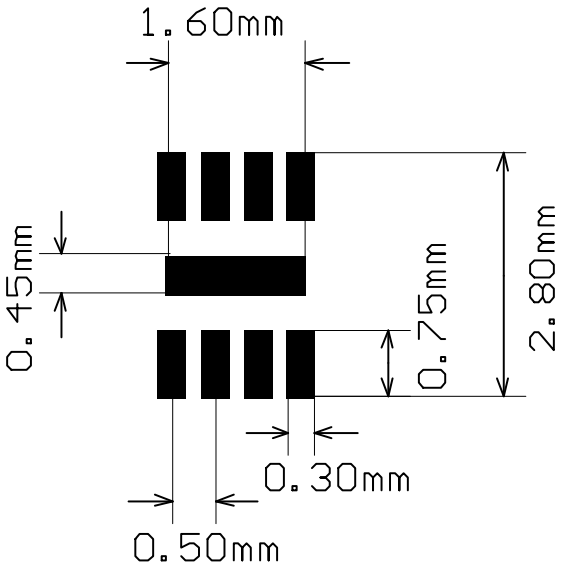


Table 10. **DFN8 2x2 package mechanical data**

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.51	0.55	0.60	0.020	0.022	0.024
A1			0.05			0.002
A3		0.15			0.006	
b	0.18	0.25	0.30	0.007	0.010	0.012
D	1.85	2.00	2.15	0.073	0.079	0.085
D2	1.45	1.60	1.70	0.057	0.063	0.067
E	1.85	2.00	2.15	0.073	0.079	0.085
E2	0.75	0.90	1.00	0.030	0.035	0.039
e		0.50			0.020	
L	0.225	0.325	0.425	0.009	0.013	0.017
ddd			0.08			0.003

Figure 45. **DFN8 2x2 recommended footprint**



Note: The exposed pad of the DFN8 2x2 can be connected to VCC- or left floating.

6.4 MiniSO8 package information

Figure 46. **MiniSO8 package outline**

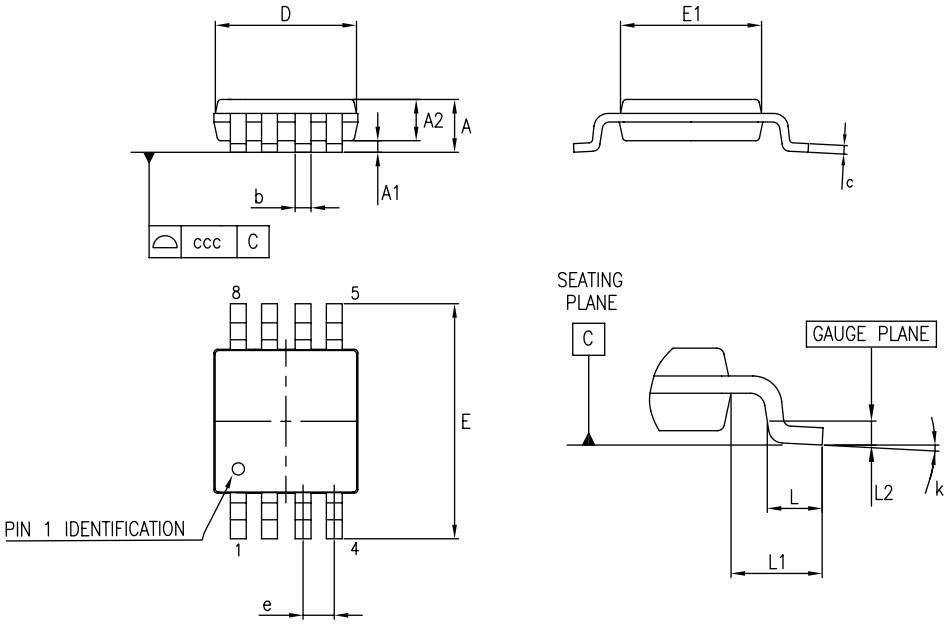


Table 11. **MiniSO8 mechanical data**

Dim.	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			1.1			0.043
A1	0		0.15	0		0.006
A2	0.75	0.85	0.95	0.03	0.033	0.037
b	0.22		0.4	0.009		0.016
c	0.08		0.23	0.003		0.009
D	2.8	3	3.2	0.11	0.118	0.126
E	4.65	4.9	5.15	0.183	0.193	0.203
E1	2.8	3	3.1	0.11	0.118	0.122
e		0.65			0.026	
L	0.4	0.6	0.8	0.016	0.024	0.031
L1		0.95			0.037	
L2		0.25			0.01	
k	0°		8°	0°		8°
ccc			0.1			0.004

Revision history

Table 12. Document revision history

Date	Version	Changes
16-Apr-2019	1	Initial release.
24-May-2019	2	Updated Table 4. Electrical characteristics V_{CC} = +1.2 V, T_{amb} = 25 °C, V_{ICM} = V_{CC}/2 (unless otherwise specified), Table 5. Electrical characteristics V_{CC} = +2.7 V, T_{amb} = 25 °C, V_{ICM} = V_{CC}/2 (unless otherwise specified) and Table 6. Electrical characteristics V_{CC} = +5 V, T_{amb} = 25 °C, V_{ICM} = V_{CC}/2 (unless otherwise specified).

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