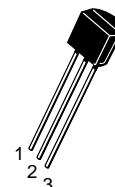
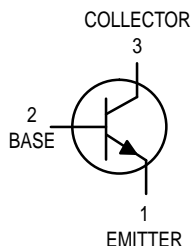


Switching Transistors

NPN Silicon

MPS2369 MPS2369A*

*Motorola Preferred Device



CASE 29-04, STYLE 1
TO-92 (TO-226AA)

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V_{CEO}	15	Vdc
Collector–Emitter Voltage	V_{CES}	40	Vdc
Collector–Base Voltage	V_{CBO}	40	Vdc
Emitter–Base Voltage	V_{EBO}	4.5	Vdc
Collector Current — Continuous	I_C	200	mA _{dc}
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	625 5.0	mW mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	–55 to +150	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage ⁽¹⁾ ($I_C = 10 \text{ mA}_{dc}, I_B = 0$)	$V_{(BR)CEO}$	15	—	—	Vdc
Collector–Emitter Breakdown Voltage ($I_C = 10 \mu\text{A}_{dc}, V_{BE} = 0$)	$V_{(BR)CES}$	40	—	—	Vdc
Collector–Base Breakdown Voltage ($I_C = 10 \mu\text{A}_{dc}, I_E = 0$)	$V_{(BR)CBO}$	40	—	—	Vdc
Emitter–Base Breakdown Voltage ($I_E = 10 \mu\text{A}_{dc}, I_C = 0$)	$V_{(BR)EBO}$	4.5	—	—	Vdc
Collector Cutoff Current ($V_{CB} = 20 \text{ Vdc}, I_E = 0$) ($V_{CB} = 20 \text{ Vdc}, I_E = 0, T_A = 125^\circ\text{C}$)	I_{CBO}	— —	— —	0.4 30	μA_{dc}
Collector Cutoff Current ($V_{CE} = 20 \text{ Vdc}, V_{BE} = 0$)	I_{CES}	—	—	0.4	μA_{dc}

1. Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $\leq 2.0\%$.

Preferred devices are Motorola recommended choices for future use and best overall value.

MPS2369 MPS2369A

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Typ	Max	Unit
ON CHARACTERISTICS					
DC Current Gain ⁽¹⁾ (I _C = 10 mAdc, V _{CE} = 1.0 Vdc) (I _C = 10 mAdc, V _{CE} = 1.0 Vdc, T _A = -55°C) (I _C = 10 mAdc, V _{CE} = 1.0 Vdc) (I _C = 10 mAdc, V _{CE} = 0.35 Vdc) (I _C = 10 mAdc, V _{CE} = 0.35 Vdc, T _A = -55°C) (I _C = 30 mAdc, V _{CE} = 0.4 Vdc) (I _C = 100 mAdc, V _{CE} = 2.0 Vdc) (I _C = 100 mAdc, V _{CE} = 1.0 Vdc)	h _{FE} MPS2369A MPS2369 MPS2369 MPS2369A MPS2369A MPS2369A MPS2369 MPS2369A	— 20 40 40 20 30 20 20	— — — — — — — —	120 — 120 — — — — —	—
Collector–Emitter Saturation Voltage ⁽¹⁾ (I _C = 10 mAdc, I _B = 1.0 mAdc) (I _C = 10 mAdc, I _B = 1.0 mAdc) (I _C = 10 mAdc, I _B = 1.0 mAdc, T _A = +125°C) (I _C = 30 mAdc, I _B = 3.0 mAdc) (I _C = 100 mAdc, I _B = 10 mAdc)	V _{CE(sat)} MPS2369 MPS2369A MPS2369A MPS2369A MPS2369A	— — — — —	— — — — —	0.25 0.20 0.30 0.25 0.50	Vdc
Base–Emitter Saturation Voltage ⁽¹⁾ (I _C = 10 mAdc, I _B = 1.0 mAdc) (I _C = 10 mAdc, I _B = 1.0 mAdc, T _A = +125°C) (I _C = 10 mAdc, I _B = 1.0 mAdc, T _A = -55°C) (I _C = 30 mAdc, I _B = 3.0 mAdc) (I _C = 100 mAdc, I _B = 10 mAdc)	V _{BE(sat)} MPS2369 MPS2369A MPS2369A MPS2369A MPS2369A	0.7 0.5 — — —	— — — — —	0.85 — 1.02 1.15 1.60	Vdc

SMALL–SIGNAL CHARACTERISTICS

Output Capacitance (V _{CB} = 5.0 Vdc, I _E = 0, f = 1.0 MHz)	MPS2369,A	C _{obo}	—	—	4.0	pF
Small–Signal Current Gain (I _C = 10 mAdc, V _{CE} = 10 Vdc, f = 100 MHz)	MPS2369,A	h _{fe}	5.0	—	—	—

SWITCHING CHARACTERISTICS

Storage Time (I _{B1} = I _{B2} = I _C = 10 mAdc) (Figure 3)	MPS2369,A	t _s	—	5.0	13	ns
Turn–On Time (V _{CC} = 3.0 Vdc, I _C = 10 mAdc, I _{B1} = 3.0 mAdc) (Figure 1)	MPS2369,A	t _{on}	—	8.0	12	ns
Turn–Off Time (V _{CC} = 3.0 Vdc, I _C = 10 mAdc, I _{B1} = 3.0 mAdc, I _{B2} = 1.5 mAdc) (Figure 2)	MPS2369,A	t _{off}	—	10	18	ns

1. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.

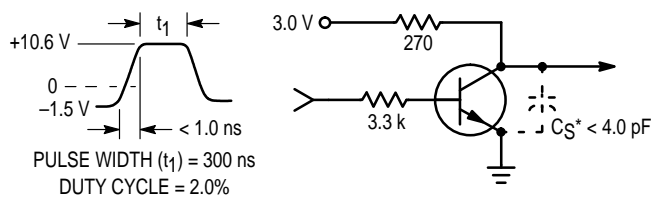
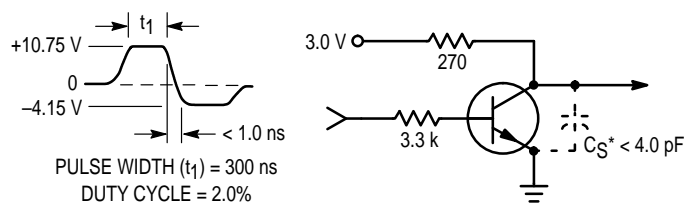
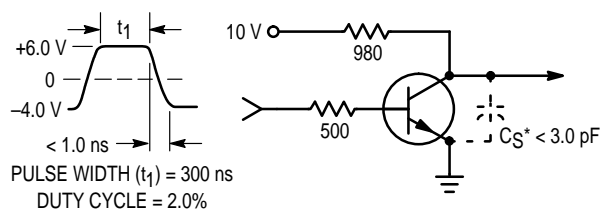
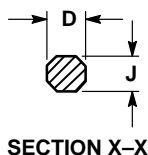
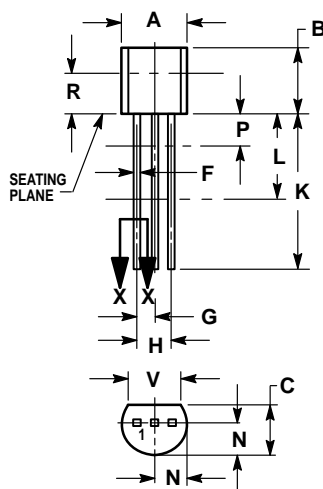
Figure 1. t_{on} CircuitFigure 2. t_{off} Circuit

Figure 3. Storage Test Circuit

* Total shunt capacitance of test jig and connectors.

PACKAGE DIMENSIONS



**CASE 029-04
(TO-226AA)
ISSUE AD**


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. DIMENSION F APPLIES BETWEEN P AND L. DIMENSION D AND J APPLY BETWEEN L AND K. MINIMUM LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.175	0.205	4.45	5.20
B	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.022	0.41	0.55
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
H	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500	—	12.70	—
L	0.250	—	6.35	—
N	0.080	0.105	2.04	2.66
P	—	0.100	—	2.54
R	0.115	—	2.93	—
V	0.135	—	3.43	—

STYLE 1:

1. PIN 1. EMITTER
2. BASE
3. COLLECTOR

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