

μ A747 ✓

DUAL FREQUENCY-COMPENSATED OPERATIONAL AMPLIFIER FAIRCHILD LINEAR INTEGRATED CIRCUITS

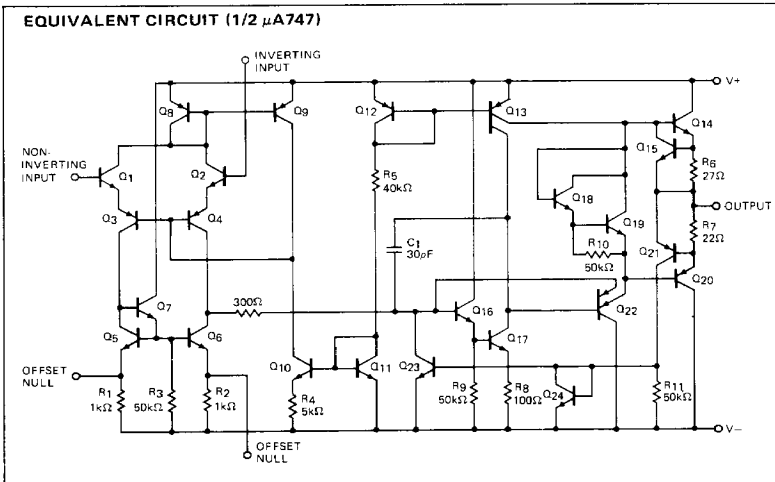
GENERAL DESCRIPTION — The μ A747 is a pair of high performance monolithic Operational Amplifiers constructed using the Fairchild Planar* epitaxial process. They are intended for a wide range of analog applications where board space or weight are important. High common mode voltage range and absence of latch-up make the μ A747 ideal for use as a voltage follower. The high gain and wide range of operating voltage provides superior performance in integrator, summing amplifier, and general feedback applications. The μ A747 is short circuit protected and requires no external components for frequency compensation. The internal 6 dB/octave roll-off insures stability in closed loop applications. For single amplifier performance, see μ A741 data sheet.

- NO FREQUENCY COMPENSATION REQUIRED
- SHORT CIRCUIT PROTECTION
- OFFSET VOLTAGE NULL CAPABILITY
- LARGE COMMON MODE AND DIFFERENTIAL VOLTAGE RANGES
- LOW POWER CONSUMPTION
- NO LATCH-UP

ABSOLUTE MAXIMUM RATINGS

Supply Voltage		
Military (μ A747A, μ A747, μ A747E)	A 4.0	± 22 V
Commercial (μ A747C)	E 4.0	± 18 V
Internal Power Dissipation (Note 1)	C 7.5	
Metal Can		500 mW
DIP		670 mW
Differential Input Voltage	Planar 6.0	± 30 V
Input Voltage (Note 2)		± 15 V
Voltage between Offset Null and V—		± 0.5 V
Storage Temperature Range		-65°C to $+150^{\circ}\text{C}$
Operating Temperature Range		
Military (μ A747A, μ A747)		-55°C to $+125^{\circ}\text{C}$
Commercial (μ A747E, μ A747C)		0°C to 70°C
Pin Temperature (Soldering 60 s)		300°C
Output Short Circuit Duration (Note 3)		Indefinite

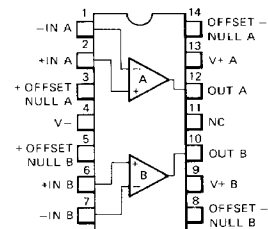
EQUIVALENT CIRCUIT (1/2 μ A747)



Notes on following pages.

CONNECTION DIAGRAMS 14-PIN DIP (TOP VIEW)

PACKAGE OUTLINE 7A 9A
PACKAGE CODE D P

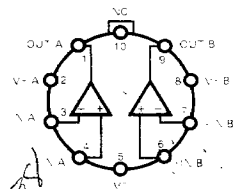


ORDER INFORMATION

TYPE	PART NO.
μ A747A	μ A747ADM ✓
μ A747	μ A747DM ✓
μ A747E	μ A747EDC ✓
μ A747C	μ A747DC ✓
μ A747C	μ A747EC ✓
μ A747-I	μ A747-IDM
μ A747-IC	μ A747-IDC

10-PIN METAL CAN (TOP VIEW)

PACKAGE OUTLINE 5N
PACKAGE CODE H



ORDER INFORMATION

TYPE	PART NO.
μ A747A	μ A747AHM ✓
μ A747	μ A747HM ✓
μ A747E	μ A747EHC ✓
μ A747C	μ A747HC ✓
μ A747-I	μ A747-IHM
μ A747-IC	μ A747-IHC

NOTE:

V+A is internally connected to V+B for μ A747A, μ A747, μ A747E, and μ A747C. They are not internally connected for μ A747-I and μ A747-IC.

*Planar is a patented Fairchild process.

$\mu A747A$ **ELECTRICAL CHARACTERISTICS:** $\pm 5\text{ V} \leq V_S \leq \pm 20\text{ V}$, $T_A = 25^\circ\text{C}$ unless otherwise specified.

CHARACTERISTICS		CONDITIONS	MIN	TYP	MAX	UNITS
Input Offset Voltage		$R_S \leq 50\,\Omega$		0.8	3.0	mV
Average Input Offset Voltage Drift					15	$\mu\text{V}/^\circ\text{C}$
Input Offset Current				3.0	30	nA
Average Input Offset Current Drift		$T_A = 25^\circ\text{C}$ to $+125^\circ\text{C}$ $T_A = -55^\circ\text{C}$ to $+25^\circ\text{C}$			0.2 0.5	nA/ $^\circ\text{C}$ nA/ $^\circ\text{C}$
Input Bias Current				30	80	nA
Power Supply Rejection Ratio		$V_S = +10$ to $+20$, -20 ; $V_S = +20$, -10 to -20 $R_S = 50\,\Omega$		15	50	$\mu\text{V}/\text{V}$
Common Mode Rejection Ratio		$V_S = \pm 20\,\text{V}$, $V_{IN} = \pm 15\,\text{V}$ $R_S = 50\,\Omega$	80	95		dB
Adjustment for Input Offset Voltage		$V_S = \pm 20\,\text{V}$	10			mV
Output Short Circuit Current			10	25	40	mA
Power Dissipation		$V_S = \pm 20\,\text{V}$ per Channel		80	150	mW
Input Impedance		$V_S = \pm 20\,\text{V}$	1.0	6		M Ω
Large Signal Voltage Gain		$V_S = \pm 20\,\text{V}$, $R_L = 2\,\text{k}\Omega$ $V_{OUT} = \pm 15\,\text{V}$	50			V/mV
Transient Response (Unity Gain)	Rise Time			0.25	0.8	μs
	Overshoot			6.0	20	%
Bandwidth (Note 4)			0.437	1.5		MHz
Slew Rate (Unity Gain)		$V_{IN} = \pm 10\,\text{V}$	0.3	0.7		V/ μs
The following specifications apply for $-55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$						
Input Offset Voltage					4.0	mV
Input Offset Current					70	nA
Input Bias Current					210	nA
Output Short Circuit Current			10		40	mA
Power Dissipation	$V_S = \pm 20\,\text{V}$	-55°C			165	mW
		$+125^\circ\text{C}$			135	mW
Input Impedance		$V_S = \pm 20\,\text{V}$	0.5			M Ω
Output Voltage Swing		$V_S = \pm 20\,\text{V}$, $R_L = 10\,\text{k}\Omega$ $R_L = 2\,\text{k}\Omega$	± 16 ± 15			V V
Large Signal Voltage Gain		$V_S = \pm 20\,\text{V}$, $R_L = 2\,\text{k}\Omega$, $V_{OUT} = \pm 15\,\text{V}$	32			V/mV
		$V_S = \pm 5\,\text{V}$, $R_L = 2\,\text{k}\Omega$, $V_{OUT} = \pm 2\,\text{V}$	10			V/mV
Channel Separation		$V_S = \pm 20\,\text{V}$	100			dB

NOTES:

- Rating applies to ambient temperatures up to 70°C . Above 70°C ambient derate linearly at $6.3\text{ mW}/^\circ\text{C}$ for the Metal Can, $8.3\text{ mW}/^\circ\text{C}$ for the DIP.
- For supply voltages less than $\pm 15\text{ V}$, the absolute maximum input voltage is equal to the supply voltage.
- Short circuit may be to ground or either supply. Rating applies to $+125^\circ\text{C}$ case temperature or 75°C ambient temperature.
- Calculated value from: $\text{BW (MHz)} = \frac{0.35}{\text{RISE TIME } (\mu\text{s})}$

$\mu A747$ **ELECTRICAL CHARACTERISTICS:** Each Amplifier ($V_S = \pm 15$ V, $T_A = 25^\circ\text{C}$ unless otherwise specified)

CHARACTERISTICS (see definitions)	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Input Offset Voltage	$R_S \leq 10\text{ k}\Omega$		1.0	5.0	mV
Input Offset Current			20	200	nA
Input Bias Current			80	500	nA
Input Resistance		0.3	2.0		M Ω
Input Capacitance			1.4		pF
Offset Voltage Adjustment Range			± 15		mV
Large Signal Voltage Gain	$R_L \geq 2\text{ k}\Omega$, $V_{OUT} = \pm 10$ V	50,000	200,000		V/V
Output Resistance			75		Ω
Output Short-Circuit Current			25		mA
Supply Current			1.7	2.8	mA
Power Consumption			50	85	mW
Transient Response (Unity Gain)	Rise time	$V_{IN} = 20\text{ mV}$, $R_L = 2\text{ k}\Omega$, $C_L \leq 100\text{ pF}$	0.3		μs
	Overshoot		5.0		%
Slew Rate	$R_L \geq 2\text{ k}\Omega$		0.5		V/ μs
Channel Separation			120		dB

The following specifications apply for $-55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$.

Input Offset Voltage	$R_S \leq 10\text{ k}\Omega$		1.0	6.0	mV
Input Offset Current	$T_A = +125^\circ\text{C}$		7.0	200	nA
	$T_A = -55^\circ\text{C}$		85	500	nA
Input Bias Current	$T_A = +125^\circ\text{C}$		0.03	0.5	μA
	$T_A = -55^\circ\text{C}$		0.3	1.5	μA
Input Voltage Range		± 12	± 13		V
Common Mode Rejection Ratio	$R_S \leq 10\text{ k}\Omega$	70	90		dB
Supply Voltage Rejection Ratio	$R_S \leq 10\text{ k}\Omega$		30	150	$\mu\text{V/V}$
Large Signal Voltage Gain	$R_L \geq 2\text{ k}\Omega$, $V_{OUT} = \pm 10$ V	25,000			V/V
Output Voltage Swing	$R_L \geq 10\text{ k}\Omega$	± 12	± 14		V
	$R_L \geq 2\text{ k}\Omega$	± 10	± 13		V
Supply Current	$T_A = +125^\circ\text{C}$		1.5	2.5	mA
	$T_A = -55^\circ\text{C}$		2.0	3.3	mA
Power Consumption	$T_A = +125^\circ\text{C}$		45	75	mW
	$T_A = -55^\circ\text{C}$		60	100	mW

FAIRCHILD • μ A747

μ A747C

ELECTRICAL CHARACTERISTICS: Each Amplifier ($V_S = \pm 15$ V, $T_A = 25^\circ\text{C}$ unless otherwise specified)

CHARACTERISTICS (see definitions)		CONDITIONS	MIN	TYP	MAX	UNITS
Input Offset Voltage		$R_S \leq 10\text{ k}\Omega$		1.0	6.0	mV
Input Offset Current				20	200	nA
Input Bias Current				80	500	nA
Input Resistance			0.3	2.0		M Ω
Input Capacitance				1.4		pF
Offset Voltage Adjustment Range				± 15		mV
Large Signal Voltage Gain		$R_L \geq 2\text{ k}\Omega$, $V_{OUT} = \pm 10$ V	25,000	200,000		V/V
Output Resistance				75		Ω
Output Short-Circuit Current				25		mA
Supply Current				1.7	2.8	mA
Power Consumption				50	85	mW
Transient Response (Unity Gain)	Rise time	$V_{IN} = 20\text{ mV}$, $R_L = 2\text{ k}\Omega$, $C_L \leq 100\text{ pF}$		0.3		μ s
	Overshoot			5.0		%
Slew Rate		$R_L \geq 2\text{ k}\Omega$		0.5		V/ μ s
Channel Separation				120		dB

The following specifications apply for $0^\circ\text{C} \leq T_A \leq +70^\circ\text{C}$.

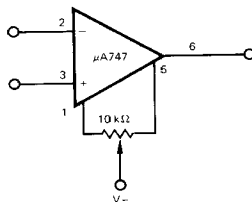
Input Offset Voltage		$R_S \leq 10\text{ k}\Omega$		1.0	7.5	mV
Input Offset Current				7.0	300	nA
Input Bias Current				0.03	0.8	μ A
Input Voltage Range			± 12	± 13		V
Common Mode Rejection Ratio		$R_S \leq 10\text{ k}\Omega$	70	90		dB
Supply Voltage Rejection Ratio		$R_S \leq 10\text{ k}\Omega$		30	150	μ V/V
Large Signal Voltage Gain		$R_L \geq 2\text{ k}\Omega$, $V_{OUT} = \pm 10$ V	15,000			V/V
Output Voltage Swing		$R_L \geq 10\text{ k}\Omega$	± 12	± 14		V
		$R_L \geq 2\text{ k}\Omega$	± 10	± 13		V
Supply Current				2.0	3.3	mA
Power Consumption				60	100	mW

$\mu A747E$

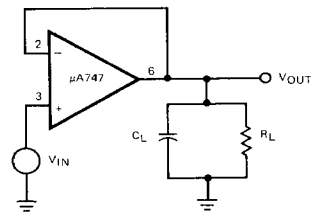
ELECTRICAL CHARACTERISTICS: $\pm 5 \text{ V} \leq V_S \leq \pm 20 \text{ V}$, $T_A = 25^\circ\text{C}$ unless otherwise specified.

CHARACTERISTICS (see definitions)	CONDITIONS	MIN	TYP	MAX	UNITS
Input Offset Voltage	$R_S \leq 50 \Omega$		0.8	3.0	mV
Average Input Offset Voltage Drift				15	$\mu\text{V}/^\circ\text{C}$
Input Offset Current			3	30	nA
Average Input Offset Current Drift	$T_A = 25^\circ\text{C}$ to 70°C $T_A = 0^\circ\text{C}$ to 25°C			0.2 0.5	$\text{nA}/^\circ\text{C}$ $\text{nA}/^\circ\text{C}$
Input Bias Current			30	80	nA
Power Supply Rejection Ratio	$V_S = +10, -20; V_S = +20 \text{ V}, -10 \text{ V}$ $R_S = 50 \Omega$		15	50	$\mu\text{V}/\text{V}$
Common Mode Rejection Ratio	$V_S = \pm 20 \text{ V}$, $V_{IN} = \pm 15 \text{ V}$ $R_S = 50 \Omega$	80	95		dB
Adjustment for Input Offset Voltage	$V_S = \pm 20 \text{ V}$	10			mV
Output Short Circuit Current		10	25	35	mA
Power Dissipation	$V_S = \pm 20 \text{ V}$		80	150	mW
Input Impedance	$V_S = \pm 20 \text{ V}$	1.0	6		$\text{M}\Omega$
Large Signal Voltage Gain	$V_S = \pm 20 \text{ V}$, $R_L = 2 \text{ k}\Omega$, $V_{OUT} = \pm 15 \text{ V}$	50			V/mV
Transient Response (Unity Gain)	Rise Time		0.25	0.8	μs
	Overshoot		6	20	%
Bandwidth (Note 4)		0.437	1.5		MHz
Slew Rate (Unity Gain)	$V_{IN} = \pm 10 \text{ V}$	0.3	0.7		$\text{V}/\mu\text{s}$
The following specifications apply for $0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$					
Input Offset Voltage				4.0	mV
Input Offset Current				70	nA
Input Bias Current				210	nA
Output Short Circuit Current		10		40	mA
Power Dissipation	$V_S = \pm 20 \text{ V}$			165	mW
Input Impedance	$V_S = \pm 20 \text{ V}$	0.5			$\text{M}\Omega$
Output Voltage Swing	$V_S = \pm 20 \text{ V}$, $R_L = 10 \text{ k}\Omega$	± 16			V
	$R_L = 2 \text{ k}\Omega$	± 15			V
Large Signal Voltage Gain	$V_S = \pm 20 \text{ V}$, $R_L = 2 \text{ k}\Omega$, $V_{OUT} = \pm 15 \text{ V}$	32			V/mV
	$V_S = \pm 5 \text{ V}$, $R_L = 2 \text{ k}\Omega$, $V_{OUT} = \pm 2 \text{ V}$	10			V/mV
Channel Separation	$V_S = \pm 20 \text{ V}$	100			dB

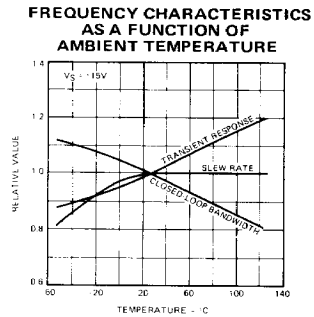
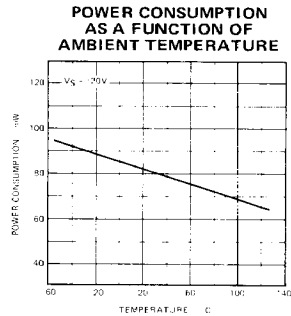
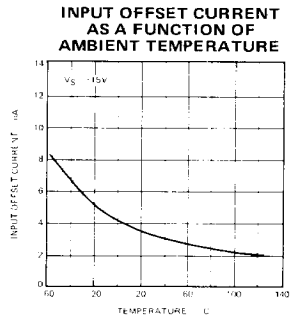
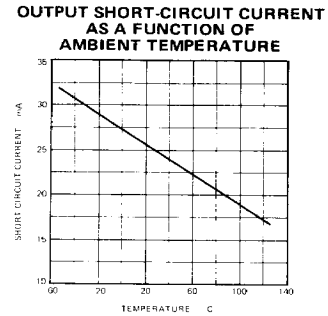
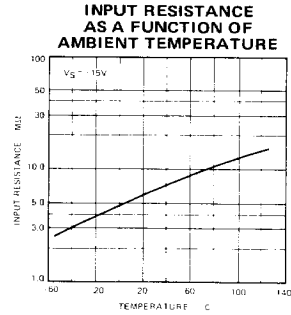
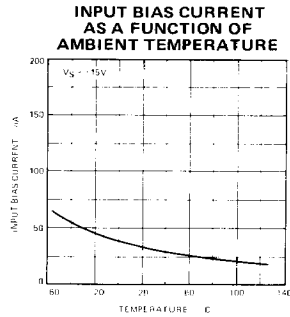
VOLTAGE OFFSET NULL CIRCUIT



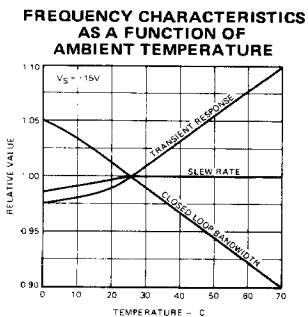
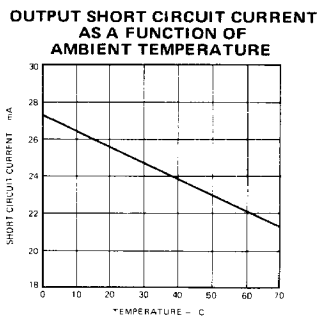
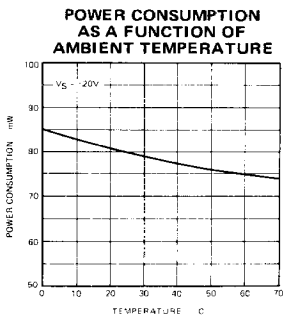
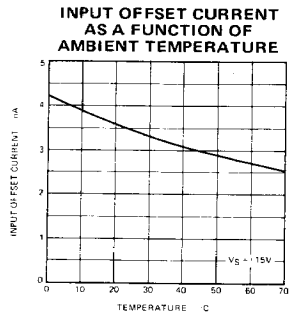
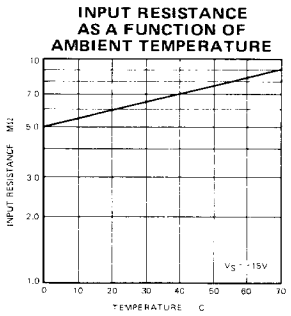
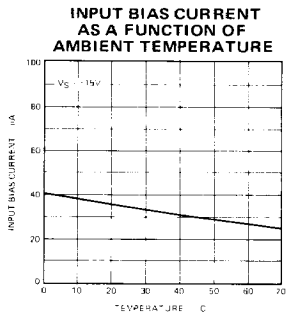
TRANSIENT RESPONSE TEST CIRCUIT



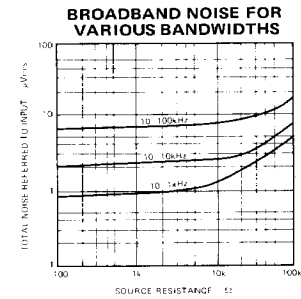
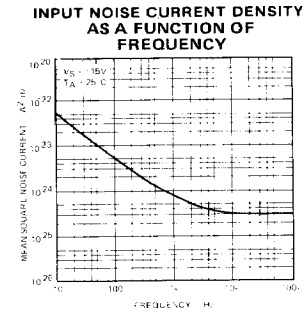
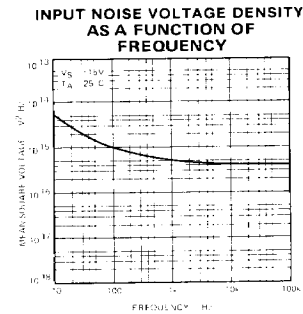
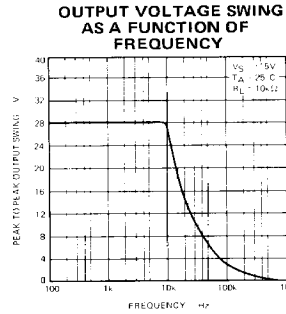
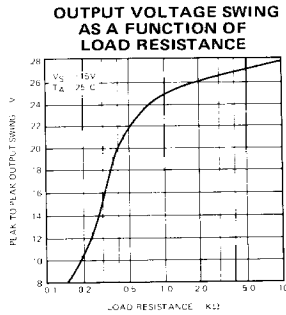
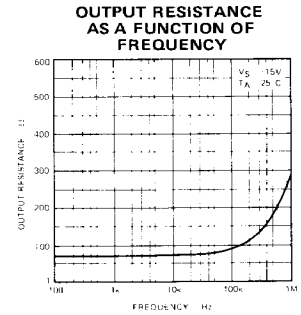
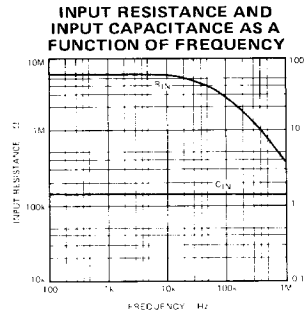
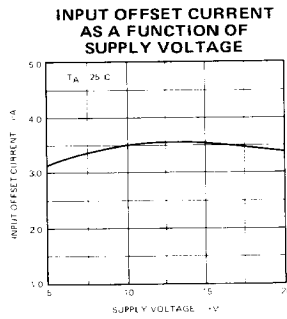
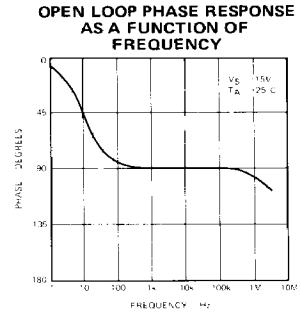
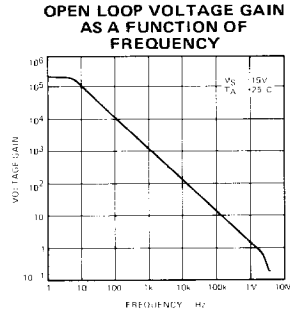
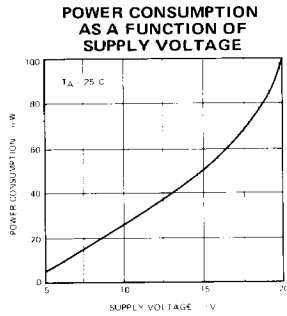
TYPICAL PERFORMANCE CURVES FOR $\mu A747A$ AND $\mu A747$

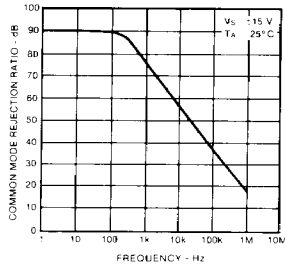


TYPICAL PERFORMANCE CURVES FOR $\mu A747E$ AND $\mu A747C$

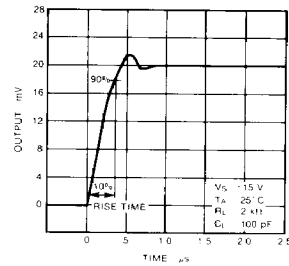
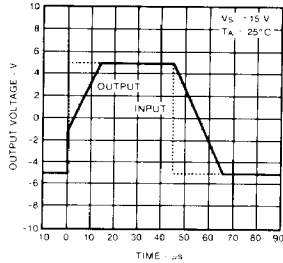
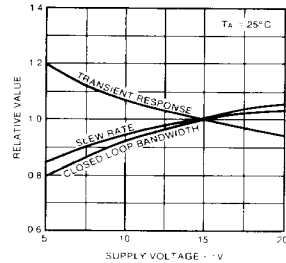


TYPICAL PERFORMANCE CURVES FOR $\mu A747A$, $\mu A747C$, $\mu A747$ AND $\mu A747E$



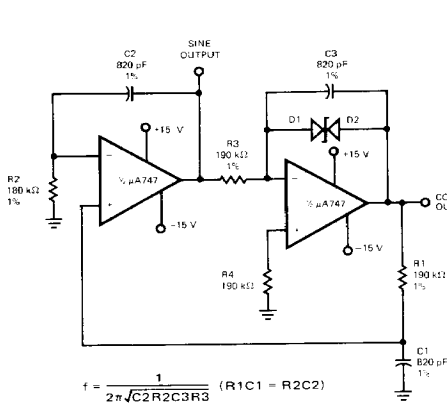
TYPICAL PERFORMANCE CURVES (Each Amplifier) FOR $\mu A747$ AND $\mu A747C$ COMMON MODE REJECTION
RATIO AS A FUNCTION OF
FREQUENCY

TRANSIENT RESPONSE

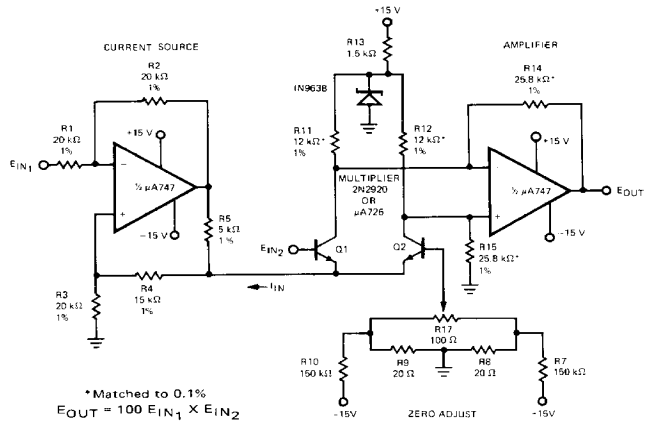
VOLTAGE FOLLOWER
LARGE SIGNAL PULSE RESPONSEFREQUENCY CHARACTERISTICS
AS A FUNCTION OF
SUPPLY VOLTAGE

TYPICAL APPLICATIONS

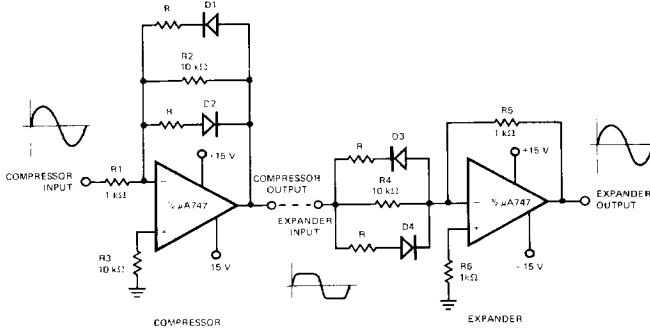
QUADRATURE OSCILLATOR



ANALOG MULTIPLIER

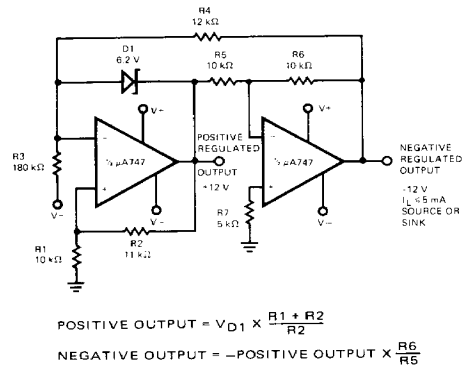


COMPRESSOR/EXPANDER AMPLIFIERS

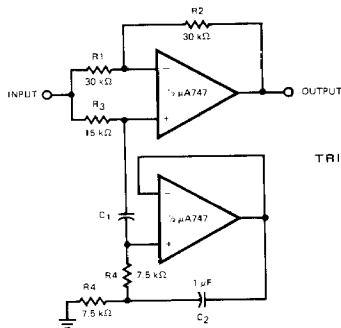


MAXIMUM COMPRESSION EXPANSION RATIO = R_1/R ($10 \text{ k}\Omega > R > 0$)
NOTE: DIODES D1 THROUGH D4 ARE MATCHED FD666 OR EQUIVALENT

TRACKING POSITIVE AND NEGATIVE VOLTAGE REFERENCES

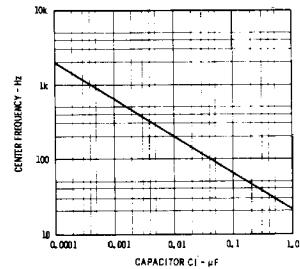


NOTCH FILTER USING THE $\mu A747$ AS A GYRATOR



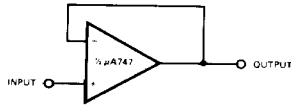
TRIM R_3 SUCH THAT
 $\frac{R_1}{R_2} = \frac{R_3}{2 R_4}$

NOTCH FREQUENCY AS A FUNCTION OF C_1



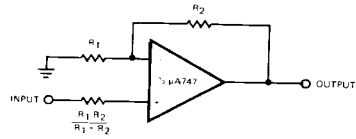
TYPICAL APPLICATIONS

UNITY-GAIN VOLTAGE FOLLOWER



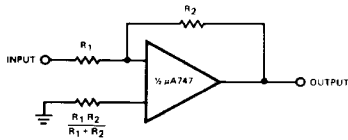
$R_{IN} = 400 \text{ M}\Omega$
 $C_{IN} = 1 \text{ pF}$
 $R_{OUT} < 1 \Omega$
 $BW = 1 \text{ MHz}$

NON-INVERTING AMPLIFIER



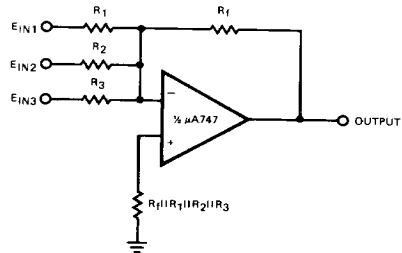
GAIN	R_1	R_2	B.W.	R_{IN}
10	1 k Ω	9 k Ω	100 kHz	400 M Ω
100	100 Ω	9.9 k Ω	10 kHz	280 M Ω
1000	100 Ω	99.9 k Ω	1 kHz	80 M Ω

INVERTING AMPLIFIER



GAIN	R_1	R_2	B.W.	R_{IN}
1	10 k Ω	10 k Ω	1 MHz	10 k Ω
10	1 k Ω	10 k Ω	100 kHz	1 k Ω
100	1 k Ω	100 k Ω	10 kHz	1 k Ω
1000	100 Ω	100 k Ω	1 kHz	100 Ω

WEIGHTED AVERAGING AMPLIFIER



$$-E_{OUT} = E_{IN1} \left(\frac{R_f}{R_1} \right) + E_{IN2} \left(\frac{R_f}{R_2} \right) + E_{IN3} \left(\frac{R_f}{R_3} \right)$$