

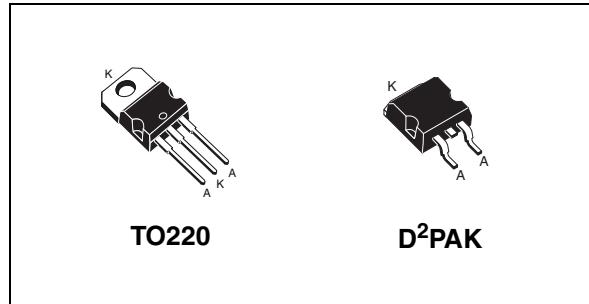
## Cool bypass switch for photovoltaic applications

### Features

- $I_F = 16 \text{ A}$ ,  $V_R = 40 \text{ V}$
- Very low forward voltage drop
- Very low reverse leakage current
- $175^\circ\text{C}$  operating junction temperature

### Applications

- Photovoltaic panels



### Description

The SPV1001 is a system-in-package solution for photovoltaic applications to perform cool bypass rectification similar to that of a conventional Schottky diode but with much lower forward voltage drop and reverse leakage current.

The device consists of a power MOSFET transistor which charges a capacitor during the OFF time, and drives its gate during the ON time using the charge previously stored in the capacitor.

The ON and OFF times are set to reduce the average voltage drop across the drain and source terminals, resulting in reduced power dissipation.

**Table 1. Device summary**

Order codes	Package	Packaging
SPV1001D40	D <sup>2</sup> PACK	Tube
SPV1001D40TR		Tape and reel
SPV1001T40	TO220	Tube

# 1 Maximum ratings

## 1.1 Absolute maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Range [min, max]	Unit
$V_R$	Max DC reverse voltage	40	V
$I_F$	Max. forward current	16	A
$I_{FSM}$	Non repetitive peak surge (half-wave, single-phase, 60 Hz)	250	A
ESD level	Human body level	$\geq 8\text{ k}$	V

## 1.2 Thermal data

Table 3. Thermal data

Symbol	Parameter	Value	Unit
$R_{thJC}$	Thermal resistance, junction-to-ambient (D <sup>2</sup> PAK, TO220)	1.5	°C/W
$T_J$	Junction temperature operating range	-40 to 175	°C
$T_{STG}$	Storage temperature range	-40 to 175	°C

## 2 Electrical characteristics

**Table 4. Electrical characteristics**

Symbol	Parameter	Test condition		Values			Unit
				Min	Typ	Max	
$V_{F,AVG}$	AVG forward voltage drop	IF = 16A <sup>(1)</sup>	$T_J = 25^\circ\text{C}$	-	230	-	mV
		IF = 8A <sup>(1)</sup>	$T_J = 25^\circ\text{C}$	-	120	-	mV
			$T_J = 125^\circ\text{C}$	-	270	-	mV
$I_R$	Reverse leakage current	$VR = 40\text{V}$	$T_J = 25^\circ\text{C}$	-	1	-	$\mu\text{A}$
			$T_J = 125^\circ\text{C}$	-	10	-	$\mu\text{A}$
D	TON/T ratio	IF = 8A <sup>(1)</sup>	$T_J = 25^\circ\text{C}$	-	95%	-	-
			$T_J = 125^\circ\text{C}$	-	75%	-	-
$V_F$	Forward voltage drop	IF = 8A, $T_{OFF}$	$T_J = 25^\circ\text{C}$	-	920	-	mV
			$T_J = 125^\circ\text{C}$	-	600	-	mV
		IF = 8A <sup>(1)</sup> , $T_{ON}$	$T_J = 25^\circ\text{C}$	-	70	-	mV
			$T_J = 125^\circ\text{C}$	-	160	-	mV

1. For correct power dissipation and heatsink sizing, please refer to [Figure 1, 4 e 7](#)

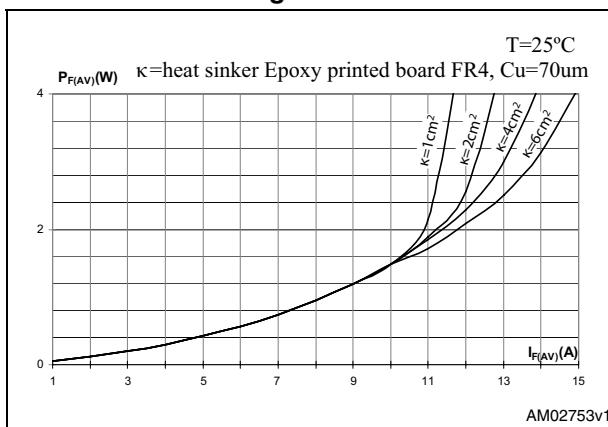
### 3 Device description

A photovoltaic panel consists of a series of PV cells. In optimal conditions, all the cells are equally irradiated and function at the same current level. However, during normal operation some cells may become partially shaded or obscured. These shaded cells limit the current generated by the fully irradiated cells and, in the extreme cases where these cells are totally obscured, the current flow is blocked.

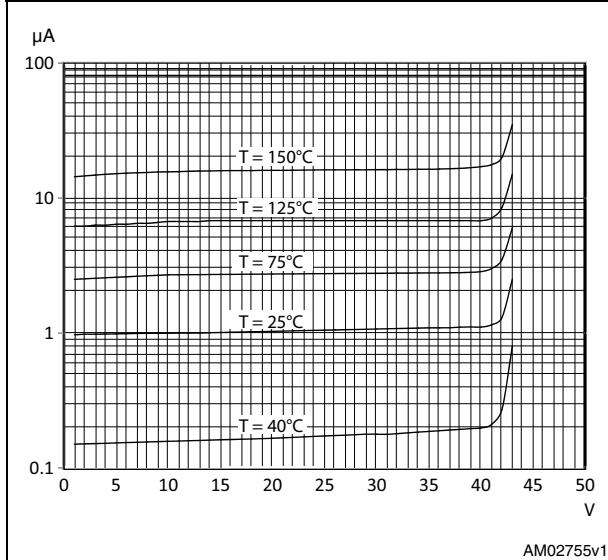
In this case the shaded cells behave like a load, and the current generated from the fully irradiated cells produces overvoltages which can reach the breakdown threshold. This phenomenon, known as a “hot spot”, can cause overheating of the shaded cells and, in some cases, even permanent damage resulting in current leakage. To prevent hot spots, therefore, bypass diodes are connected in parallel to the cell strings.

The device described here has the same functionality as a Schottky diode, but with improved performance. It features very low forward voltage drop and reverse leakage current. It consists of a power MOSFET transistor which charges a capacitor during the OFF time, and drives its gate during the ON time using the charge previously stored in the capacitor. The ON and OFF times are set to reduce the average voltage drop across the drain and source terminals, resulting in reduced power dissipation.

**Figure 1. Average forward power dissipation vs average forward current**

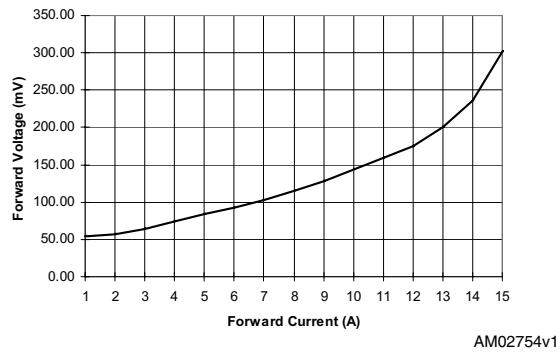


**Figure 3. Reverse current**

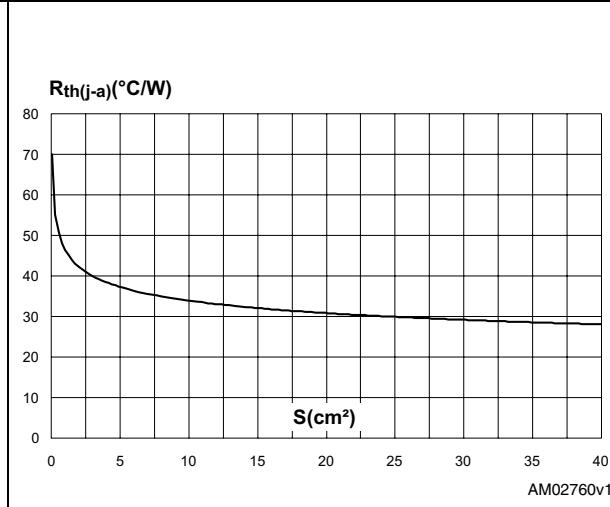


1. Epoxy printed board FR4, Cu = 35  $\mu\text{m}$

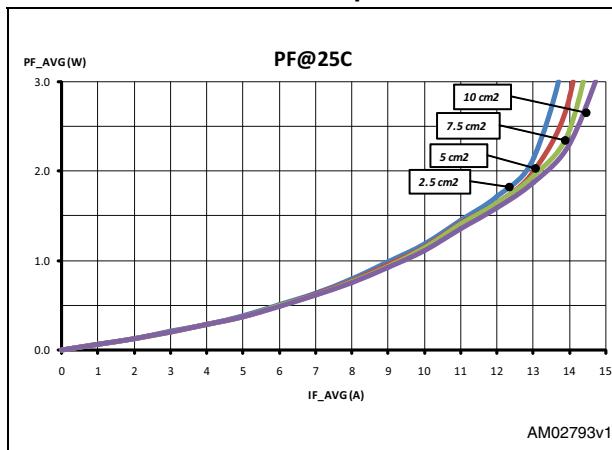
**Figure 2. Forward voltage vs average forward current**



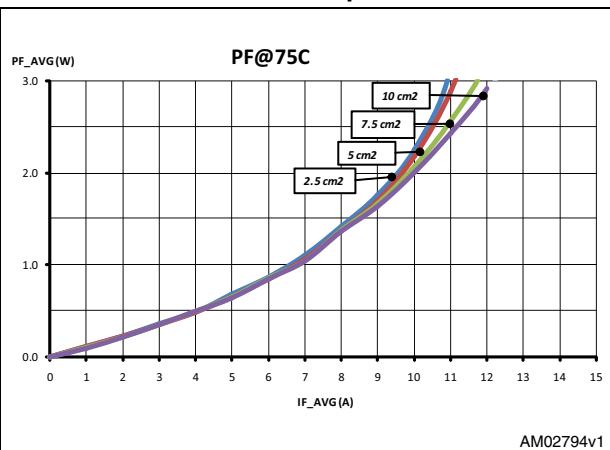
**Figure 4. Thermal resistance junction-to-ambient vs copper surface under tab (1)**



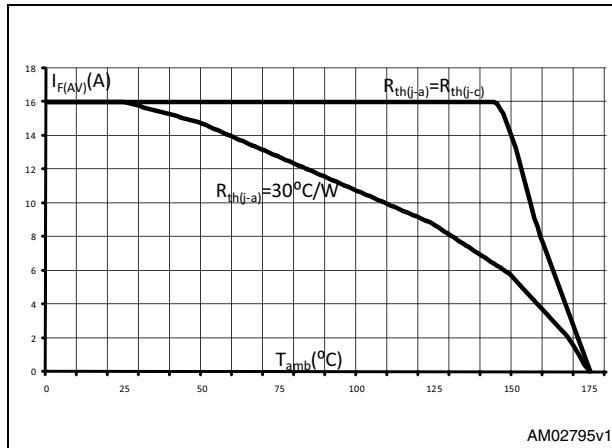
**Figure 5. Average forward power dissipation vs average forward current @ 25°C of ambient temperature**



**Figure 6. Average forward power dissipation vs average forward current @ 75°C of ambient temperature**



**Figure 7. Average current versus ambient temperature**



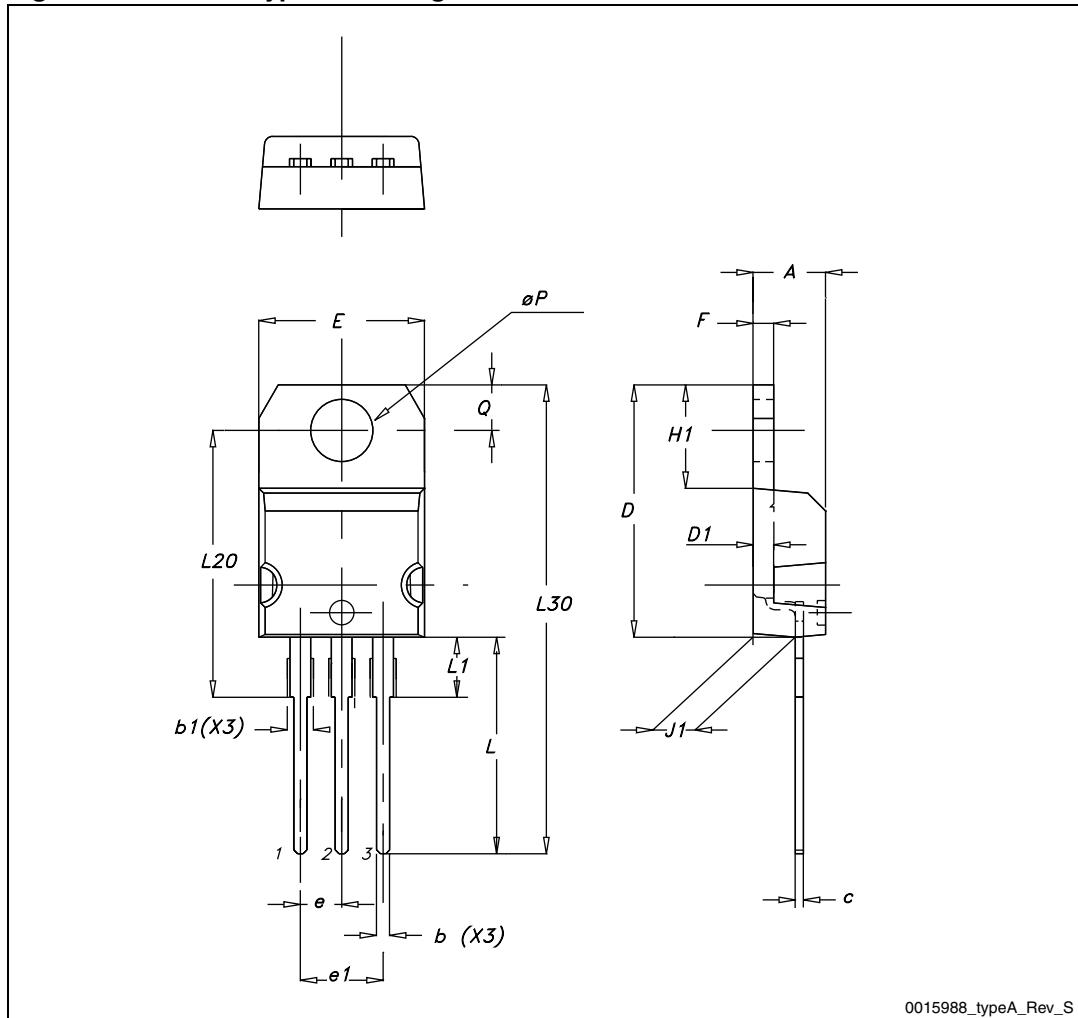
## 4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com).  
ECOPACK® is an ST trademark.

**Table 5. TO-220 type A mechanical data**

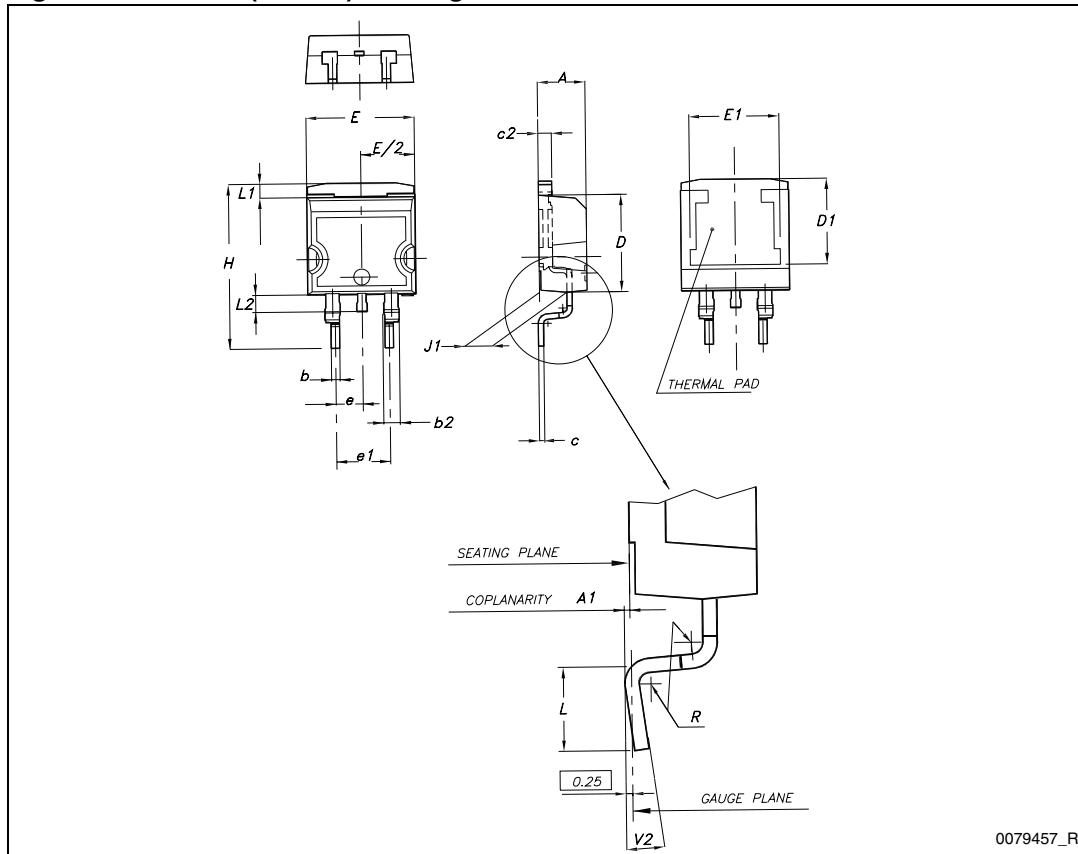
Dim.	mm		
	Min.	Typ.	Max.
A	4.40		4.60
b	0.61		0.88
b1	1.14		1.70
c	0.48		0.70
D	15.25		15.75
D1		1.27	
E	10		10.40
e	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13		14
L1	3.50		3.93
L20		16.40	
L30		28.90	
ØP	3.75		3.85
Q	2.65		2.95

Figure 8. TO-220 type A drawing



**Table 6. D<sup>2</sup>PAK (TO-263) mechanical data**

Dim.	mm		
	Min.	Typ.	Max.
A	4.40		4.60
A1	0.03		0.23
b	0.70		0.93
b2	1.14		1.70
c	0.45		0.60
c2	1.23		1.36
D	8.95		9.35
D1	7.50		
E	10		10.40
E1	8.50		
e		2.54	
e1	4.88		5.28
H	15		15.85
J1	2.49		2.69
L	2.29		2.79
L1	1.27		1.40
L2	1.30		1.75
R		0.4	
V2	0°		8°

Figure 9. D<sup>2</sup>PAK (TO-263) drawing

## 5 Revision history

**Table 7. Document revision history**

Date	Revision	Changes
30-Mar-2011	1	Initial release
14-Jun-2011	2	<ul style="list-style-type: none"><li>– Added D<sup>2</sup>PAK package</li><li>– Document status promoted from preliminary data to full datasheet</li><li>– Part number in title changed to SPV1001</li><li>– Minor text changes</li></ul>
11-Nov-2011	3	Updated <i>Figure 3</i>

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