

### 2.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

### **Features**

- Guard Ring Die Construction for Transient Protection
- Ideally Suited for Automated Assembly
- Low Power Loss, High Efficiency
- Surge Overload Rating to 50A Peak
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Application
- High Temperature Soldering: 260°C/10 Second at Terminal
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

## **Mechanical Data**

- Case: SMA/SMB
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Lead Free Plating (Matte Tin Finish). Solderable per MIL-STD-202, Method 208@3
- Polarity: Cathode Band or Cathode Notch
- Weight: SMA 0.064 grams (Approximate) SMB 0.093 grams (Approximate)

### SMA/SMB





Top View

**Bottom View** 

# Ordering Information (Note 4)

Part Number	Qualification	Case	Packaging
B2xxA-13-F	Standard	SMA	5000/Tape & Reel
B2xx-13-F	Standard	SMB	3000/Tape & Reel
B250Q-13	Automotive	SMB	3000/Tape & Reel
B240AQ-13-F	Automotive	SMA	5000/Tape & Reel
B240Q-13-F	Automotive	SMB	3000/Tape & Reel

<sup>\*</sup> x = Device type, e.g. B260A-13-F (SMA package); B240-13-F (SMB package).

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

## **Marking Information**



B2X0A = Product type marking code, ex: B220A (SMA package) B2X0 = Product type marking code, ex: B230 (SMB package) );; = Manufacturers' code marking YWW = Date code marking Y = Last digit of year (ex: 2 for 2002) WW = Week code (01 to 53)



## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

Characteristic	Symbol	B220/A	B230/A	B240/A	B250/A	B260/A	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	20	30	40	50	60	>
RMS Reverse Voltage	V <sub>R(RMS)</sub>	14	21	28	35	42	V
Average Rectified Output Current @ T <sub>L</sub> = +100°C	Io			2.0			Α
Non-Repetitive Peak Forward Surge Current, 8.3ms Single Half Sine-Wave Superimposed on Rated Load		50				А	

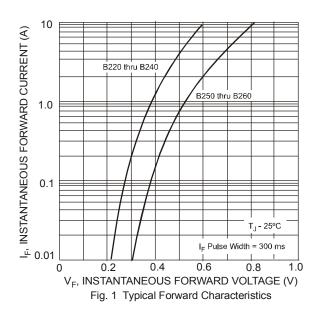
### **Thermal Characteristics**

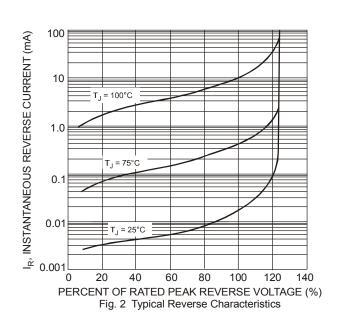
Characteristic		Symbol	Value	Unit
Typical Thermal Resistance, Junction to Lead	SMA SMB	$R_{ heta JL}$	25 20	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

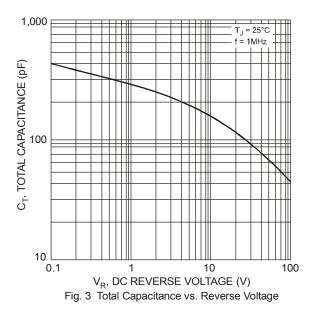
Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop	B220/A, B230/A, B240/A B250/A, B260/A	\/-	_		0.50 0.70	>	I <sub>F</sub> = 2.0A, T <sub>A</sub> = +25°C
Leakage Current (Note 5)		I <sub>R</sub>	_		0.5 20		@ Rated V <sub>R</sub> , T <sub>A</sub> = +25°C @ Rated V <sub>R</sub> , T <sub>A</sub> = +100°C
Total Capacitance		C <sub>T</sub>	_	_	200	pF	V <sub>R</sub> = 4V, f = 1MHz

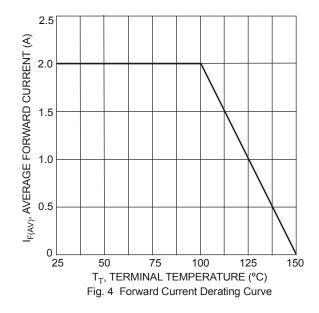
Note: 5. Short duration pulse test used to minimize self-heating effect.

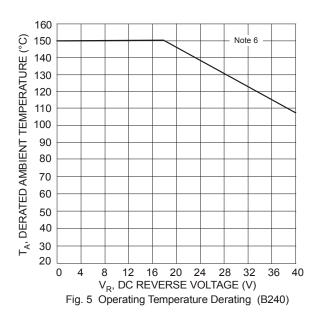


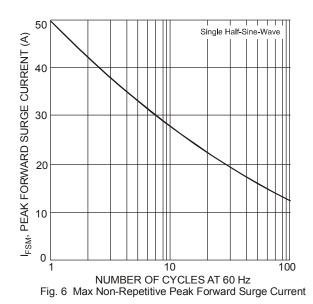










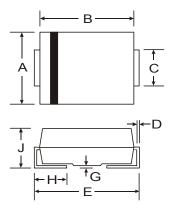


6. Device mounted on FR-4 PC board with minimum recommended pad layout pattern as per http://www.diodes.com.



# **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

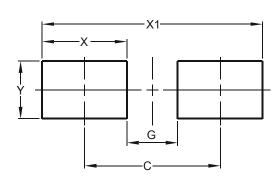


SMA			
Dim	Min	Max	
Α	2.29	2.92	
В	4.00	4.60	
С	1.27	1.63	
D	0.15	0.31	
Е	4.80	5.59	
G	0.05	0.20	
Н	0.76	1.52	
J	2.01	2.30	
All Dimensions in mm			

SMB				
Dim	Min	Max		
Α	3.30	3.94		
В	4.06	4.57		
С	1.96	2.21		
D	0.15	0.31		
Е	5.00	5.59		
G	0.05	0.20		
Н	0.76	1.52		
J	2.00	2.50		
All Dimensions in mm				

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



SMA		
Dimensions Value (in mr		
С	4.00	
G	1.50	
Х	2.50	
X1	6.50	
Υ	1.70	

SMB			
Dimensions	Value (in mm)		
С	4.30		
G	1.80		
X	2.50		
X1	6.80		
Y	2.30		



#### **IMPORTANT NOTICE**

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

#### LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
  - 1. are intended to implant into the body, or
  - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2013, Diodes Incorporated

www.diodes.com