

# SIL05E SERIES

*0.75 Vin to 3.63 Vin Single output*



5 A current rating

Output voltage range: 0.75 Vdc to 3.63 Vdc

Applications for 5 V or 3.3 V input POL converters

High power density (179W/in<sup>3</sup>)

High efficiency - typically 94% for 5 V in and 3.3 V out

SIL POL converter that saves board space

Industry standard footprint

Remote ON/OFF

Available RoHS compliant

THE SIL05E series are non-isolated SIL dc-dc converters packaged in an industry standard footprint giving designers a cost effective solution for conversion from either a 5 V or 3.3 V input to output voltages of 3.63 Vdc and 0.75 Vdc. The SIL05E offers a wide output range, which allows maximum design flexibility and a pathway for future upgrades. Local voltage conversion by the SIL05E series from existing 5 V or 3.3 V system voltages eliminates the need for redesign of existing power architectures when voltage requirements change. The SIL05E is designed for applications that include

distributed power, workstations, optical network and wireless applications.

Implemented using state of the art surface mount technology and automated manufacturing techniques, the SIL05E offers compact size and efficiencies of up to 94%.

**[ 2 YEAR WARRANTY ]**



Stresses in excess of the maximum ratings can cause permanent damage to the device. Operation of the device is not implied at these or any other conditions in excess of those given in the specification. Exposure to absolute maximum ratings can adversely affect device reliability.

#### Absolute Maximum Ratings

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Input voltage - continuous	$V_{in}$ (cont)	-0.3		5.5	V DC	$V_{in(+)} - V_{in(-)}$
Input voltage - peak/surge	$V_{surge}$	-0.3		6	V DC	2s max, non-repetitive
Operating temperature	$T_{op}$	-40		85	°C	Measured at thermal reference points, see Note 5 for thermal de-rating
Storage temperature	$T_{storage}$	-40		125	°C	
Output power (W3V3)	$P_{out}$ (max)	0		18.15	W	

All specifications are typical at nominal input  $V_{in} = 5V$ , full load under any resistive load combination at 25°C unless otherwise stated.

#### Input Characteristics

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Input voltage - operating	$V_{in}$ (oper)	3	5	5.5	V DC	See Note 6
Input current - no load	$I_{in}$		70	150	mA DC	$V_{in}$ (min) - $V_{in}$ (max), enabled
Input current - quiescent	$I_{in}$ (off)		2		mA DC	Converter disabled
Inrush current ( $I^2t$ )	$I_{inrush}$			0.04	A·s	Complies with ETS300 132 Part 4.7, with recommended LISN
Input ripple current			40		mA rms	
Input fuse*				6	A	Slowblow/antisurge HRC recommended

\*See Application Note 147 for manufacturer and part number

#### Turn On/Off

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Input voltage - turn on	$V_{in}$ (on)	2	2.70	3	V DC	Will regulate @ $V_{in} > 3V$ if $V_{out} \leq 2.5V$
Turn on delay - enabled, then power applied	$T_{delay}$ (power)		20		usec	With the enable signal asserted, this is the time from when the input voltage reaches the minimum specified operating voltage until the output voltage is within the total regulation band
Turn on delay - power applied, then enabled	$T_{delay}$ (enable)		20		usec	$V_{in} = V_{in}$ (nom), then enabled. This is the time taken until the output voltage is within the total error band
Rise time	$T_{rise}$		15		usec	From 10% to 90%; full resistive load, no external capacitance

## Signal Electrical Interface

Characteristic - Signal Name	Symbol	Min	Typ	Max	Units	Notes and Conditions
At remote/control ON/OFF pin						See Notes 2 and 3
High level input current	$I_{ih}$			500	µA	Current flowing into control pin when pin is pulled high (max at $V_{ih} = 5.5V$ )
Acceptable high level leakage current	$I_{ih}$ (leakage)			-10	µA	Acceptable leakage current from signal pin into the open collector driver (neg = from converter)
Low level input voltage	$V_{il}$	0		0.4	V	Converter guaranteed ON when control pin is less than $V_{il}$ (max)
High level input voltage	$V_{ih}$	2.5			V	Converter guaranteed OFF when control pin is greater than $V_{ih}$ (max)

## Reliability and Service Life

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Mean time between failure	MTBF	551,000			Hours	MIL-HDBK-217F, $V_{in} = V_{in}$ (nom); $I_{out} = I_{out}$ (max); ambient 25°C; ground benign environment
Mean time between failure	MTBF	9,009,000			Hours	Telcordia SR-332
Mean time between failure	MTBF	TBA			Hours	Demonstrated. This entry will be periodically updated as the number of test hours increase

Other Specifications						
Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Switching frequency	$f_{sw}$		300		kHz	Fixed frequency
Weight			2.5		g	

EMC Electromagnetic Compatibility					
Phenomenon	Port	Standard	Test level	Criteria	Notes and conditions
<b>Immunity:</b>					
Conducted immunity		EN61000-4-6			
Radiated immunity		EN61000-4-3			
ESD	Enclosure	EN61000-4-2	6kV contact 8kV air	NP	As per ETS 300 386-1 table 5

Safety Agency Approvals	
Standard	Category
IEC60950	EN60950
UL/cUL CAN/CSA 22.2 No. 60950-00 : UL60950	File No. E174104
TÜV Product Service	Certificate No. B 03 10 38572 037
CB Certificate No	DE3-51686M1

Material Ratings	
Characteristic - Signal Name	Notes and Conditions
Flammability rating	UL9V-0

Model Numbers					
Model Number	Input Voltage	Output Voltage	Output Current (Max.)	Typical Efficiency	Max. Load Regulation
SIL05E-05W3V3-VJ	3.0- 5.5 Vdc	0.75-3.63 Vdc	5 A	94.0%	±1.0%

Note: Efficiency at 5Vin, 3V3 Vout

RoHS Compliance Ordering Information	
	<p>The 'J' at the end of the part number indicates that the part is Pb-free (RoHS 6/6 compliant). TSE RoHS 5/6 (non Pb-free) compliant versions may be available on special request, please contact your local sales representative for details.</p>

## W3V3 Model

Input Characteristics						
Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Input current - operating	$I_{in}$		3.5	3.9	A DC	$V_{in} = V_{in}$ (nom); $I_{out} = I_{out}$ (max.); $V_O = V_O$ (nom)
Reflected ripple current	$I_{in}$ (ripple)		40		mA rms	$I_{out} = I_{out}$ (max.), measured without external filter
Input capacitance - internal filter	$C_{input}$		9.4		$\mu F$	Internal to converter
Input capacitance - external bypass	$C_{bypass}$	100			$\mu F$	Recommended customer added capacitance

## W3V3 Model

Electrical Characteristics – O/P						
Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Nominal set-point voltage	$V_O$ (nom)	3.24	3.30	3.36	V DC	$V_{in} = V_{in}$ (nom); $I_{out} = I_{out}$ (max)
Total regulation band	$V_O$	3.19		3.41	V DC	For all line, static load and temperature until end of life
Line regulation				1	%	$I_{out} = I_{out}$ (nom); $V_{in}$ (min) to $V_{in}$ (max)
Load regulation				1	%	$V_{in} = V_{in}$ (nom); $I_{out}$ (min) to $I_{out}$ (max)
Output current continuous	$I_{out}$	0		5	A DC	
Output current - short circuit	$I_{sc}$		10	20	A rms	Continuous, unit auto recovers from short, $V_O < 100mV$
Output voltage - noise	$V_{p-p}$ $V_{rms}$			75 25	mV pk-pk mV rms	Measurement bandwidth: 20 MHz. See Application Note 142 for measurement set-up details

## W3V3 Model

## Electrical Characteristics - O/P

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Load transient response - peak deviation	$V_{dynamic}$		60		mV	Peak deviation for 50% to 75% step load, $di/dt = 100 \text{ mA}/\mu\text{sec}$ .
Load transient response - recovery	$T_{recovery}$		50		$\mu\text{sec}$	Settling time to within 1% of output set point voltage for 50% to 75% step load.
External load capacitance	$C_{ext}$	0		10,000	$\mu\text{F}$	

## W3V3 Model

## Protection and Control Features

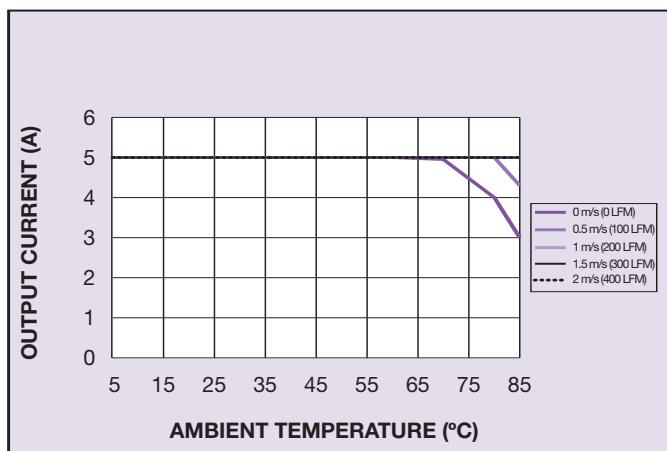
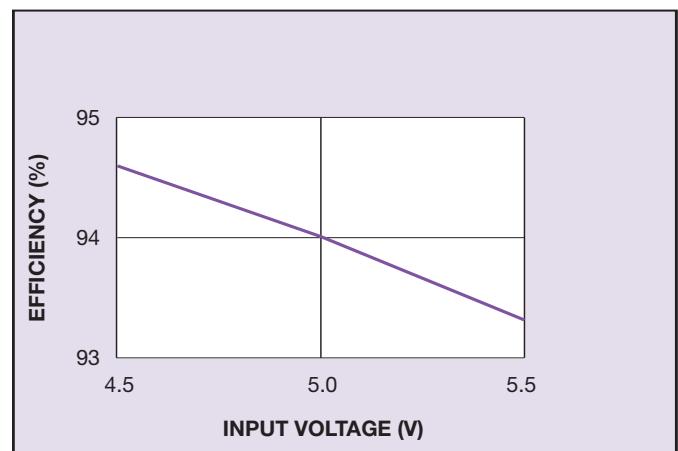
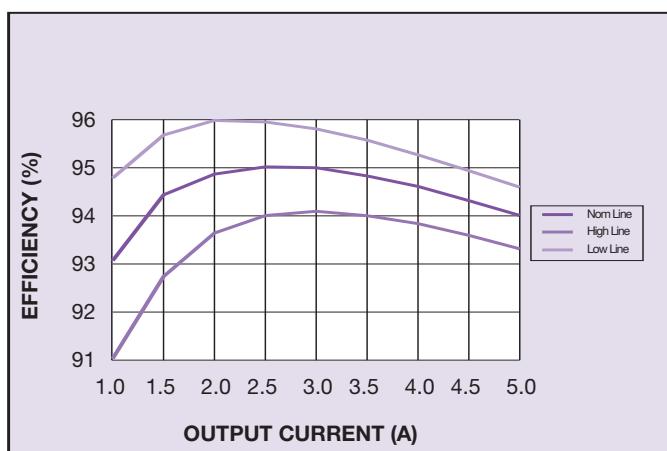
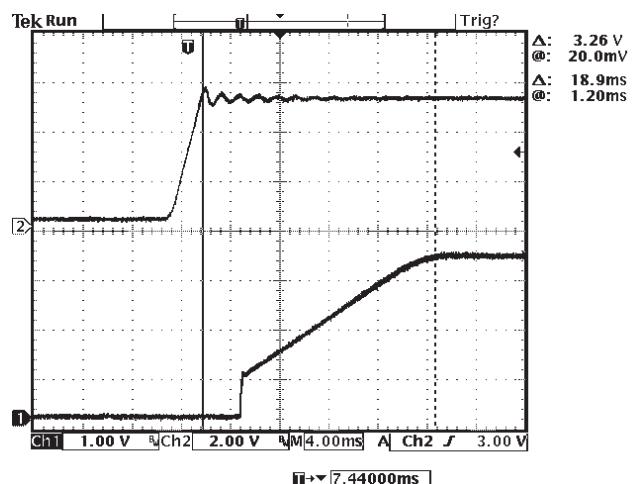
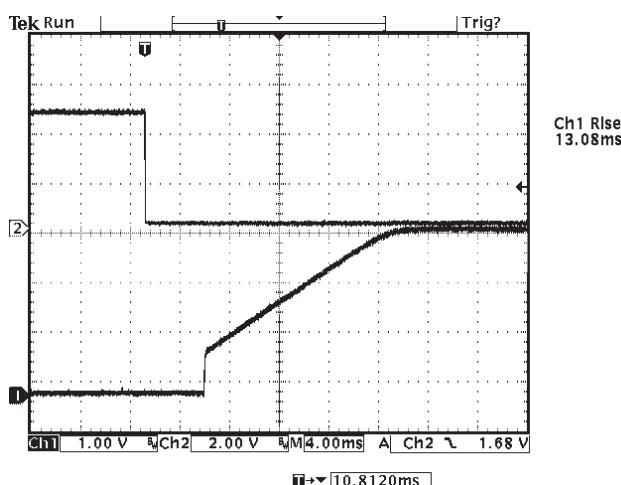
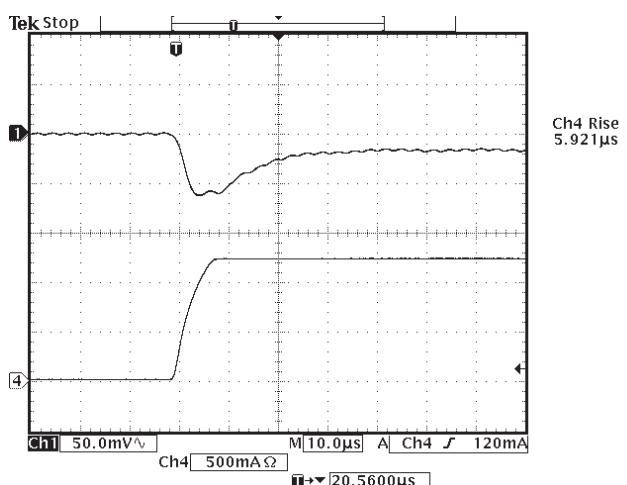
Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Allowable output voltage				384	%	Trim up (% of $V_O$ nom) = 0.75V See Application Note 142 for details of trim equations and trim curves

## W3V3 Model

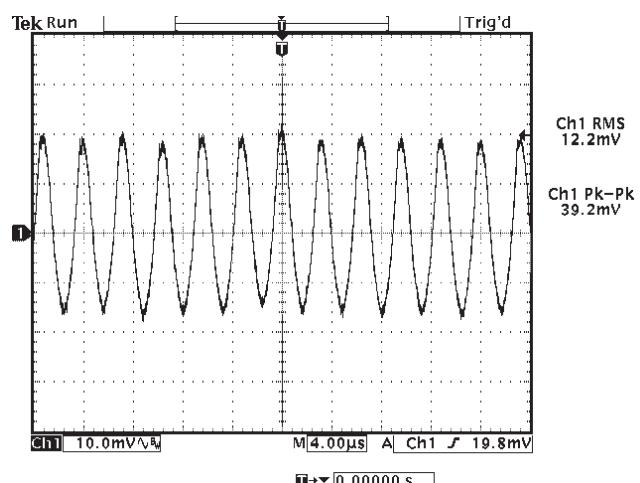
## Efficiency

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Efficiency	$\eta$	92	94		%	$I_{out} = 100\% I_{out}$ (max), $V_{in} = 5V$
Efficiency	$\eta$	93	95		%	$I_{out} = 50\% I_{out}$ (max), $V_{in} = 5V$

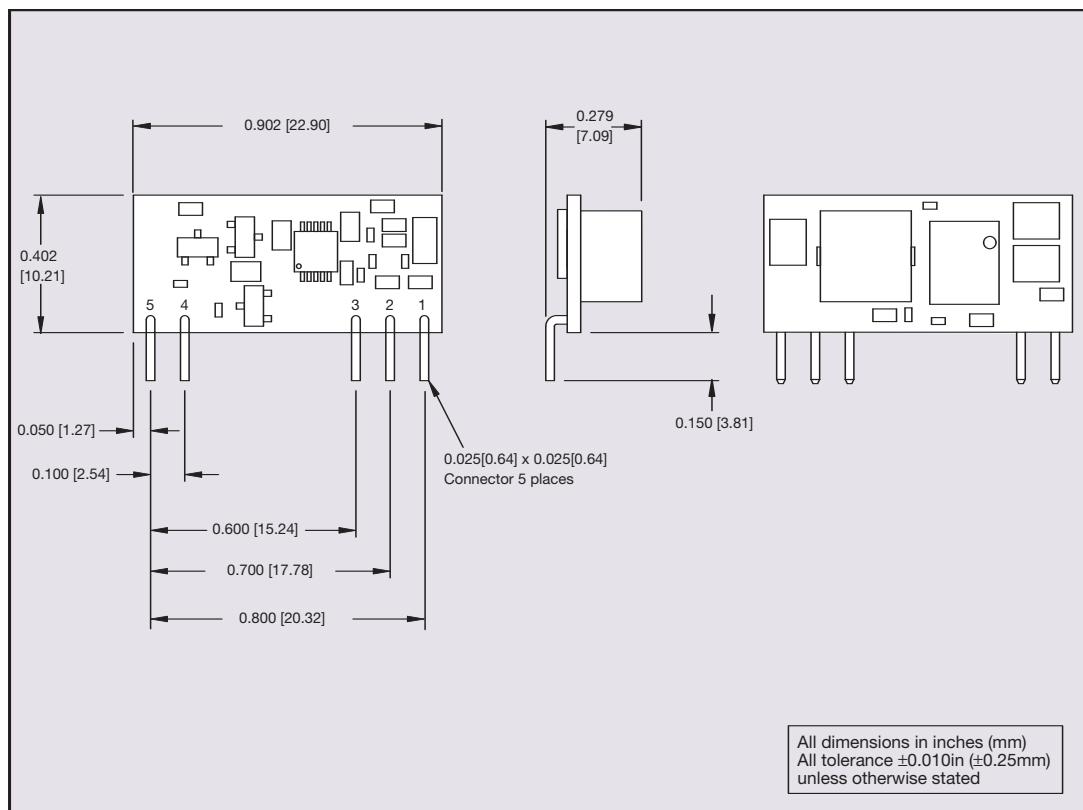
## W3V3 Model

Figure 1: De-rating Curve ( $V_o = 3.3V$ )Figure 2: Efficiency vs Line ( $V_o = 3.3V$ )Figure 3: Efficiency vs Load ( $V_o = 3.3V$ )Figure 4: Typical Power-up Characteristic  
(Channel 1:  $V_o$ , Channel 2:  $V_{in}$ )Figure 5: Control On/Off Characteristic  
(Channel 2 Remote ON/OFF: , Channel 1:  $V_o$ )Figure 6: Typical Transient Response 50% - 75% Step Load Change (Channel 1:  $V_o$ , Channel 4:  $I_o$ )

## W3V3 Model



**Figure 7: Typical Ripple and Noise  
(Channel 1: Vo)**



Pin Connections	
Pin No.	Function
1	Vout
2	Trim
3	GND
4	Vin
5	Remote ON/OFF

Figure 8: Mechanical Drawing and Pinout Table

**Note 1**

Thermal reference is defined as the highest temperature measured at any one of the specified thermal reference points. See Figure 9: Thermal Reference Points.

**Note 2**

The Remote ON/OFF pin is referenced to ground.

**Note 3**

The SIL05E features a 'Negative Logic' Remote ON/OFF operation. If not using the Remote ON/OFF pin, leave the pin open (the converter will be on). The Remote ON/OFF pin is referenced to ground.

The following conditions apply for the SIL05E:

Configuration	Converter Operation
Remote pin open circuit	Unit is ON
Remote pin pulled low	Unit is ON
Remote pin pulled high [Von/off >2.5V]	Unit is OFF

A 'Positive Logic' Remote ON/OFF version is also possible with this converter. To order please place the suffix '-R' at the end of the model number, e.g. SIL05E-05W3V3-VRJ.

**Note 4**

Thermal reference set up: Unit mounted on an edge card test board 203mm x 190mm. Test board mounted vertically. For test details and recommended set-up see Application Note 142.

**Note 5**

Max 69°C for full load in still air.

**Note 6**

For SIL05E-05W3V3 minimum operating voltage is 4.5V, for  $V_o = 3V3$

**CAUTION:** Hazardous internal voltages and high temperatures. Ensure that unit is accessible only to trained personnel. The user must provide the recommended fusing in order to comply with safety approvals.

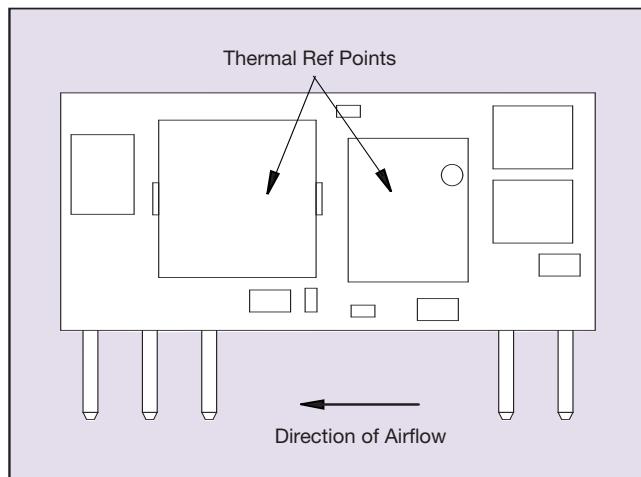


Figure 9: Thermal Reference Points

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