

NTE1535 Integrated Circuit TV Sync. Horizontal/Vertical OSC

Features:

- Single Chip for Vertical and Horizontal Deflection System
- Synchro Pulse Processing
- Vertical Deflection: Oscillator to Driver, Video Blanking
- Horizontal Deflection: AFC Comparator to Buffer, Video Blanking

Absolute Maximum Ratings: ($T_A = +25^\circ\text{C}$ unless otherwise specified)

Maximum Supply Voltage, V_{12} , V_{24} 14V
 Allowable Power Dissipation ($T_A \leq +65^\circ\text{C}$), P_D 750mW
 Operating Temperature Range, T_{opr} -20° to $+75^\circ\text{C}$
 Storage Temperature Range, T_{stg} -55° to $+125^\circ\text{C}$

Electrical Characteristics: ($T_A = +25^\circ\text{C}$, $V_{12} = V_{24} = 12\text{V}$ unless otherwise specified)

Parameter	Test Conditions	Min	Typ	Max	Unit
Gain of Synchro Pulse Amp		—	6.8	—	
Burst Gate Pulse Peak		—	6.7	—	V
Delay Time		—	0.3	—	μs
Back Side Delay Time		—	3.7	—	μs
Vertical Frequency Holding Range		—	10.5	—	Hz
Pulling In Range		—	10.5	—	Hz
For V_{CC} Varying	$V_{24} = 12\text{V} \pm 1\text{V}$			± 1	Hz
Oscillator		—	60	—	Hz
Vertical Retracing Period		—	700	—	μs
AFC Pulling In Range		—	750	—	Hz
AFC Center Error		—	—	± 100	Hz
Horizontal Frequency Oscillator		—	15.734	—	Hz
Oscillator Duty Cycle		—	50	—	%
For V_{CC} Varying	$V_{12} = 12\text{V} \pm 1\text{V}$	—	—	± 50	Hz

Pin Connection Diagram

Vert Trigger Input	1	24	V _{CC}
Bypass	2	23	Video Input
Vertical Output	3	22	Sync Output
Center Voltage	4	21	Sync Separator Input
Vert Driver Input	5	20	Time Constant Circuit
Vert Driver Output	6	19	Sync Separator
GND	7	18	Output
Blanking Pulse Output	8	17	Burst Gate Pulse
GND	9	16	Output
Hold Down Input	10	15	Flyback Pulse Input
Horiz Buffer Output	11	14	Bypass
Bypass	12	13	AFC Output
V _{CC}			Bypass
			Horiz Sync Input

