

HZM6.2Z4MWA

Silicon Planar Zener Diode for Surge Absorb

REJ03G0368-0100

Rev.1.00

Oct 01, 2004

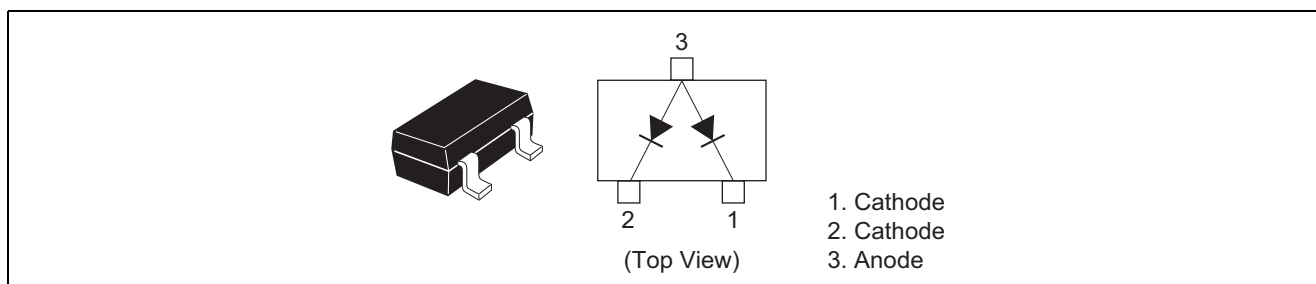
Features

- HZM6.2Z4MWA has two devices in a monolithic, and can absorb surge.
- Low capacitance ($C = 4.0 \text{ pF Typ} / 4.5 \text{ pF Max}$) and can protect ESD of signal line.
- MPAK Package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HZM6.2Z4MWA	N1	MPAK

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Power dissipation	Pd *	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: Two device total, See Fig.2.

Electrical Characteristics *¹

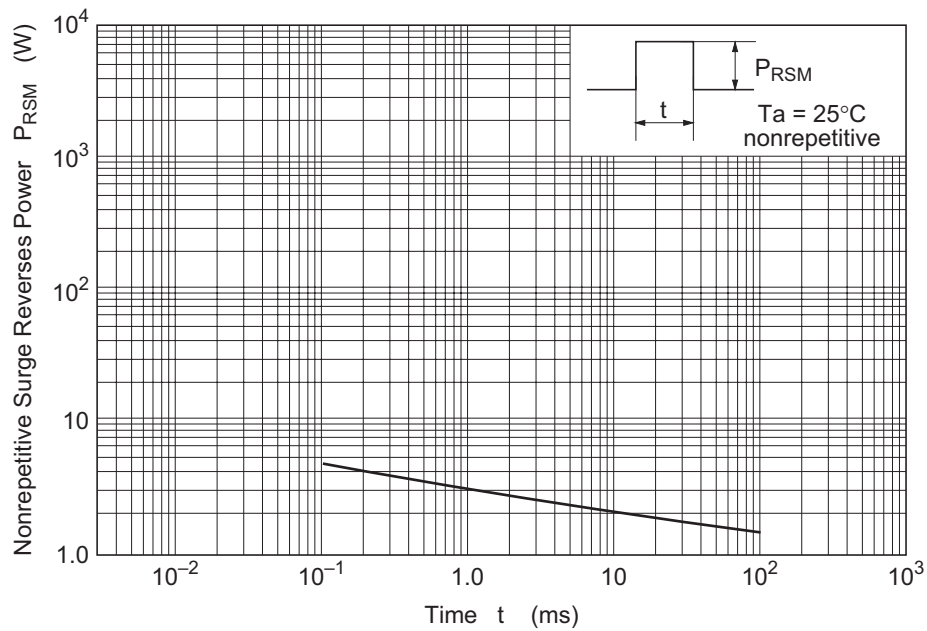
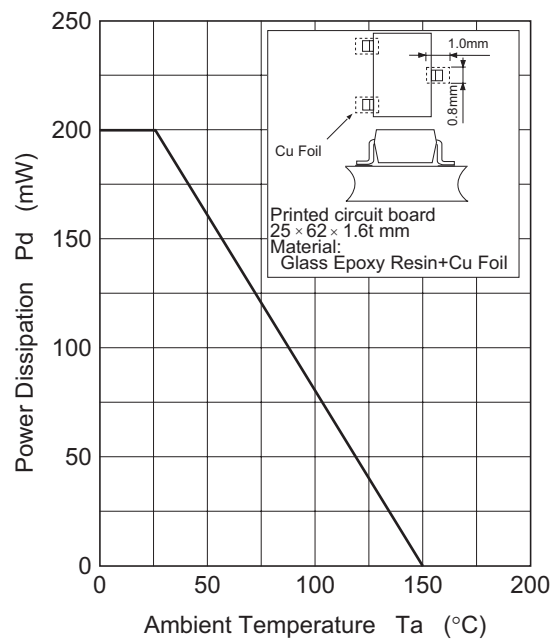
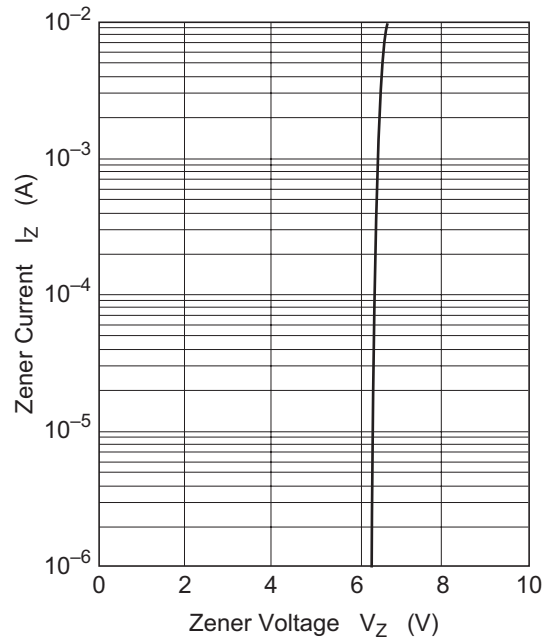
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	V _Z	5.90	—	6.50	V	I _Z = 5 mA, 40 ms pulse
Reverse current	I _R	—	—	3	μA	V _R = 5.5 V
Capacitance	C	—	4.0	4.5	pF	V _R = 0 V, f = 1 MHz
Dynamic resistance	r _d	—	—	60	Ω	I _Z = 5 mA
ESD-Capability * ²	—	8	—	—	kV	C = 150 pF, R = 330 Ω, Both forward and reverse direction 10 pulse

Notes: 1. Per one device.

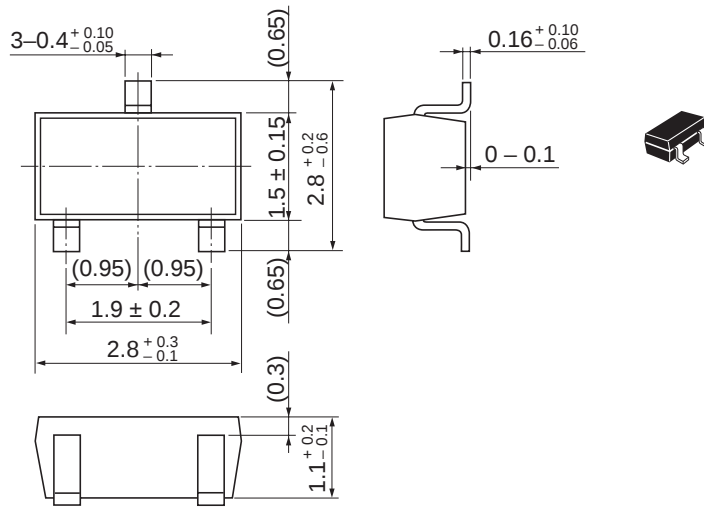
2. Failure criterion ; I_R > 3 μA at V_R = 5.5 V.

Main Characteristics



Package Dimensions

As of January, 2003
Unit: mm



Package Code	MPAK
JEDEC	—
JEITA	Conforms
Mass (reference value)	0.011 g

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