BLF7G27L-100; BLF7G27LS-100 Power LDMOS transistor

AMPLEON

Rev. 5 — 1 December 2016

Product data sheet

Product profile

1.1 General description

100 W LDMOS power transistor for base station applications at frequencies from 2500 MHz to 2700 MHz.

Typical performance Table 1.

Typical RF performance at $T_{case} = 25 \, ^{\circ}$ C in a common source class-AB production test circuit.

Mode of operation	f	I_{Dq}	V _{DS}	P _{L(AV)}	Gp	η_D	ACPR _{885k}	ACPR _{5M}
	(MHz)	(mA)	(V)	(W)	(dB)	(%)	(dBc)	(dBc)
IS-95	2500 to 2700	900	28	20	18	28	-45 <mark>[1]</mark>	-
Single carrier W-CDMA	2500 to 2700	900	28	25	17.5	30	-	-41 ^[2]

^[1] Single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF. Channel bandwidth is 1.2288 MHz.

1.2 Features and benefits

- Excellent ruggedness
- High efficiency
- Low R_{th} providing excellent thermal stability
- Designed for low memory effects providing excellent pre-distortability
- Internally matched for ease of use
- Integrated ESD protection
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

1.3 Applications

RF power amplifiers for base stations and multi carrier applications in the 2500 MHz to 2700 MHz frequency range.

^{[2] 3}GPP; test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF. Channel bandwidth is 3.84 MHz.

2. Pinning information

Table 2. Pinning

Pin	Description		Simplified outline	Graphic symbol
BLF7G2	7L-100 (SOT502A)			
1	drain			,
2	gate			1
3	source	[1]	2	2 — 3 sym112
BLF7G2	7LS-100 (SOT502B)		+	
1	drain			
2	gate		1 1 3	1
3	source	<u>[1]</u>	2	2 — 3 sym112

^[1] Connected to flange.

3. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
BLF7G27L-100	-	flanged LDMOST ceramic package; 2 mounting holes; 2 leads	SOT502A			
BLF7G27LS-100	-	earless flanged LDMOST ceramic package; 2 leads	SOT502B			

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage		-	65	V
V_{GS}	gate-source voltage		-0.5	+13	V
I _D	drain current		-	28	Α
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	200	°C

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-c)}	thermal resistance from junction to case	$T_{case} = 80 ^{\circ}C; P_{L} = 100 W$	0.25	K/W

BLF7G27L-100_7G27LS-100

6. Characteristics

Table 6. Characteristics

 T_i = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 1 \text{ mA}$	65	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	V _{DS} = 10 V; I _D = 153 mA	1.5	1.8	2.3	V
I _{DSS}	drain leakage current	V _{GS} = 0 V; V _{DS} = 28 V	-	-	5	μΑ
I _{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$	25.1	29	-	А
I _{GSS}	gate leakage current	V _{GS} = 11 V; V _{DS} = 0 V	-	-	500	nA
g _{fs}	forward transconductance	V _{DS} = 10 V; I _D = 153 mA	-	1.34	-	S
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $I_D = 5.35 \text{ A}$	-	0.1	-	Ω

7. Test information

Remark: All testing performed in a class-AB production test circuit.

Table 7. Functional test information

Mode of operation: 1-carrier N-CDMA, single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF, channel bandwidth is 1.2288 MHz; f_1 = 2500 MHz; f_2 = 2700 MHz; RF performance at V_{DS} = 28 V; I_{Dq} = 900 mA; T_{case} = 25 °C; unless otherwise specified.

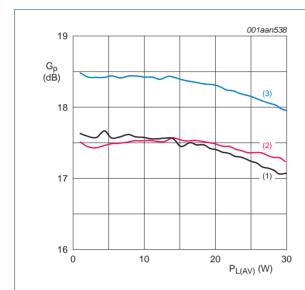
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$P_{L(AV)}$	average output power		-	20	-	W
Gp	power gain		16.3	18	-	dB
RLin	input return loss		-	-10	-	dB
η_{D}	drain efficiency		24	28	-	%
ACPR _{885k}	adjacent channel power ratio (885 kHz)		-	-4 5	-40	dBc

7.1 Ruggedness in class-AB operation

The BLF7G27L-100 and BLF7G27LS-100 are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: $V_{DS} = 28 \text{ V}$; $I_{Dq} = 900 \text{ mA}$; $P_L = 100 \text{ W}$ (CW); f = 2500 MHz.

7.2 Single carrier IS-95

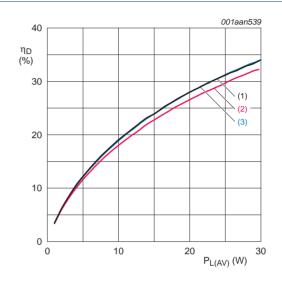
Single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF. Channel bandwidth is 1.2288 MHz.



 V_{DS} = 28 V; I_{Dq} = 900 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

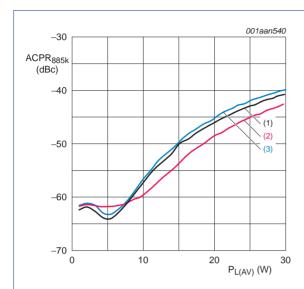
Fig 1. Single carrier IS-95 power gain as a function of average output power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

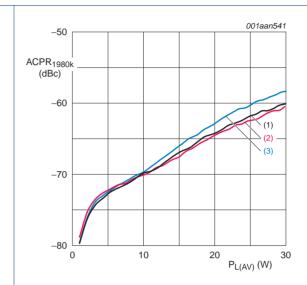
Fig 2. Single carrier IS-95 drain efficiency as a function of average output power; typical values



 V_{DS} = 28 V; I_{Dq} = 900 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

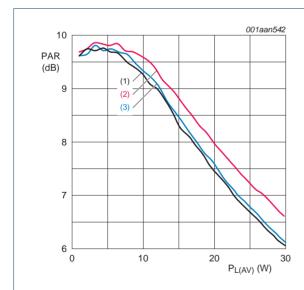
Fig 3. Single carrier IS-95 ACPR at 885 kHz as a function of average output power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

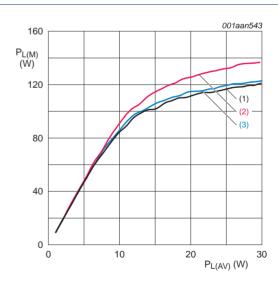
Fig 4. Single carrier IS-95 ACPR at 1980 kHz as a function of average output power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 5. Single carrier IS-95 peak-to-average power ratio as a function of average output power; typical values

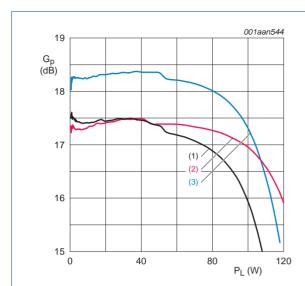


 V_{DS} = 28 V; I_{Dq} = 900 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 6. Single carrier IS-95 peak output power as a function of average output power; typical values

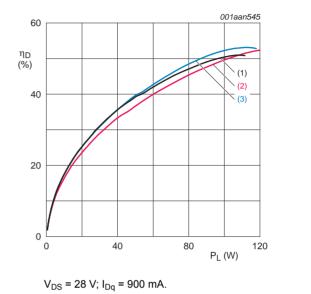
7.3 Pulsed CW



 V_{DS} = 28 V; I_{Dq} = 900 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 7. Pulsed CW power gain as a function of output power; typical values

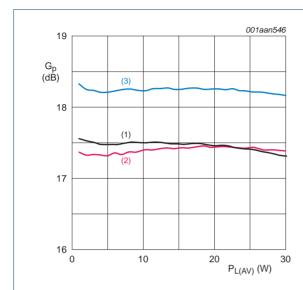


- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 8. Pulsed CW drain efficiency as a function of output power; typical values

7.4 Single carrier W-CDMA

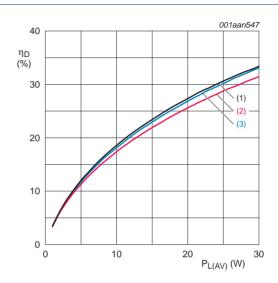
3GPP; test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF. Channel bandwidth is 3.84 MHz.



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

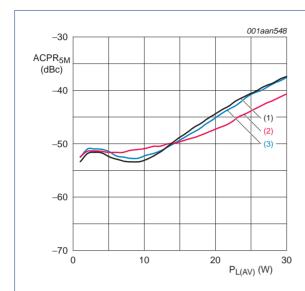
Fig 9. Single carrier W-CDMA power gain as a function of average output power; typical values



 $V_{DS} = 28 \text{ V}; I_{Da} = 900 \text{ mA}.$

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

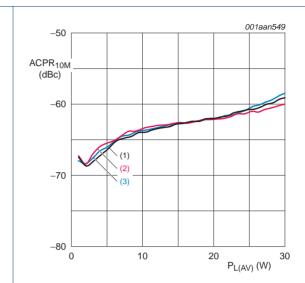
Fig 10. Single carrier W-CDMA drain efficiency as a function of average output power; typical values



 V_{DS} = 28 V; I_{Dq} = 900 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

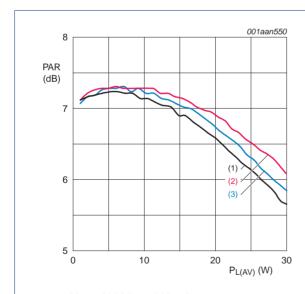
Fig 11. Single carrier W-CDMA ACPR at 5 MHz as a function of average output power; typical values



 V_{DS} = 28 V; I_{Dq} = 900 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

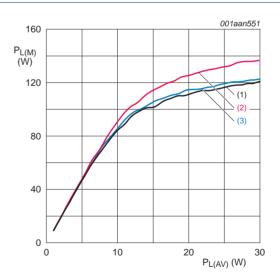
Fig 12. Single carrier W-CDMA ACPR at 10 MHz as a function of average output power; typical values



 V_{DS} = 28 V; I_{Dq} = 900 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 13. Single carrier W-CDMA peak-to-average power ratio as a function of average output power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 14. Single carrier W-CDMA peak output power as a function of average output power; typical values

Package outline

Flanged ceramic package; 2 mounting holes; 2 leads

SOT502A

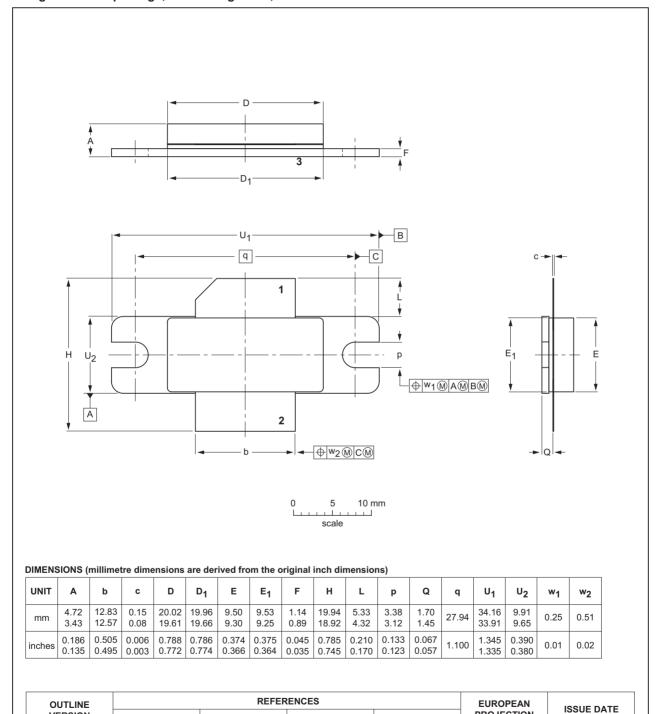


Fig 15. Package outline SOT502A

IEC

JEDEC

VERSION

SOT502A

JEITA

-03-01-10

12-05-02

PROJECTION

Earless flanged ceramic package; 2 leads

SOT502B

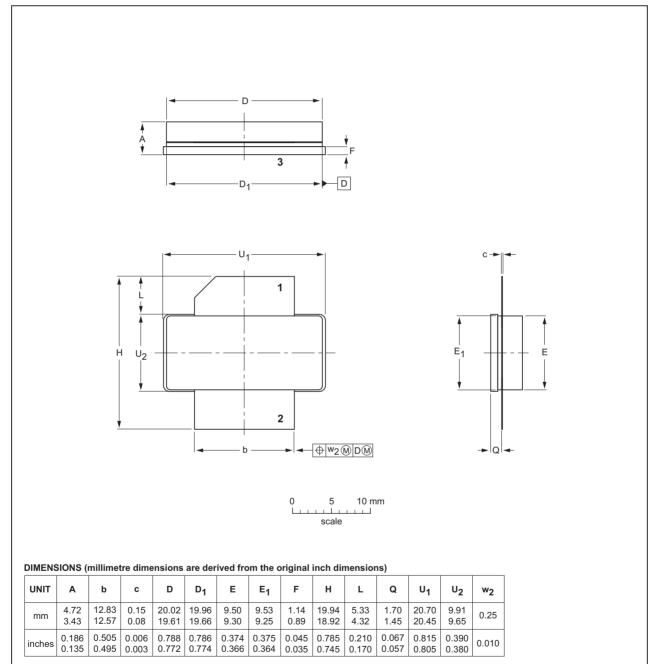


Fig 16. Package outline SOT502B

IEC

OUTLINE

VERSION

SOT502B

JEITA

REFERENCES

JEDEC

ISSUE DATE

07-05-09

12-05-02

EUROPEAN

PROJECTION

9. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

Table 8. ESD sensitivity

ESD model	Class
Charged Device Model (CDM); According to ANSI/ESDA/JEDEC standard JS-002	C2A [1]
Human Body Model (HBM); According to ANSI/ESDA/JEDEC standard JS-001	3A [2]

- [1] CDM classification C2A is granted to any part that passes after exposure to an ESD pulse of 500 V, but fails after exposure to an ESD pulse of 750 V.
- [2] HBM classification 3A is granted to any part that passes after exposure to an ESD pulse of 4000 V, but fails after exposure to an ESD pulse of 8000 V.

10. Abbreviations

Table 9. Abbreviations

Acronym	Description
3GPP	Third Generation Partnership Project
CCDF	Complementary Cumulative Distribution Function
CW	Continuous Wave
DPCH	Dedicated Physical CHannel
IS-95	Interim Standard 95
ESD	ElectroStatic Discharge
LDMOS	Laterally Diffused Metal Oxide Semiconductor
LDMOST	Laterally Diffused Metal Oxide Semiconductor Transistor
N-CDMA	Narrowband Code Division Multiple Access
PAR	Peak-to-Average power Ratio
RF	Radio Frequency
VSWR	Voltage Standing Wave Ratio

11. Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLF7G27L-100_7G27LS-100 v.5	20161201	Product data sheet	-	BLF7G27L-100_7G27LS-100 v.4
Modifications:	Section 9	on page 11: updated Ha	ndling information	1
BLF7G27L-100_7G27LS-100 v.4	20150901	Product data sheet	-	BLF7G27L-100_7G27LS-100 v.3
BLF7G27L-100_7G27LS-100 v.3	20110722	Product data sheet	-	BLF7G27L-100_7G27LS-100 v.2
BLF7G27L-100_7G27LS-100 v.2	20110405	Preliminary data sheet	-	BLF7G27L-100_7G27LS-100 v.1
BLF7G27L-100_7G27LS-100 v.1	20100421	Objective data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.ampleon.com.

12.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Ampleon does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Ampleon sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Ampleon and its customer, unless Ampleon and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Ampleon product is deemed to offer functions and qualities beyond those described in the Product data sheet.

12.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Ampleon does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Ampleon takes no responsibility for the content in this document if provided by an information source outside of Ampleon.

In no event shall Ampleon be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Ampleon's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of Ampleon.

Right to make changes — Ampleon reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Ampleon products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an

Ampleon product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Ampleon and its suppliers accept no liability for inclusion and/or use of Ampleon products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Ampleon makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Ampleon products, and Ampleon accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Ampleon product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Ampleon does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Ampleon products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Ampleon does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Ampleon products are sold subject to the general terms and conditions of commercial sale, as published at http://www.ampleon.com/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Ampleon hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Ampleon products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

BLF7G27L-100 7G27LS-100

BLF7G27L-100; BLF7G27LS-100

Power LDMOS transistor

Non-automotive qualified products — Unless this data sheet expressly states that this specific Ampleon product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Ampleon accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Ampleon's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Ampleon's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Ampleon for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Ampleon's standard warranty and Ampleon's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

12.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Any reference or use of any 'NXP' trademark in this document or in or on the surface of Ampleon products does not result in any claim, liability or entitlement vis-à-vis the owner of this trademark. Ampleon is no longer part of the NXP group of companies and any reference to or use of the 'NXP' trademarks will be replaced by reference to or use of Ampleon's own trademarks.

13. Contact information

For more information, please visit: http://www.ampleon.com

For sales office addresses, please visit: http://www.ampleon.com/sales

BLF7G27L-100; BLF7G27LS-100

AMPLEON

Power LDMOS transistor

14. Contents

1	Product profile
1.1	General description
1.2	Features and benefits
1.3	Applications 1
2	Pinning information
3	Ordering information
4	Limiting values
5	Thermal characteristics
6	Characteristics 3
7	Test information 3
7.1	Ruggedness in class-AB operation 3
7.2	Single carrier IS-95
7.3	Pulsed CW
7.4	Single carrier W-CDMA
8	Package outline
9	Handling information 11
10	Abbreviations
11	Revision history
12	Legal information
12.1	Data sheet status
12.2	Definitions
12.3	Disclaimers
12.4	Trademarks14
13	Contact information 14
14	Contents

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

NXP:

BLF7G27L-100,118 BLF7G27LS-100,112 BLF7G27LS-100,118