



1N5059, 1N5060 1N5061, 1N5062

December 1993

1A, 200V - 800V Diodes

Features

- High-Temperature Metallurgically Bonded, No Compression Contacts as Found in Diode-Constructed Rectifiers
- Glass-Passivated Junction
- 1A Operation at $T_A = 100^\circ\text{C}$ with No Thermal Runaway
- Low Reverse Current
- Exceeds Environmental Standard of MIL-STD-19500
- Hermetically Sealed Package
- High-Temperature Soldering Guaranteed: $300^\circ\text{C}/10\text{s}/0.375\text{ in. (9.5 mm)}$ Lead Length

Description

The 1N5059, 1N5060, 1N5061, and 1N5062 are glass-passivated "transient voltage protected," silicon rectifiers intended for general-purpose applications.

These rectifiers will dissipate up to 800 watts in reverse direction without damage. Voltage transients generated by household or industrial power lines are dissipated.

These rectifiers are supplied in a JEDEC style DO-204 pack-

Package

JEDEC STYLE DO-204
TOP VIEW



Symbol



Absolute Maximum Ratings

Supply Frequency of 60Hz, Resistive or Inductive Loads

	1N5059	1N5060	1N5061	1N5062	UNITS
Maximum Peak (Repetitive) Reverse Voltage (Note 1) V_{RRM}	200	400	600	800	V
Maximum RMS Input (Supply) Voltage For Resistive or Inductive Loads. V_{RMS}	140	280	420	560	V
Maximum DC Reverse (Blocking) Voltage (Note 1) $V_{R(DC)}$	200	400	600	800	V
Maximum Average Forward Current (Note 1) For Resistive or Inductive Loads, $T_A = +75^\circ\text{C}$ I_O	1	1	1	1	A
Maximum Peak Surge (Non Repetitive) Forward Current (Note 1) For 8.3ms Half Sine Wave, Superimposed on Rated Load I_{FSM}	50	50	50	50	A
Operating Junction and Storage Temperature Range T_J, T_{STG}	-65 to +175	-65 to +175	-65 to +175	-65 to +175	$^\circ\text{C}$

NOTE:

1. In accordance with JEDEC registration format.

Specifications 1N5059, 1N5060, 1N5061, 1N5062**Electrical Specifications** $T_A = +25^\circ\text{C}$, Unless Otherwise Specified

PARAMETERS	SYMBOL	LIMITS FOR ALL TYPES			UNITS
		MIN	TYP	MAX	
Maximum Instantaneous Forward-Voltage Drop (Note 1) At 1A, $T_A = +75^\circ\text{C}$	V_F	-	-	1.2	V
Maximum Full-Load Reverse Current At Average Full-Cycle, Lead Length = 0.375 in. (9.5mm) $T_A = +25^\circ\text{C}$	I_R	-	-	5	μA
At Average Full-Cycle, Lead Length = 0.375 in. (9.5mm) $T_A = +175^\circ\text{C}$	I_R	-	-	150 (Note 2)	μA
Maximum Reverse Current: (Note 1) At Average DC Reverse (Blocking) Voltage, $T_A = +25^\circ\text{C}$	I_R	-	-	5	μA
At Maximum DC Reverse (Blocking) Voltage, $T_A = +175^\circ\text{C}$	I_R	-	-	300 (Note 3)	μA
Maximum Reverse Recovery Time At $I_F = 0.5\text{A}$, $I_R = 1\text{A}$, $I_{RR} = 0.25\text{A}$	I_R	-	-	2	μs
Typical Junction Capacitance At Frequency = 1MHz and Applied Reverse Voltage = 4V	C_J	-	15	-	pF

NOTES:

1. In accordance with JEDEC registration format.
2. $100\mu\text{A}$ for 1N5061 and 1N5062.
3. $200\mu\text{A}$ for 1N5061 and 1N5062.

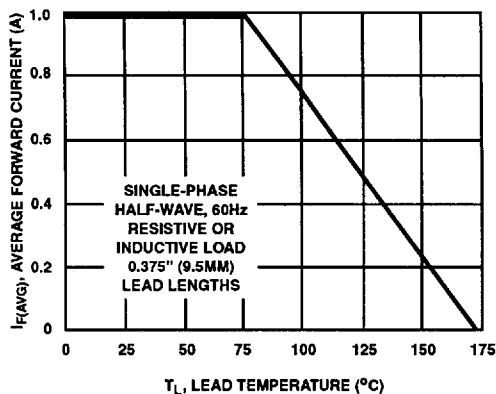
Typical Performance Curves

FIGURE 1. MAXIMUM AVERAGE FORWARD OUTPUT CURRENT CHARACTERISTIC

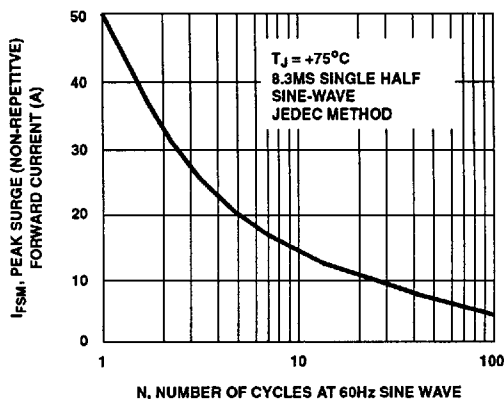


FIGURE 2. MAXIMUM PEAK SURGE (NON-REPETITIVE) FORWARD CURRENT CHARACTERISTIC

1N5059, 1N5060, 1N5061, 1N5062

Typical Performance Curves (Continued)

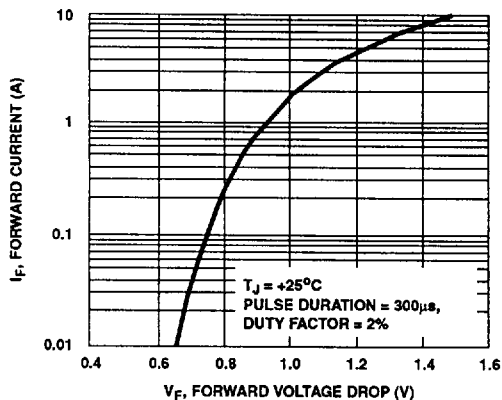


FIGURE 3. TYPICAL INSTANTANEOUS FORWARD CURRENT CHARACTERISTIC

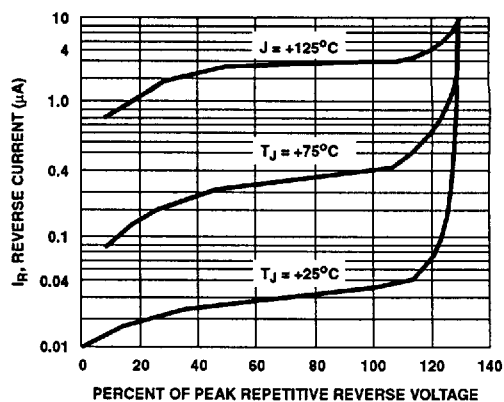


FIGURE 4. TYPICAL REVERSE LEAKAGE CURRENT CHARACTERISTICS

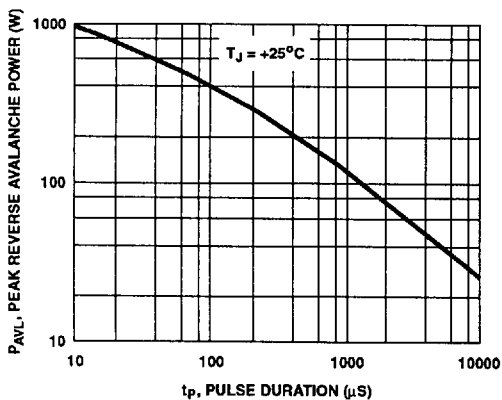


FIGURE 5. MAXIMUM NON-REPETITIVE REVERSE AVALANCHE POWER

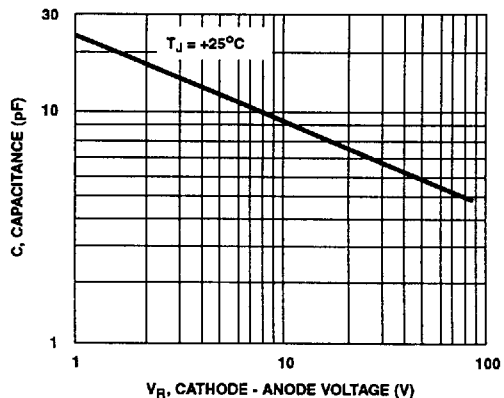


FIGURE 6. TYPICAL JUNCTION CAPACITANCE CHARACTERISTIC